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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR 2SK3305B

SWITCHING N-CHANNEL POWER MOSFET

DESCRIPTION

The 2SK3305B is N-channel MOSFET device that features a low gate charge and excellent switching characteristics, and designed for high voltage applications such as switching power supply, AC adapter.

FEATURES

• Low gate charge

 $Q_G = 13 \text{ nC TYP}$. $(V_{DD} = 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5.0 \text{ A})$

- Gate voltage rating: ±30 V
- Low on-state resistance

 $R_{DS(on)}$ = 1.5 Ω MAX. (Vgs = 10 V, ID = 2.5 A)

Avalanche capability ratings

ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE
2SK3305B-S19-AY Note	Pure Sn (Tin)	Tube 50 p/tube	TO-220AB (MP-25) typ. 1.9 g

Note Pb-free (This product does not contain Pb in external electrode.)

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

VDSS	500	V
Vgss	±30	V
I _{D(DC)}	±5	Α
ID(pulse)	±20	Α
P _{T1}	75	W
P _{T2}	1.5	W
Tch	150	°С
Tstg	-55 to +150	°C
las	5.0	Α
Eas	125	mJ
	VGSS ID(DC) ID(pulse) PT1 PT2 Tch Tstg IAS	VGSS ± 30 ID(DC) ± 5 ID(pulse) ± 20 PT1 75 PT2 1.5 Tch 150 Tstg -55 to +150 IAS 5.0



(TO-220AB)

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Starting T_{ch} = 25°C, V_{DD} = 150 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V

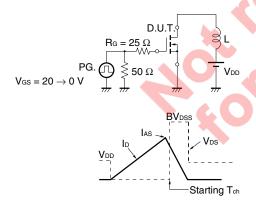
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ELECTRICAL CHARACTERISTICS (TA = 25°C)

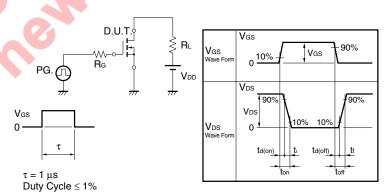
CHRACTERISTICS	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 500 V, V _{GS} = 0 V			100	μΑ
Gate Leakage Current	Igss	V _{GS} = ±30 V, V _{DS} = 0 V			±100	nA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	2.5		3.5	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 2.5 A	1.0	2.2		S
Drain to Source On-state Resistance Note	R _{DS(on)}	V _G S = 10 V, I _D = 2.5 A		1.2	1.5	Ω
Input Capacitance	Ciss	V _{DS} = 10 V		730		pF
Output Capacitance	Coss	V _{GS} = 0 V		120		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		6		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 150 V, I _D = 2.5 A		13		ns
Rise Time	tr	V _{GS} = 10 V		7.5		ns
Turn-off Delay Time	td(off)	R _G = 10 Ω		25		ns
Fall Time	tf	R _L = 60 Ω	7	6.5		ns
Total Gate Charge	Q G	V _{DD} = 400 V		13		nC
Gate to Source Charge	Qgs	V _{GS} = 10 V	K (6		nC
Gate to Drain Charge	Q _{GD}	I _D = 5.0 A		4		nC
Body Diode Forward Voltage Note	V _F (S-D)	I _F = 5.0 A, V _{GS} = 0 V		0.9		V
Reverse Recovery Time	trr	I _F = 5.0 A, V _{GS} = 0 V		240		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/µs	_	1200		nC

Note Pulsed

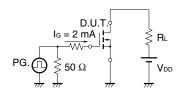
TEST CIRCUIT 1 AVALANCHE CAPABILITY



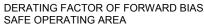
TEST CIRCUIT 2 SWITCHING TIME

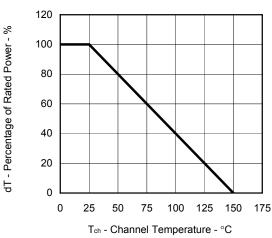


TEST CIRCUIT 3 GATE CHARGE

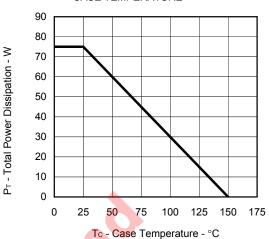


TYPICAL CHARACTERISTICS (TA = 25°C)

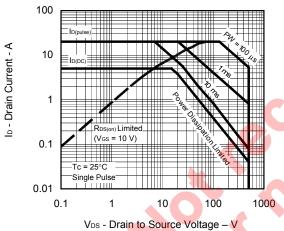




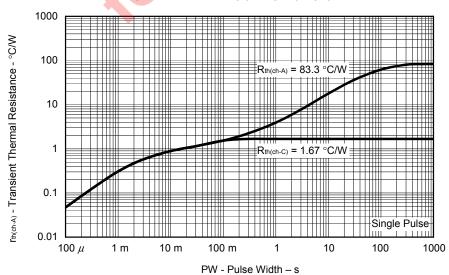
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



FORWARD BIAS SAFE OPERATING AREA

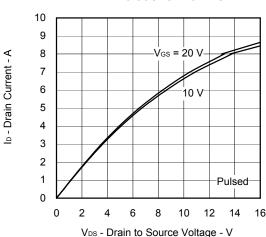




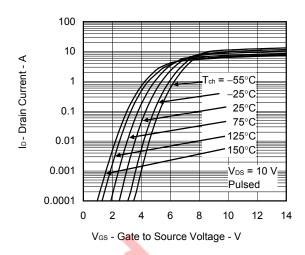


VGS(off) - Gate Cut-off Voltage - V

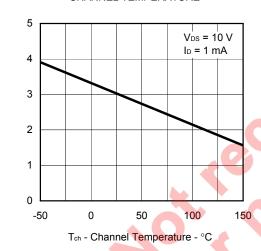
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



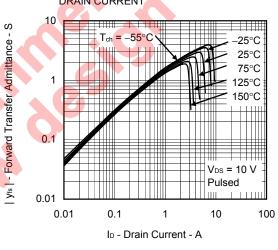
FORWARD TRANSFER CHARACTERISTICS



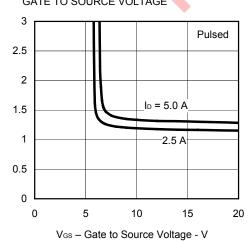
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



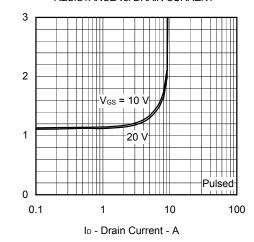
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

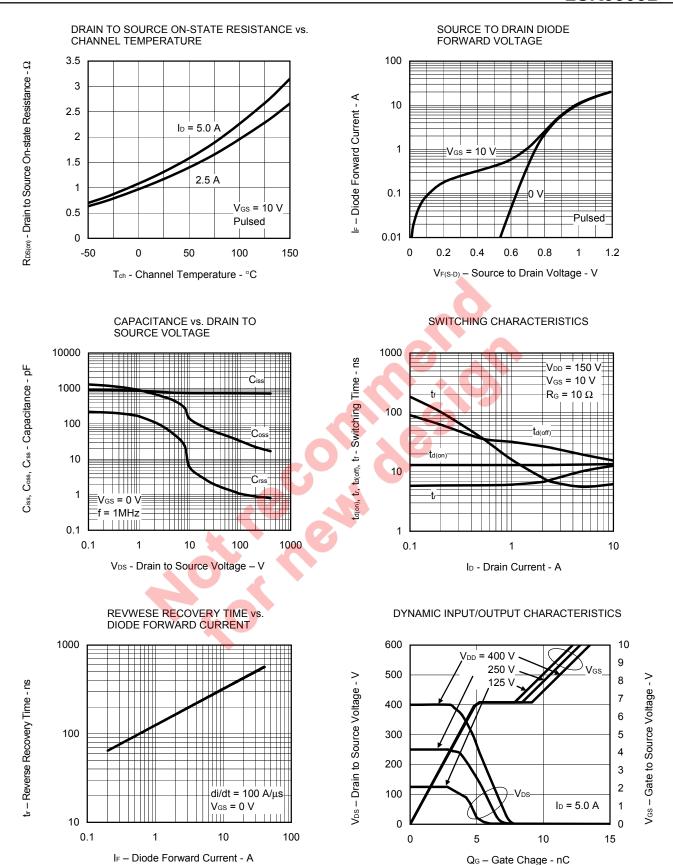


DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

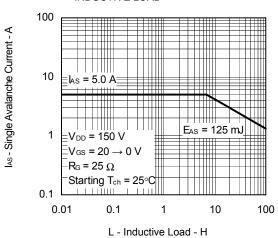


 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - Drain to Source On-state Resistance - Ω

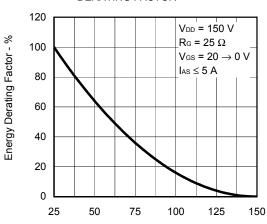
R_{DS(ση)} - Drain to Source On-state Resistance - Ω



SINGLE AVALANCHE CURRENT vs. INDUCTIVE LOAD



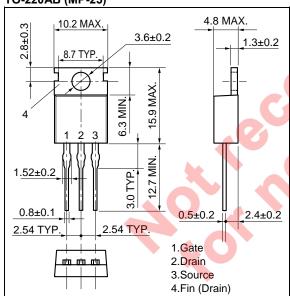
SINGLE AVALANCHE ENERGY DERATING FACTOR



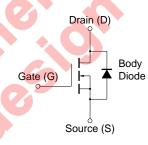
Starting Tch - Starting Channel Temperature - °C

PACKAGE DRAWING (Unit: mm)





EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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