## OCTAL BIDIRECTIONAL TRANSCEIVER WITH 8-BIT PARITY GENERATOR CHECKER (3-STATE OUTPUTS)

The MC74F657A and MC74F657B are Octal Bidirectional Transceivers with an 8-bit parity Generator/Checker and 3-state outputs.

The $A$ and $B$ options are faster versions of the F 657 and contain eight noninverting buffers with 3 -state outputs and an 8 -bit parity generator/checker. These devices are intended for bus-oriented applications. The buffers have a guaranteed current sinking capability of 24 mA at the A ports and 64 mA at the B ports. The Transmit/Receiver (T/R) input determines the direction of the data flow through the bidirectional transceivers. Transmit (active HIGH) enables data from A ports to B ports; Receive (active LOW) enables data from B ports to A ports.

- High-Impedance NPN Base Input for Reduced Loading (20 $\mu \mathrm{A}$ in HIGH and LOW States)
- Ideal in Applications Where High Output Drive and Light Bus Loading are Required ( $\mathrm{I}_{\mathrm{LL}}$ is $20 \mu \mathrm{~A}$ versus Fast std of $600 \mu \mathrm{~A}$ )
- Combines F245 and F280A Functions in One Package
- 3-State Outputs
- B Outputs, PARITY, ERROR, Sink 64 mA and Source 15 mA
- 15 mA Source Current
- Input Diodes for Termination Effects
- Glitchless Outputs During Power Up and Power Down
- High Impedance Outputs During Power Off
- ESD Protection > 4000 Volts


## PIN ASSIGNMENT



LOGIC SYMBOL


MC74F657A,B

OCTAL BIDIRECTIONAL TRANSCEIVER WITH 8-BIT PARITY GENERATOR CHECKER (3-STATE OUTPUTS) FAST ${ }^{\text {T }}$ SCHOTTKY TTL


GUARANTEED OPERATING RANGES

| Symbol | Parameter | Min | Typ | Max | Unit |  |
| :---: | :--- | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{CC}}$ | Supply Voltage | 74 | 4.5 | 5.0 | 5.5 | V |
| $\mathrm{~T}_{\mathrm{A}}$ | Operating Ambient <br> Temperature Range | 74 | 0 | 25 | 70 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{I}_{\mathrm{OH}}$ | Output Current - High | 74 |  |  | $-3.0 /-15$ | mA |
| IOL | Output Current - Low | 74 |  |  | $24 / 64$ | mA |

FUNCTION TABLE

| Number of <br> Inputs That are High | Inputs |  |  | Input/Output | Outputs |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $0,2,4,6,8$ | OE | T/R | Even/Odd | Parity | Error | Outputs Mode |
|  | L | H | H | H | Z | Transmit |
|  | L | H | L | L | Z | Transmit |
|  | L | L | H | H | H | Receive |
|  | L | L | H | L | L | Receive |
|  | L | L | L | H | L | Receive |
|  | L | L | L | L | H | Receive |


|  | Number of Inputs That are High | Inputs |  |  | Input/Output | Outputs |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\square$ | 1, 3, 5, 7 | OE | T/R | Even/Odd | Parity | Error | Outputs Mode |
|  |  | L | H | H | L | Z | Transmit |
|  |  | L | H | L | H | Z | Transmit |
|  |  | L | L | H | H | L | Receive |
|  |  | L | L | H | L | H | Receive |
|  |  | L | L | L | H | H | Receive |
|  |  | L | L | L | L | L | Receive |
|  | Don't Care | H | X | X | Z | Z | Z |

[^0]MC74F657A, B

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

| Symbol | Parameter |  |  | Limits |  |  | Unit | Test Conditions |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | Min | Typ | Max |  |  |  |
| $\mathrm{V}_{\mathrm{IH}}$ | Input HIGH Voltage |  |  | 2.0 |  |  | V | Guaranteed Input HIGH Voltage |  |
| $\mathrm{V}_{\text {IL }}$ | Input LOW Voltage |  |  |  |  | 0.8 | V | Guaranteed Input LOW Voltage |  |
| $\mathrm{V}_{\text {IK }}$ | Input Clamp Diode Voltage |  |  |  | -0.73 |  | V | $\mathrm{V}_{\mathrm{CC}}=\mathrm{MIN}, \mathrm{I} \mathrm{IN}=-18 \mathrm{~mA}$ |  |
| VOH | Output HIGH Voltage | All Outputs | 74 | 2.4 |  |  | V | $\mathrm{IOH}=-3.0 \mathrm{~mA}$ | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}$ |
|  |  |  |  | 2.7 | 3.4 |  |  |  | $\mathrm{V}_{\mathrm{CC}}=4.75 \mathrm{~V}$ |
|  |  | B0-B7 <br> PARITY, <br> ERROR | 74 | 2.0 |  |  | V | $\mathrm{IOH}=-15 \mathrm{~mA}$ | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}$ |
|  |  | A0-A7 | 74 |  | 0.35 | 0.5 | V | $\mathrm{IOL}=24 \mathrm{~mA}$ |  |
| VOL | Output LOW Voltage | B0-B7 PARITY, ERROR | 74 |  | 0.4 | 0.55 | V | $\mathrm{IOL}=64 \mathrm{~mA}$ | $\mathrm{V}_{\mathrm{CC}}=\mathrm{MIN}$ |
|  |  | T/R, OE, EVE | ODD |  |  | 100 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{CC}}=0 \mathrm{~V}, \mathrm{~V}_{\text {IN }}$ | 7.0 V |
|  |  | A0-A7 |  |  |  | 2.0 |  | $\mathrm{V}_{\mathrm{CC}}=5.5 \mathrm{~V}, \mathrm{~V}_{\text {IN }}$ | $=5.5 \mathrm{~V}$ |
| ${ }^{1} \mathrm{H}$ | Input HIGH Current | B0-B7, PA |  |  |  | 1.0 |  | $\mathrm{V}_{\mathrm{CC}}=5.5 \mathrm{~V}, \mathrm{~V}_{\text {IN }}$ | $=5.5 \mathrm{~V}$ |
|  |  | EVEN/OD |  |  |  | 20 |  |  |  |
|  |  | T/R, OE |  |  |  | 40 | $\mu \mathrm{A}$ | $V_{C C}=M A X, V_{\text {IN }}$ | $=2.7 \mathrm{~V}$ |
|  |  | EVEN/OD |  |  |  | -20 |  |  |  |
| IIL | Input LOW Current | T/R, OE |  |  |  | -40 | $\mu \mathrm{A}$ | $V_{C C}=M A X, V_{\text {IN }}$ | 0.5 V |
| $\begin{aligned} & \mathrm{l} \mathrm{H} \\ & +\mathrm{OZHH} \end{aligned}$ | Off-State Current HIGH Level Voltage Applied | $\begin{aligned} & \mathrm{A} 0-\mathrm{A} 7 \\ & \mathrm{~B} 0-\mathrm{B7} \end{aligned}$ |  |  |  | 70 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{CC}}=\mathrm{MAX}, \mathrm{V}_{\mathrm{O}}$ | UT $=2.7 \mathrm{~V}$ |
| $\begin{aligned} & 1 \mathrm{IL} \\ & +\mathrm{l}_{\mathrm{OZL}} \end{aligned}$ | Off-State Current LOW Level Voltage Applied | PARIT |  |  |  | -70 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{CC}}=\mathrm{MAX}, \mathrm{V}_{\mathrm{O}}$ | UT $=0.5 \mathrm{~V}$ |
| IOZH | Off-State Output Current, High-Level Voltage Applied |  |  |  |  | 50 |  | $\mathrm{V}_{\mathrm{CC}}=\mathrm{MAX}, \mathrm{V}_{\mathrm{O}}$ | UT $=2.7 \mathrm{~V}$ |
| IOZL | Off-State Output Current, Low-Level Voltage Applied | ERROR |  |  |  | -50 | $\mu \mathrm{A}$ | $V_{C C}=\mathrm{MAX}, \mathrm{V}_{\mathrm{O}}$ | UT $=0.5 \mathrm{~V}$ |
|  |  | $A_{n}$ Outpu |  | -60 |  | -150 |  |  |  |
| Ios | Current (Note 2) | PARITY, $\mathrm{B}_{n} \mathrm{O}$ ERROR | puts, | -100 |  | -225 | mA | $\mathrm{V}_{\mathrm{CC}}=\mathrm{MAX}, \mathrm{V}_{\mathrm{O}}$ | $\mathrm{UT}=0 \mathrm{~V}$ |
|  |  | ${ }^{\text {ICCH }}$ |  |  | 90 | 135 |  |  |  |
| ICC | Total Supply Current | ICCL |  |  | 106 | 150 | mA | $V_{C C}=M A X$ |  |
|  |  | ICCZ |  |  | 98 | 145 |  |  |  |

NOTES:

1. For conditions shown as MIN or MAX, use appropriate value specified under recommended operating conditions for the applicable device type.
2. Not more than one output should be shorted at one time, nor for more than 1 second.

F657A
AC ELECTRICAL CHARACTERISTICS

| Symbol | Parameter | 74F |  |  | 74F |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} \mathrm{T}_{\mathrm{A}} & =+25^{\circ} \mathrm{C} \\ \mathrm{~V}_{\mathrm{CC}} & =+5.0 \mathrm{~V} \\ \mathrm{C}_{\mathrm{L}} & =50 \mathrm{pF} \end{aligned}$ |  |  | $\begin{gathered} \mathrm{T}_{\mathrm{A}}=0^{\circ} \mathrm{C} \text { to }+70^{\circ} \mathrm{C} \\ \mathrm{~V}_{\mathrm{CC}}=+5.0 \mathrm{~V} \pm 10 \% \\ \mathrm{C}_{\mathrm{L}}=50 \mathrm{pF} \end{gathered}$ |  |  |
|  |  | Min | Typ | Max | Min | Max |  |
| $\begin{aligned} & \text { tpLH } \\ & \text { tpHL } \end{aligned}$ | Propagation Delay <br> $A_{n}$ to $B_{n}$ or $B_{n}$ to $A_{n}$ | $\begin{aligned} & 2.0 \\ & 2.0 \end{aligned}$ |  | $\begin{aligned} & 7.0 \\ & 7.0 \end{aligned}$ | $\begin{aligned} & 2.0 \\ & 2.0 \end{aligned}$ | $\begin{aligned} & 7.5 \\ & 7.5 \end{aligned}$ | ns |
| tpLH <br> tpHL | Propagation Delay $A_{n}$ to PARITY | $\begin{aligned} & 6.0 \\ & 6.5 \end{aligned}$ |  | $\begin{aligned} & 13 \\ & 13 \end{aligned}$ | $\begin{aligned} & 5.5 \\ & 6.5 \end{aligned}$ | $\begin{aligned} & 14 \\ & 14 \end{aligned}$ | ns |
| $\begin{aligned} & \text { tpLH } \\ & \text { tPHL } \end{aligned}$ | Propagation Delay EVEN /ODD to PARITY, ERROR | $\begin{aligned} & 4.5 \\ & 4.5 \end{aligned}$ |  | $\begin{aligned} & 10.5 \\ & 10.5 \end{aligned}$ | $\begin{aligned} & 4.5 \\ & 4.5 \end{aligned}$ | $\begin{gathered} 11 \\ 11.5 \end{gathered}$ | ns |
| $\begin{aligned} & \text { tPLH } \\ & \text { tPHL } \end{aligned}$ | Propagation Delay <br> $B_{n}$ to ERROR | $\begin{aligned} & 7.0 \\ & 7.0 \end{aligned}$ |  | $\begin{aligned} & 18 \\ & 18 \end{aligned}$ | $\begin{aligned} & 6.5 \\ & 6.5 \end{aligned}$ | $\begin{aligned} & 19 \\ & 19 \end{aligned}$ | ns |
| $\begin{aligned} & \text { tpLH } \\ & \text { tPHL } \end{aligned}$ | Propagation Delay PARITY to ERROR | $\begin{aligned} & 8.0 \\ & 7.0 \end{aligned}$ |  | $\begin{aligned} & 14 \\ & 14 \end{aligned}$ | $\begin{aligned} & 7.0 \\ & 7.0 \end{aligned}$ | $\begin{aligned} & 15 \\ & 15 \end{aligned}$ | ns |
| $\begin{aligned} & \text { tPZH } \\ & \text { tPZL } \end{aligned}$ | Output Enable Time to HIGH or LOW Level | $\begin{aligned} & 3.0 \\ & 4.0 \end{aligned}$ |  | $\begin{aligned} & 8.0 \\ & 9.0 \end{aligned}$ | $\begin{aligned} & 3.0 \\ & 4.0 \end{aligned}$ | $\begin{gathered} 9.0 \\ 10 \end{gathered}$ | ns |
| $\begin{aligned} & \text { tphZ } \\ & \text { tpLZ } \\ & \hline \end{aligned}$ | Output Disable Time from HIGH or LOW Level | $\begin{aligned} & 2.0 \\ & 2.0 \end{aligned}$ |  | $\begin{aligned} & 7.5 \\ & 6.0 \end{aligned}$ | $\begin{aligned} & 2.0 \\ & 2.0 \end{aligned}$ | $\begin{aligned} & 8.0 \\ & 6.5 \end{aligned}$ | ns |

## F657B

AC ELECTRICAL CHARACTERISTICS

| Symbol | Parameter | 74F |  |  | 74F |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{aligned} \mathrm{T}_{\mathrm{A}} & =+25^{\circ} \mathrm{C} \\ \mathrm{v}_{\mathrm{CC}} & =+5.0 \mathrm{~V} \\ \mathrm{C} & =50 \mathrm{pF} \end{aligned}$ |  |  | $\begin{gathered} \mathrm{T}_{\mathrm{A}}=0^{\circ} \mathrm{C} \text { to }+70^{\circ} \mathrm{C} \\ \mathrm{~V}_{\mathrm{CC}}=+5.0 \mathrm{~V} \pm 10 \% \\ \mathrm{C}_{\mathrm{L}}=50 \mathrm{pF} \end{gathered}$ |  |  |
|  |  | Min | Typ | Max | Min | Max |  |
| $\begin{aligned} & \hline \text { tPLH } \\ & \text { tpHL } \end{aligned}$ | Propagation Delay <br> $A_{n}$ to $B_{n}$ or $B_{n}$ to $A_{n}$ | $\begin{aligned} & 2.0 \\ & 2.0 \end{aligned}$ |  | $\begin{aligned} & 6.0 \\ & 6.0 \end{aligned}$ | $\begin{aligned} & 2.0 \\ & 2.0 \end{aligned}$ | $\begin{aligned} & 6.5 \\ & 6.5 \end{aligned}$ | ns |
| $\begin{aligned} & \text { tPLH } \\ & \text { tpHL } \end{aligned}$ | Propagation Delay $A_{n}$ to PARITY | $\begin{aligned} & 4.5 \\ & 4.5 \end{aligned}$ |  | $\begin{aligned} & 11.5 \\ & 11.5 \end{aligned}$ | $\begin{aligned} & 4.5 \\ & 4.5 \end{aligned}$ | $\begin{aligned} & 13 \\ & 13 \end{aligned}$ | ns |
| $\begin{aligned} & \text { tpLH } \\ & \text { tpHL } \end{aligned}$ | Propagation Delay EVEN/ODD to PARITY, ERROR | $\begin{aligned} & 2.0 \\ & 2.0 \end{aligned}$ |  | $\begin{aligned} & 7.5 \\ & 7.5 \end{aligned}$ | $\begin{aligned} & 2.0 \\ & 2.0 \end{aligned}$ | $\begin{aligned} & 8.5 \\ & 8.5 \end{aligned}$ | ns |
| $\begin{aligned} & \text { tpLH } \\ & \text { tpHL } \end{aligned}$ | Propagation Delay <br> $B_{n}$ to ERROR | $\begin{aligned} & 4.0 \\ & 4.0 \end{aligned}$ |  | $\begin{aligned} & 15 \\ & 15 \end{aligned}$ | $\begin{aligned} & 3.5 \\ & 3.5 \end{aligned}$ | $\begin{aligned} & \hline 16 \\ & 16 \end{aligned}$ | ns |
| $\begin{aligned} & \hline \text { tPLH } \\ & \text { tpHL } \end{aligned}$ | Propagation Delay PARITY to ERROR | $\begin{aligned} & \hline 5.0 \\ & 5.0 \end{aligned}$ |  | $\begin{aligned} & \hline 11 \\ & 11 \end{aligned}$ | $\begin{aligned} & \hline 4.0 \\ & 4.0 \end{aligned}$ | $\begin{aligned} & 12 \\ & 10 \end{aligned}$ | ns |
| $\begin{aligned} & \text { tPZH } \\ & \text { tPZL } \end{aligned}$ | Output Enable Time to HIGH or LOW Level | $\begin{aligned} & 2.0 \\ & 2.0 \end{aligned}$ |  | $\begin{aligned} & 7.0 \\ & 7.0 \end{aligned}$ | $\begin{aligned} & 2.0 \\ & 2.0 \end{aligned}$ | $\begin{aligned} & 8.0 \\ & 8.0 \end{aligned}$ | ns |
| $\begin{aligned} & \text { tpHZ } \\ & \text { tpLZ } \\ & \hline \end{aligned}$ | Output Disable Time from HIGH or LOW Level | $\begin{aligned} & 2.0 \\ & 2.0 \end{aligned}$ |  | $\begin{aligned} & 6.0 \\ & 6.0 \end{aligned}$ | $\begin{aligned} & 2.0 \\ & 2.0 \end{aligned}$ | $\begin{aligned} & 6.5 \\ & 6.5 \end{aligned}$ | ns |

MC74F657A, B


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[^0]:    H = HIGH Voltage Level; L = LOW Voltage Level; X = Don't Care; Z = HIGH impedance state.

