FAIRCHILD

SEMICONDUCTOR®

SGW13N60UFD

Ultra-Fast IGBT

General Description

Fairchild's UFD series of Insulated Gate Bipolar Transistors (IGBTs) provides low conduction and switching losses. The UFD series is designed for applications such as motor control and general inverters where high speed switching is a required feature.

Features

- High speed switching
- Low saturation voltage : $V_{CE(sat)} = 2.1 \text{ V} @ I_C = 6.5 \text{ A}$

SGW13N60UFD

IGBT

- High input impedance
- CO-PAK, IGBT with FRD : t_{rr} = 37ns (typ.)

Applications

AC & DC motor controls, general purpose inverters, robotics, and servo controls.





Absolute Maximum Ratings $T_c = 25^{\circ}C$ unless otherwise noted

Symbol	Description		SGW13N60UFD	Units	
V _{CES}	Collector-Emitter Voltage		600	V	
V _{GES}	Gate-Emitter Voltage		± 20	V	
1	Collector Current	@ T _C = 25°C	13	A	
I _C	Collector Current	@ T _C = 100°C	6.5	A	
I _{CM (1)}	Pulsed Collector Current		52	А	
I _F	Diode Continuous Forward Current	@ T _C = 100°C	8	A	
I _{FM}	Diode Maximum Forward Current		56	A	
PD	Maximum Power Dissipation	@ $T_{C} = 25^{\circ}C$	60	W	
	Maximum Power Dissipation	@ T _C = 100°C	25	W	
Τ _J	Operating Junction Temperature		-55 to +150	°C	
T _{stg}	Storage Temperature Range		-55 to +150	°C	
TL	Maximum Lead Temp. for Soldering Purposes from Case for 5 Seconds		300	°C	

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
R _{θJC} (IGBT)	Thermal Resistance, Junction-to-Case		2.0	°C/W
R _{0JC} (DIODE)	Thermal Resistance, Junction-to-Case		3.5	°C/W
$R_{ hetaJA}$	Thermal Resistance, Junction-to-Ambient (PCB Mount) (2)		40	°C/W

Notes :

(2) Mounted on 1" squre PCB (FR4 or G-10 Material)

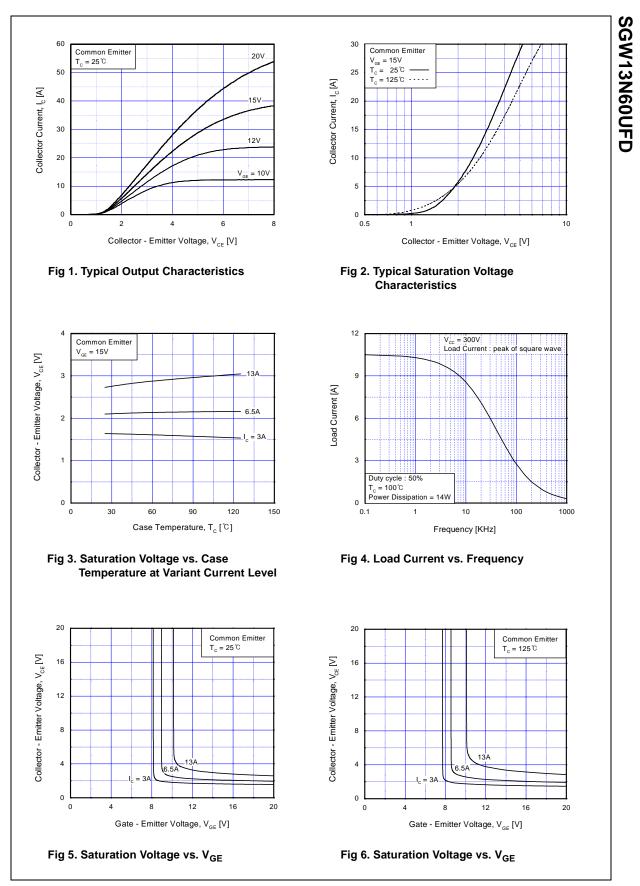
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Cha	racteristics					
BV _{CES}	Collector-Emitter Breakdown Voltage V _{GE} = 0V, I _C = 250uA		600			V
$\Delta B_{VCES}/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0V, I_C = 1mA$		0.6		V/∘C
I _{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$			250	uA
I _{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$			± 100	nA
On Cha	racteristics					
V _{GE(th)}	G-E Threshold Voltage	I_{C} = 6.5mA, V_{CE} = V_{GE}	3.5	4.5	6.5	V
	Collector to Emitter	$I_{C} = 6.5A, V_{GE} = 15V$		2.1	2.6	V
V _{CE(sat)}	Saturation Voltage	I _C = 13A, V _{GE} = 15V		2.6		V
Dynami C _{ies}	c Characteristics			375		pF
C _{oes}	Output Capacitance	$V_{CE} = 30V_{,}V_{GE} = 0V_{,}$		63		pF
C _{res}	Reverse Transfer Capacitance	f = 1MHz		13		pF
t _{d(on)}	ng Characteristics Turn-On Delay Time			20		ns
<u>tr</u>	Rise Time	-		20		ns
t _{d(off)}	Turn-Off Delay Time	V _{CC} = 300 V, I _C = 6.5A,		70	130	ns
t _f	Fall Time	$R_{G} = 50\Omega, V_{GE} = 15V,$		97	150	ns
	Turn-On Switching Loss	Inductive Load, $T_C = 25^{\circ}C$		85		uJ
⊏on				95		uJ
E _{off}	Turn-Off Switching Loss			180	270	uJ
E _{off}	Turn-Off Switching Loss Total Switching Loss	-		100		
E _{off} E _{ts}	•	-		30		ns
E _{off} E _{ts} t _{d(on)}	Total Switching Loss	-				ns
E _{off} E _{ts} t _{d(on)} t _r	Total Switching Loss Turn-On Delay Time	- V _{CC} = 300 V, I _C = 6.5A,		30		
$\frac{E_{off}}{E_{ts}}$ $\frac{t_{d(on)}}{t_{r}}$ $\frac{t_{d(off)}}{t_{f}}$	Total Switching Loss Turn-On Delay Time Rise Time	V _{CC} = 300 V, I _C = 6.5A, R _G = 50Ω, V _{GE} = 15V,		30 32		ns
$\frac{E_{off}}{E_{ts}}$ $\frac{t_{d(on)}}{t_{r}}$ $\frac{t_{d(off)}}{t_{f}}$	Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time	$V_{CC} = 300 \text{ V}, I_C = 6.5\text{A},$ $R_G = 50\Omega, V_{GE} = 15\text{V},$ Inductive Load, $T_C = 125^{\circ}\text{C}$		30 32 85	 200	ns ns
E _{off} E _{ts} t _{d(on)} t _r t _{d(off)} t _f E _{on} E _{off}	Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Turn-Off Switching Loss	$R_{G} = 50\Omega, V_{GE} = 15V,$	 	30 32 85 168	 200 250	ns ns ns
E _{off} E _{ts} t _{d(on)} t _r t _{d(off)} t _f E _{on} E _{off} E _{ts}	Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Total Switching Loss	$R_{G} = 50\Omega, V_{GE} = 15V,$	 	30 32 85 168 180 165 345	 200 250 500	ns ns uJ uJ uJ
$\begin{array}{c} E_{off} \\ E_{ts} \\ t_{d(on)} \\ t_r \\ t_{r} \\ t_{d(off)} \\ t_{f} \\ E_{on} \\ E_{off} \\ E_{ts} \\ Q_{g} \end{array}$	Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Turn-Off Switching Loss	$R_G = 50\Omega$, $V_{GE} = 15V$, Inductive Load, $T_C = 125^{\circ}C$	 	30 32 85 168 180 165	 200 250 	ns ns ns uJ uJ
$\begin{array}{c} E_{off} \\ E_{ts} \\ t_{d(on)} \\ t_r \\ t_r \\ t_{d(off)} \\ E_{on} \\ E_{off} \\ E_{ts} \\ Q_g \\ Q_{ge} \end{array}$	Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Total Switching Loss Total Gate Charge Gate-Emitter Charge	$R_{G} = 50\Omega, V_{GE} = 15V,$ Inductive Load, $T_{C} = 125^{\circ}C$ $V_{CE} = 300 \text{ V}, I_{C} = 6.5\text{A},$	 	30 32 85 168 180 165 345 25 7	 200 250 500 35 12	ns ns uJ uJ uJ nC nC
Eon Eoff Ets td(on) tr tdoff Eon Eonff Eonff Ets Qg Qge Qgc	Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Total Switching Loss Total Gate Charge	$R_G = 50\Omega$, $V_{GE} = 15V$, Inductive Load, $T_C = 125^{\circ}C$	 	30 32 85 168 180 165 345 25	 200 250 500 35	ns ns uJ uJ uJ nC

Electrical Characteristics of DIODE $T_{C} = 25^{\circ}C$ unless otherwise noted

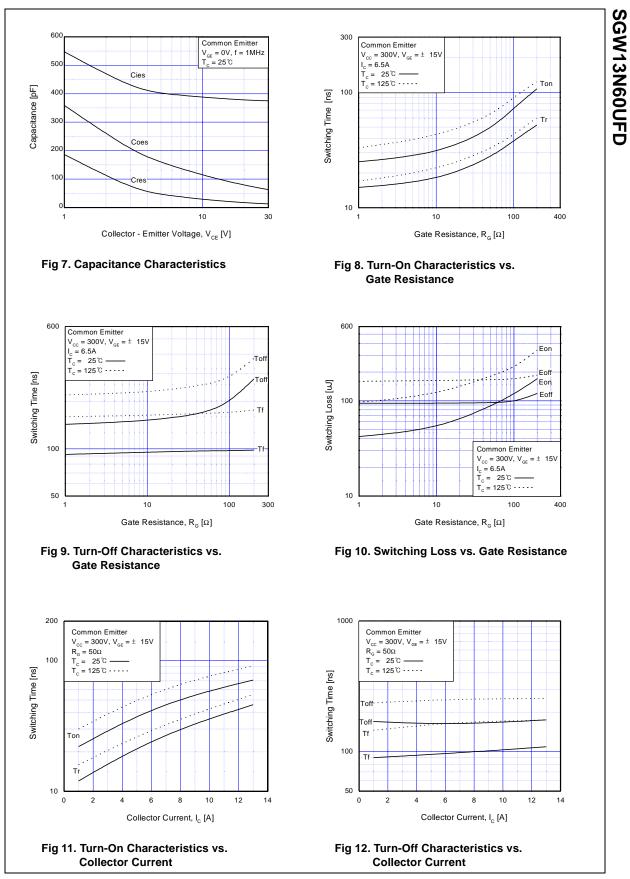
Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Units
V	Diode Forward Voltage	1 _ 94	$T_{C} = 25^{\circ}C$		1.4	1.7	V
V _{FM} Diode Forward Vo	Didde Forward Voltage	I _F = 8A	$T_{C} = 100^{\circ}C$		1.3		V
rr Diode Reverse Recovery Time		$T_{C} = 25^{\circ}C$		37	55	-	
		$T_{C} = 100^{\circ}C$		55		ns	
1	Diode Peak Reverse Recovery	I _F = 8A,	$T_{C} = 25^{\circ}C$		3.5	5.0	Α
rr	Current	di/dt=200A/us	$T_{C} = 100^{\circ}C$		4.5		A
Q _{rr} Diode Reverse Re	Diada Davarra Daaavarra Channa		$T_{C} = 25^{\circ}C$		65	138	
	Diode Reverse Recovery Charge		T _C = 100°C		124		nC

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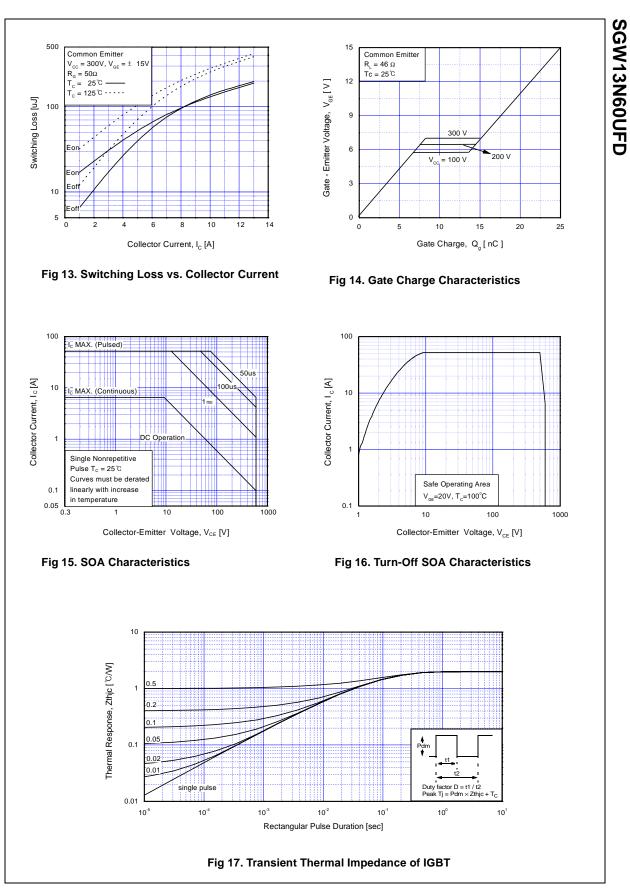
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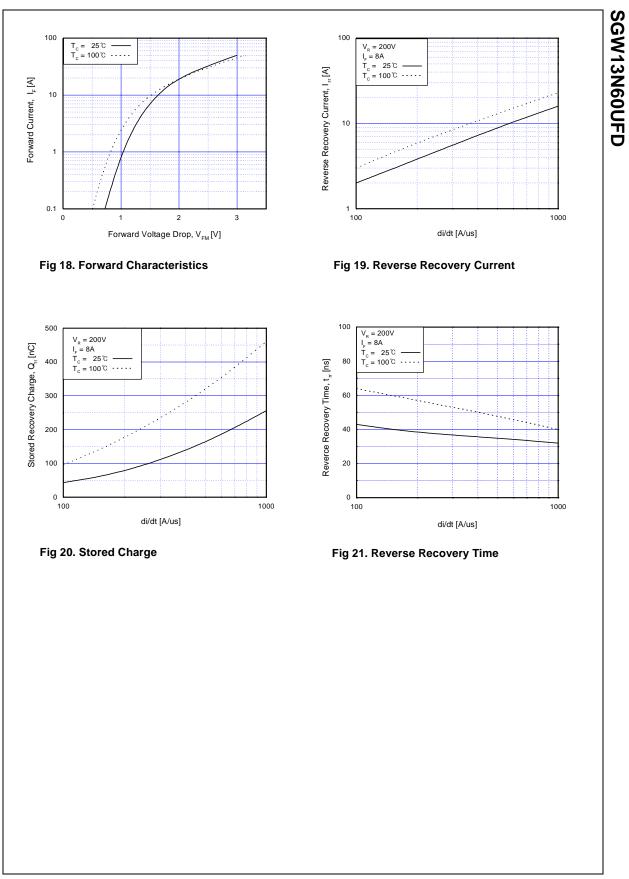


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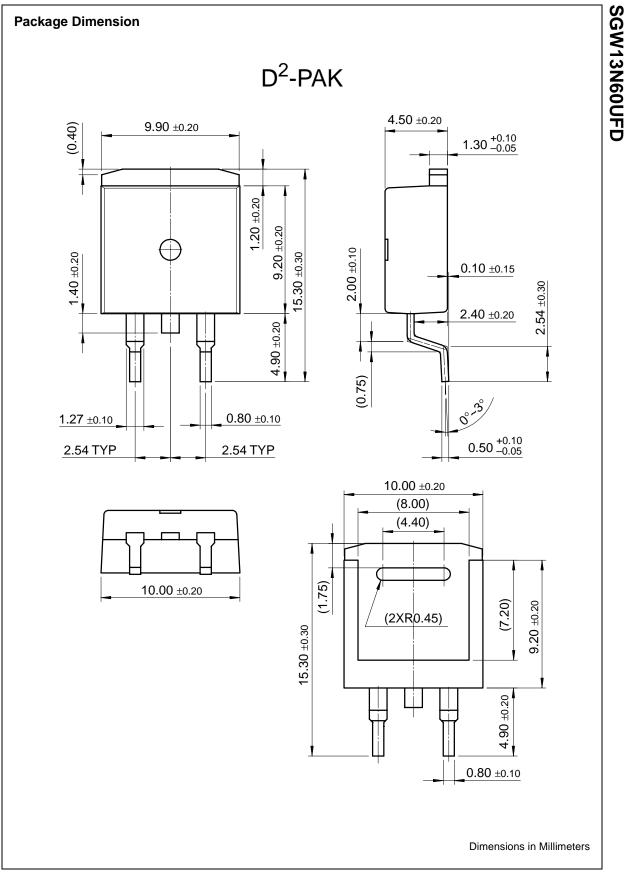


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PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
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Interface	<u>General description Features Applications </u> Product status/pricing/packaging	datasheet	Dotted line Product Change Notices
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Memory	General description	e-mail this datasheet	Support Dotted line
Optoelectronics		[E-	Distributor and field sales
Markets and	Fairchild's UFD series of Insulated Gate		representatives
applications	Bipolar Transistors (IGBTs) provides low	This pagePrint version	Quality and reliability
<u>New products</u> Product selection and	conduction and switching losses. The UFD series is designed for applications such as		Dotted line
parametric search	motor control and general inverters where high		Design tools
Cross-reference	speed switching is a required feature.		
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my Fairchild	• High Speed Switching		
company	• Low Saturation Voltage : $V_{CE(sat)} = 2.1$ V @ I _C = 6.5A		
	 High Input Impedance CO-PAK, IGBT with FRD : t rr = 37ns (typ.) 		

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Applications

AC &DC Motor controls, General Purpose Inverters, Robotics, Servo Controls

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Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
SGW13N60UFDTM	Full Production	\$1.31	TO-263(D2PAK)	2	TAPE REEL

* 1,000 piece Budgetary Pricing

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