

October 2001

IGBT

SGF80N60UF

Ultra-Fast IGBT

General Description

Fairchild's Insulated Gate Bipolar Transistor(IGBT) UF series provides low conduction and switching losses. UF series is designed for the applications such as motor control and general inverters where High Speed Switching is required.

Features

- High Speed Switching
- Low Saturation Voltage : $V_{CE(sat)} = 2.1 \text{ V } @ I_C = 40 \text{A}$
- High Input Impedance

Application

AC & DC Motor controls, General Purpose Inverters, Robotics, Servo Controls





Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Description		SGF80N60UF	Units
V _{CES}	Collector-Emitter Voltage		600	V
V _{GES}	Gate-Emitter Voltage		± 20	V
I _C	Collector Current	@ $T_C = 25^{\circ}C$	80	Α
	Collector Current	@ T _C = 100°C	40	Α
I _{CM (1)}	Pulsed Collector Current		220	Α
P _D	Maximum Power Dissipation	@ T _C = 25°C	110	W
	Maximum Power Dissipation	@ T _C = 100°C	45	W
TJ	Operating Junction Temperature		-55 to +150	°C
T _{stg}	Storage Temperature Range		-55 to +150	°C
T _L	Maximum Lead Temp. for Soldering Purposes, 1/8" from Case for 5 Seco		300	°C

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		1.1	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		40	°C/W

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Cha	racteristics					
BV _{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250uA$	600			V
$\Delta B_{VCES}/$ ΔT_J	Temperature Coeff. of Breakdown Voltage	$V_{GE} = 0V$, $I_C = 1mA$		0.6		V/°C
I _{CES}	Collector Cut-Off Current	V _{CE} = V _{CES} , V _{GE} = 0V			250	uA
I _{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$			± 100	nA
On Cha	racteristics					
V _{GE(th)}	G-E Threshold Voltage	$I_C = 40 \text{mA}, V_{CE} = V_{GE}$	3.5	4.5	6.5	V
	Collector to Emitter	$I_C = 40A$, $V_{GE} = 15V$		2.1	2.6	V
$V_{CE(sat)}$	Saturation Voltage	$I_C = 80A$, $V_{GE} = 15V$		2.6		V
	c Characteristics			0700		
C _{ies}	Input Capacitance	$V_{CE} = 30V_{V_{GE}} = 0V_{V_{GE}}$		2790		pF
C _{oes} C _{res}	Output Capacitance Reverse Transfer Capacitance	f = 1MHz		350 100		pF pF
	ng Characteristics Turn-On Delay Time			23		ns
t _{d(on)}	-					
t _r	Rise Time			50	130	ns
t _{d(off)}	Turn-Off Delay Time	$V_{CC} = 300 \text{ V}, I_{C} = 40 \text{A},$		90		
t _f		D 50 W 45W				ns
	Fall Time	$R_G = 5\Omega$, $V_{GE} = 15V$,		50	150	ns
E _{on}	Turn-On Switching Loss	$R_G = 5\Omega$, $V_{GE} = 15V$, Inductive Load, $T_C = 25$ °C		570		ns uJ
E _{on}	Turn-On Switching Loss Turn-Off Switching Loss			570 590	150 	ns uJ uJ
E _{on} E _{off}	Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss			570 590 1160	150 1500	ns uJ uJ
E _{on} E _{off} E _{ts}	Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time			570 590 1160 30	150 1500	ns uJ uJ uJ ns
E _{on} E _{off} E _{ts} t _{d(on)} t _r	Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time	Inductive Load, T _C = 25°C	 	570 590 1160 30 55	150 1500 	ns uJ uJ uJ ns
E _{on} E _{off} E _{ts} t _{d(on)} t _r t _{d(off)}	Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time	Inductive Load, $T_C = 25^{\circ}C$ $V_{CC} = 300 \text{ V, } I_C = 40\text{A,}$	 	570 590 1160 30 55 150	150 1500 200	ns uJ uJ uJ ns ns
E _{on} E _{off} E _{ts} t _{d(on)} t _r t _{d(off)}	Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time	Inductive Load, $T_C = 25^{\circ}C$ $V_{CC} = 300 \text{ V, } I_C = 40\text{A,}$ $R_G = 5\Omega, V_{GE} = 15\text{V,}$	 	570 590 1160 30 55 150	150 1500 200 250	ns uJ uJ ns ns ns
E _{on} E _{off} Ets td(on) tr td(off) tf	Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn- On Switching Loss	Inductive Load, $T_C = 25^{\circ}C$ $V_{CC} = 300 \text{ V, } I_C = 40\text{A,}$	 	570 590 1160 30 55 150 160 630	150 1500 200 250	ns UJ UJ ns ns ns ns
E _{on} E _{off} Ets td(on) tr td(off) te Eon	Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn- On Switching Loss Turn- Off Switching Loss	Inductive Load, $T_C = 25^{\circ}C$ $V_{CC} = 300 \text{ V, } I_C = 40\text{A,}$ $R_G = 5\Omega, V_{GE} = 15\text{V,}$	 	570 590 1160 30 55 150 160 630 940	150 1500 200 250 	ns uJ uJ ns ns ns ns uJ
Eon Eoff Ets td(on) tr td(off) tf Eon Eoff Eoff Eoff Eoff Ets	Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn- On Switching Loss Turn- Off Switching Loss Total Switching Loss	Inductive Load, $T_C = 25^{\circ}C$ $V_{CC} = 300 \text{ V, } I_C = 40\text{A,}$ $R_G = 5\Omega, V_{GE} = 15\text{V,}$	 	570 590 1160 30 55 150 160 630 940	150 1500 200 250 2000	ns uJ uJ ns ns ns us ns us
Eon Eoff Ets td(on) tr td(off) tf Eon Eoff Eon Eoff Eog	Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn- On Switching Loss Turn- Off Switching Loss Total Switching Loss Total Gate Charge	Inductive Load, $T_C = 25^{\circ}C$ $V_{CC} = 300 \text{ V, } I_C = 40\text{A,}$ $R_G = 5\Omega, V_{GE} = 15\text{V,}$ Inductive Load, $T_C = 125^{\circ}C$	 	570 590 1160 30 55 150 160 630 940 1580 175	150 1500 200 250 2000 250	ns uJ uJ ns ns ns uJ uJ nc
Eon	Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn- On Switching Loss Turn- Off Switching Loss Total Switching Loss Total Gate Charge Gate-Emitter Charge	Inductive Load, $T_C = 25^{\circ}C$ $V_{CC} = 300 \text{ V, } I_C = 40\text{A,}$ $R_G = 5\Omega, V_{GE} = 15\text{V,}$	 	570 590 1160 30 55 150 160 630 940 1580 175 25	150 1500 200 250 2000 250 40	ns uJ uJ ns ns ns us us ns ns ns ns ns ns
E _{on} E _{off} Ets td(on) tr td(off) tf E _{on} E _{off} E _{on} Q _g Q _{gc}	Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn- On Switching Loss Turn- Off Switching Loss Total Switching Loss Total Gate Charge	Inductive Load, $T_C = 25^{\circ}C$ $V_{CC} = 300 \text{ V, } I_C = 40\text{A,}$ $R_G = 5\Omega, V_{GE} = 15\text{V,}$ Inductive Load, $T_C = 125^{\circ}C$ $V_{CE} = 300 \text{ V, } I_C = 40\text{A,}$	 	570 590 1160 30 55 150 160 630 940 1580 175	150 1500 200 250 2000 250	ns uJ uJ ns ns ns us us ns ns uJ uJ uJ

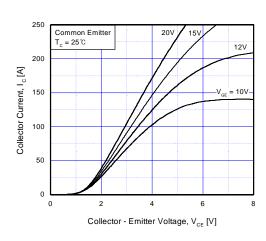


Fig 1. Typical Output Characteristics

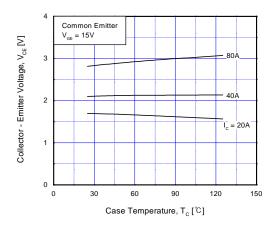


Fig 3. Saturation Voltage vs. Case Temperature at Variant Current Level

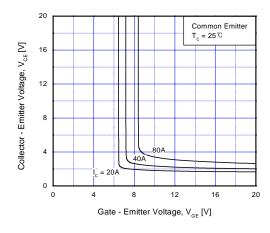


Fig 5. Saturation Voltage vs. V_{GE}

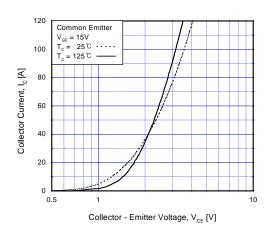


Fig 2. Typical Saturation Voltage Characteristics

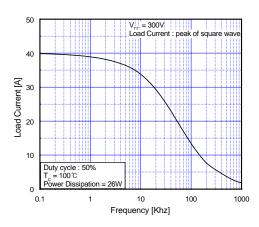


Fig 4. Load Current vs. Frequency

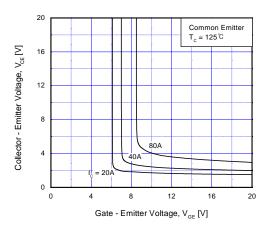
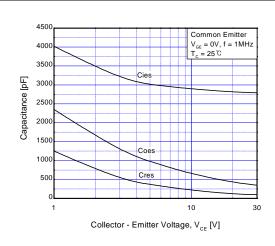


Fig 6. Saturation Voltage vs. $V_{\rm GE}$

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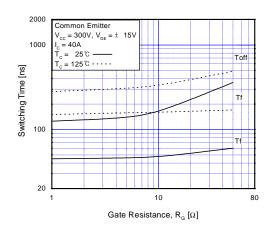
 $\begin{array}{c} V_{cc} = 300V, V_{cE} = \pm \ 15V \\ I_c = 40A \\ T_c = 25C \\ T_c = 125C \\ \end{array} \\ \begin{array}{c} T_{cc} =$

500

Common Emitter

Fig 7. Capacitance Characteristics

Fig 8. Turn-On Characteristics vs.
Gate Resistance



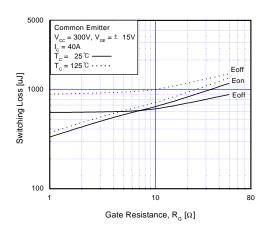
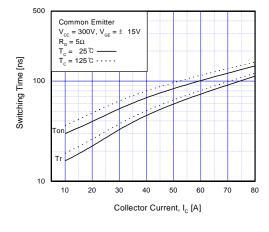


Fig 9. Turn-Off Characteristics vs.
Gate Resistance

Fig 10. Switching Loss vs. Gate Resistance



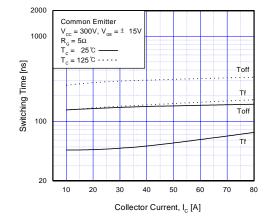


Fig 11. Turn-On Characteristics vs. Collector Current

Fig 12. Turn-Off Characteristics vs.
Collector Current

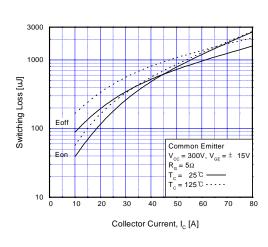
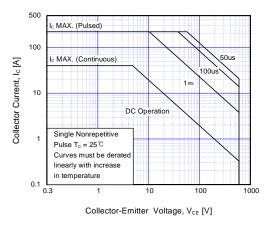


Fig 13. Switching Loss vs. Collector Current

Fig 14. Gate Charge Characteristics



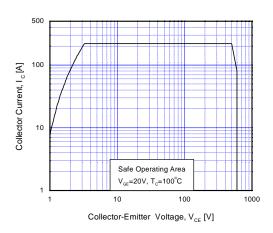


Fig 15. SOA Characteristics

Fig 16. Turn-Off SOA Characteristics

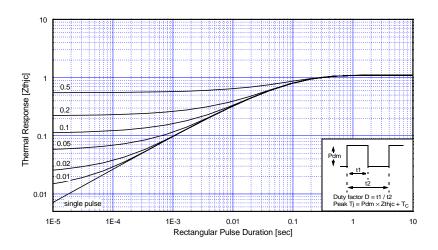
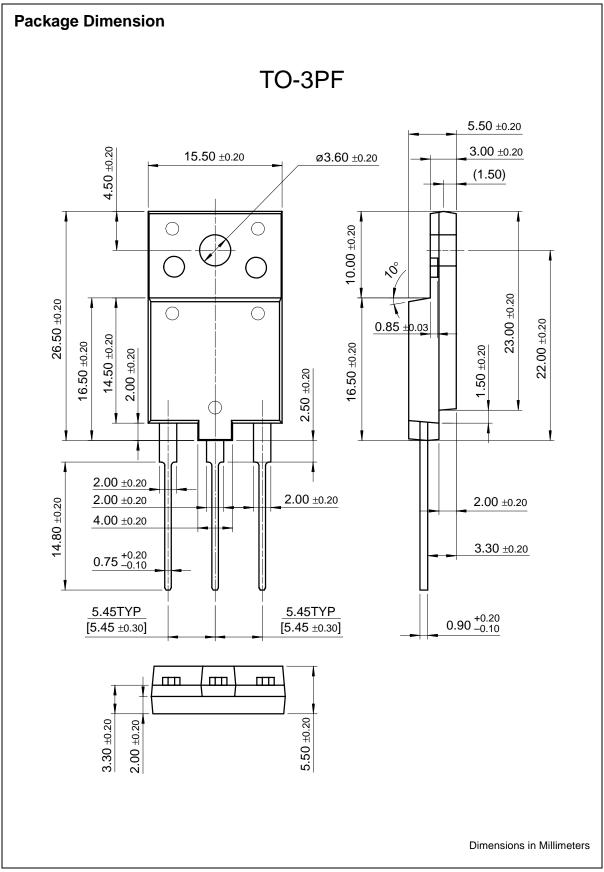


Fig 17. Transient Thermal Impedance of IGBT

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Product status/pricing/packaging

Product Product status Package type Leads Packing method
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Product Folder - Fairchild P/N SGF80N60UF - Discrete, High Performance IGBT

SGF80N60UFTU	Full Production	TO-3PF	3	RAIL

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