

μ PD5758T6J

Low Noise and High Gain Amplifier IC for Impedance Converter of Microphone

R09DS0017EJ0100 Rev.1.00 Apr 18, 2011

DESCRIPTION

The μ PD5758T6J is a silicon MOS monolithic integrated circuit designed as high gain impedance converter for electret condenser microphone. This device exhibits low noise and high voltage gain characteristics.

The package is a 3-pin thin-type lead-less minimold, suitable for high-density surface mounting.

FEATURES

• Low noise : $N_V = -101 \text{ dBV TYP.}$ @ $V_{DD} = 1.5 \text{ V}$, $C_{in} = 3 \text{ pF}$, $R_L = 2.2 \text{ k}\Omega$

: N_V = -102 dBV TYP. @ V_{DD} = 1.5 V, C_{in} = 5 pF, R_L = 2.2 k Ω

• High gain : $G_V = +5.7 \text{ dB TYP.} @V_{DD} = 1.5 \text{ V}, C_{in} = 3 \text{ pF}, R_L = 2.2 \text{ k}\Omega$

: G_V = +7.7 dB TYP. @ V_{DD} = 1.5 V, C_{in} = 5 pF, R_L = 2.2 k Ω

• Low input capacitance : $C_{input} = 1.5 \text{ pF TYP.}$ @ $V_{DD} = 1.5 \text{ V}$, $R_L = 2.2 \text{ k}\Omega$ • Low consumption current : $I_{DD} = 190 \ \mu\text{A TYP.}$ @ $V_{DD} = 1.5 \text{ V}$, $R_L = 2.2 \text{ k}\Omega$

• High-density surface mounting : 3-pin thin-type lead-less minimold $(1.2 \times 1.0 \times 0.33 \text{ mm})$

• Built-in the capacitor for RF noise immunity

• High ESD voltage

APPLICATIONS

• Microphone, Sensor etc.

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying	Form
μPD5758T6J-E4	μPD5758T6J-E4-A	3-pin thin-type	6Y	Embossed tape 8 mm	wide
		lead-less minimold		Pin 3 face the perforati	on side of the tape
		(Pb-Free)		Qtv 10 kpcs/reel	

Remark To order evaluation samples, please contact your nearby sales office.

Part number for sample order: μ PD5758T6J

CAUTION

Observe precautions when handling because these devices are sensitive to electrostatic discharge.

ABSOLUTE MAXIMUM RATINGS (T_A = +25°C, unless otherwise specified)

Paran	neter	Symbol	Ratings	Unit
Input Voltage	(IN-GND)	V _{in}	-0.5 to +0.5	V
Input Current	(IN-GND)	I _{in}	0.5	mA
Output Voltage	(OUT-GND)	V _{out}	0 to +5	V
Output Current	(OUT-GND)	l _{out}	0.5	mA
Channel Temper	ature	T _{ch}	130	°C
Operating Ambie	nt Temperature	T _A	-40 to +85	°C
Storage Tempera	ature	T _{stg}	-65 to +150	°C

RECOMMENDED OPERATING RANGE (TA = +25°C)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage Note	V_{DD}	1.0	1.5	5.0	V

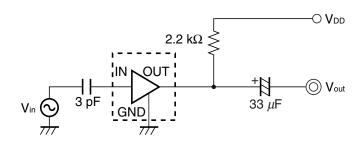
Note: $R_L = 2.2 \text{ k}\Omega$

ELECTRICAL CHARACTERISTICS ($T_A = +25$ °C, $R_L = 2.2 \text{ k}\Omega$, unless otherwise specified)

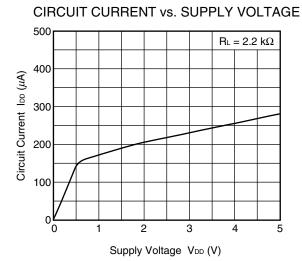
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Circuit Current	I _{DD}	V _{DD} = 1.5V, V _{in} = 0 V	140	190	250	μΑ
Input Capacitance	C _{input}	V _{DD} = 1.5 V, f = 1 MHz	-	1.5	_	pF
Voltage Gain	G _V	V_{DD} = 1.5 V, V_{in} = 10 mVrms, C_{in} = 3 pF, f = 1 kHz, see TEST_CIRCUIT	4.5	5.7	7.0	dB
Reduced Voltage Characteristics	⊿G _{VV}	V_{DD} = 1.5 \rightarrow 1.0 V, V_{in} = 10 mVrms, C_{in} = 3 pF, f = 1 kHz, see TEST CIRCUIT	_	0.7	_	dB
Frequency Characteristics	∆G _{Vf}	V_{DD} = 1.5 V, V_{in} = 10 mVrms, C_{in} = 3 pF, f = 1 kHz \rightarrow 110 Hz, see TEST CIRCUIT	_	0	_	dB
Output Noise Voltage	N _V	V_{DD} = 1.5 V, V_{in} = 0 Vrms, C_{in} = 3 pF, A-Curve, see TEST CIRCUIT	_	-101	_	dBV
Total Harmonic Distortion	THD	V_{DD} = 1.5 V, V_{out} = 50 mVrms, C_{in} = 3 pF, f = 1 kHz, see TEST CIRCUIT	-	0.7	_	%

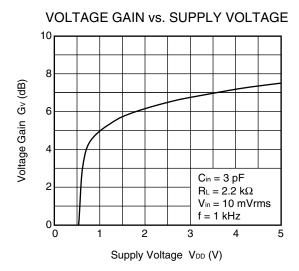
TEST CIRCUIT

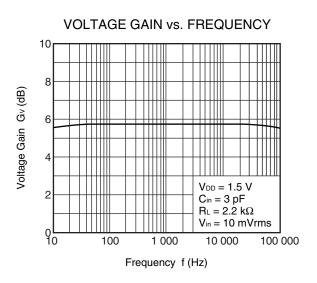
Voltage Gain, Frequency Characteristics, Output Noise Voltage, Total Harmonic Distortion

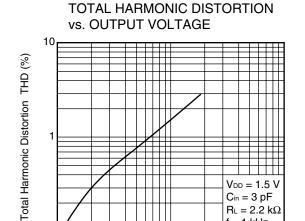


TYPICAL CHARACTERISTICS (T_A = +25°C, unless otherwise specified)











OUTPUT NOISE VOLTAGE

1<u>ا.</u> 10

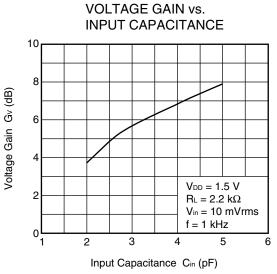
 $V_{DD} = 1.5 V$

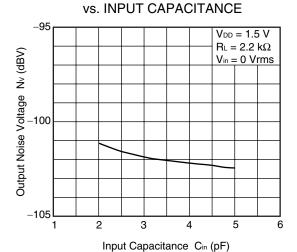
 $R_L = 2.2 \text{ k}\Omega$

1 000

 $C_{in} = 3 pF$

f = 1 kHz

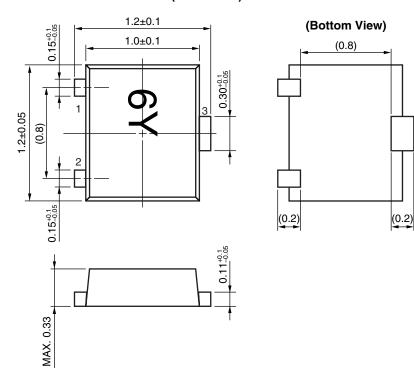




Remark The graphs indicate nominal characteristics.

PACKAGE DIMENSIONS

3-PIN THIN-TYPE LEAD-LESS MINIMOLD (UNIT: mm)



PIN CONNECTIONS

- 1. OUT
- 2. IN
- 3. GND

Remark (): Reference value

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature)	: 260°C or below	IR260
	Time at peak temperature	: 10 seconds or less	
	Time at temperature of 220°C or higher	: 60 seconds or less	
	Preheating time at 120 to 180°C	: 120±30 seconds	
	Maximum number of reflow processes	: 3 times	
	Maximum chlorine content of rosin flux (% mass)	: 0.2%(Wt.) or below	
Wave Soldering	Peak temperature (molten solder temperature)	: 260°C or below	WS260
	Time at peak temperature	: 10 seconds or less	
	Preheating temperature (package surface temperature)	: 120°C or below	
	Maximum number of flow processes	: 1 time	
	Maximum chlorine content of rosin flux (% mass)	: 0.2%(Wt.) or below	
Partial Heating	Peak temperature (terminal temperature)	: 350°C or below	HS350
	Soldering time (per side of device)	: 3 seconds or less	
	Maximum chlorine content of rosin flux (% mass)	: 0.2%(Wt.) or below	

CAUTION

Do not use different soldering methods together (except for partial heating).

Revision History

μ PD5758T6J Data Sheet

		Description		
Rev.	Date	Page	Summary	
1.00	Apr 18, 2011	_	First edition issued	

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