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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# **BIPOLAR ANALOG INTEGRATED CIRCUIT**

# $\mu$ PC3219GV

# **GENERAL PURPOSE 5 V 100 MHz AGC AMPLIFIER**

#### **DESCRIPTION**

The  $\mu$ PC3219GV is a silicon monolithic IC designed for use as AGC amplifier for digital CATV, cable modem systems. This IC consists of gain control amplifier and video amplifier.

The package is 8-pin SSOP suitable for surface mount.

This IC is manufactured using our 10 GHz  $f\tau$  NESAT II AL silicon bipolar process. This process uses silicon nitride passivation film. This material can protect chip surface from external pollution and prevent corrosion/migration. Thus, this IC has excellent performance, uniformity and reliability.

#### **FEATURES**

Low distortion
 : IM<sub>3</sub> = 58 dBc TYP. @ single-ended output, V<sub>out</sub> = 0.7 V<sub>p-p</sub>/tone

• Wide AGC dynamic range : GCR = 42.5 dB TYP.

On-chip video amplifier : Vout = 1.0 Vp-p TYP. @ single-ended output

Supply voltage : Vcc = 5.0 V TYP.
 Packaged in 8-pin SSOP suitable for surface mounting

#### **APPLICATION**

• Digital CATV/Cable modem receivers

#### ORDERING INFORMATION

Part Number	Package	Marking	Supplying Form
μPC3219GV-E1	8-pin plastic SSOP (4.45 mm (175))	3219	Embossed tape 8 mm wide
			Pin 1 indicates pull-out direction of tape
			Qty 1 kpcs/reel

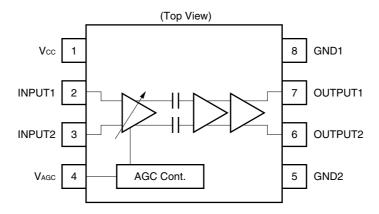
**Remark** To order evaluation samples, contact your nearby sales office.

Part number for sample order:  $\mu$ PC3219GV

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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# INTERNAL BLOCK DIAGRAM AND PIN CONNECTIONS



# PRODUCT LINE-UP OF 5 V AGC AMPLIFIER

Part Number	Icc (mA)	G <sub>мах</sub> (dB)	G <sub>MIN</sub> (dB)	GCR (dB)	NF (dB)	IM <sub>3</sub> (dBc) Note	Package
μPC3217GV	23	53	0	53	6.5	50	8-pin SSOP (4.45 mm (175))
μPC3218GV	23	63	10	53	3.5	50	
μPC3219GV	36.5	42.5	0	42.5	9.0	58	

 $\textbf{Note} \hspace{0.2cm} f_1 = 44 \hspace{0.1cm} MHz, \hspace{0.1cm} f_2 = 45 \hspace{0.1cm} MHz, \hspace{0.1cm} V_{out} = 0.7 \hspace{0.1cm} V_{p\text{-p}}/tone, \hspace{0.1cm} single\text{-ended output}$ 



#### **PIN EXPLANATIONS**

Pin No.	Pin Name	Applied Voltage (V)	Pin Voltage (V) <sup>Note</sup>	Function and Application	Internal Equivalent Circuit
1	Vcc	4.5 to 5.5	-	Power supply pin.  This pin should be externally equipped with bypass capacitor to minimize ground impedance.	
2	INPUT1	-	1.45	Signal input pins to AGC amplifier.  This pin should be coupled with capacitor for DC cut.	AGC
3	INPUT2	I	1.45		2 5 3
4	Vago	0 to Vcc		Gain control pin. This pin's bias govern the AGC output level. Minimum Gain at V <sub>AGC</sub> < 0.5 V Maximum Gain at V <sub>AGC</sub> > 4.5 V Recommended to use AGC voltage with externally resister (example:100 k $\Omega$ ).	AGC Amp.
5	GND2	0	-	Ground pin.  This pin should be connected to system ground with minimum inductance.  Ground pattern on the board should be formed as wide as possible.	
6	OUTPUT2	-	2.2	Signal output pins of video amplifier.  This pin should be coupled with capacitor for DC cut.	
7	OUTPUT1	_	2.2		
8	GND1	0	-	Ground pin.  This pin should be connected to system ground with minimum inductance.  Ground pattern on the board should be formed as wide as possible.  All ground pins must be connected together with wide ground pattern to decrease impedance difference.	

**Note** Pin voltage is measured at Vcc = 5.0 V.



#### **ABSOLUTE MAXIMUM RATINGS**

Parameter	Symbol	Test Conditions	Ratings	Unit
Supply Voltage	Vcc	T <sub>A</sub> = +25°C	6.0	V
Power Dissipation	Po	$T_A = +85^{\circ}C$ Note	250	mW
Operating Ambient Temperature	TA		-40 to +85	°C
Storage Temperature	Tstg		-55 to +150	°C

**Note** Mounted on double-sided copper-clad  $50 \times 50 \times 1.6$  mm epoxy glass PWB

# RECOMMENDED OPERATING RANGE

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Supply Voltage	Vcc		4.5	5.0	5.5	V
Operating Ambient Temperature	TA	Vcc = 4.5 to 5.5 V	-40	+25	+85	°C
Gain Control Voltage Range	Vagc		0	-	Vcc	V
Operating Frequency Range	fвw		10	45	100	MHz

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# **ELECTRICAL CHARACTERISTICS**

(TA = +25°C, Vcc = 5 V, f = 45 MHz, Zs = 50  $\Omega$ , ZL = 250  $\Omega$ , single-ended output)

Parameter	Symbol	Test Conditions		MIN.	TYP.	MAX.	Unit	
DC Characteristics								
Circuit Current	Icc	No input signal	Note 1	27.5	36.5	43.5	mA	
AGC Voltage High Level	VAGC (H)	@ Maximum gain	Note 1	4.5	1	Vcc	٧	
AGC Voltage Low Level	VAGC (L)	@ Minimum gain	Note 1	0	_	0.5	V	
RF Characteristics	RF Characteristics							
Maximum Voltage Gain	Gмах	Vagc = 4.5 V, Pin = -40 dBm	Note 1	39	42.5	45	dB	
Minimum Voltage Gain	GMIN	Vagc = 0.5 V, Pin = -20 dBm	Note 1	-4	0	4	dB	
Gain Control Range	GCR	V <sub>AGC</sub> = 0.5 to 4.5 V	Note 1	35	42.5	1	dB	
Output Voltage	Vout	$P_{in} = -38 \text{ to } -13 \text{ dBm}$	Note 1	-	1.0	1	$V_{p-p}$	
Maximum Output Voltage	Voclip	V <sub>AGC</sub> = 4.5 V @ Maximum gair	Note 1	2.5	3.4	1	$V_{p-p}$	
Noise Figure	NF	V <sub>AGC</sub> = 4.5 V @ Maximum gair	Note 2	-	9.0	10.5	dB	

Notes 1. By measurement circuit 1

2. By measurement circuit 2

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# STANDARD CHARACTERISTICS (Ta = +25°C, Vcc = 5 V, Zs = 50 $\Omega$ )

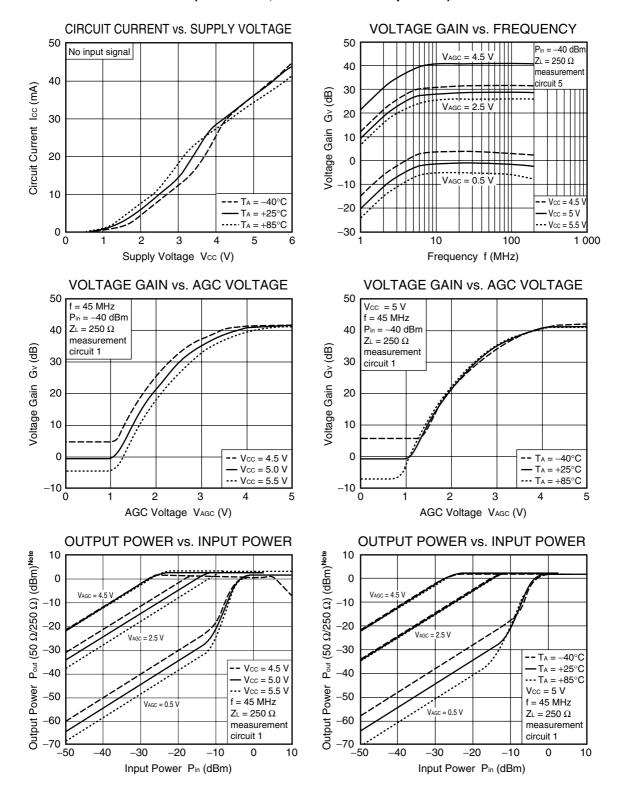
Parameter	Symbol	Test Conditions	Reference Value	Unit
Input Impedance	Zin	V <sub>AGC</sub> = 0.5 V, f = 45 MHz <b>Note 1</b>	1.2 k – j1.5 k	Ω
Output Impedance	Zout	V <sub>AGC</sub> = 0.5 V, f = 45 MHz <b>Note 1</b>	6.0 + j3.2	Ω
3rd Order Input Intercept Point	IIP <sub>3</sub>	$V_{AGC} = 0.5 \ V \ @ Minimum \ gain,$ $f_1 = 44 \ MHz, f_2 = 45 \ MHz,$ $Z_L = 250 \ \Omega \ @ \ single-ended \ output$ $\  \                                $	-1	dBm
3rd Order Intermodulation Distortion 1	IM₃1	$\begin{split} f_1 &= 44 \text{ MHz},  f_2 = 45 \text{ MHz},  Z_L = 250  \Omega, \\ P_{\text{in}} &= -37 \text{ to } -20 \text{ dBm/tone}, \\ V_{\text{out}} &= 1.0 \text{ V}_{\text{P-P}} / \text{tone @ single-ended} \\ \text{output} & \text{\textbf{Note 2}} \end{split}$	52	dBc
3rd Order Intermodulation Distortion 2	IM <sub>3</sub> 2	$\begin{split} &\text{f}_1 = 44 \text{ MHz},  \text{f}_2 = 45 \text{ MHz},  \text{ZL} = 250  \Omega, \\ &\text{P}_{\text{in}} = -40 \text{ to } -23 \text{ dBm/tone}, \\ &\text{V}_{\text{out}} = 0.7  \text{V}_{\text{P-P}} / \text{tone @single-ended} \\ &\text{output} & \text{\textbf{Note 2}} \end{split}$	58	dBc
3rd Order Intermodulation Distortion 3	IM₃3	$\begin{split} f_1 &= 44 \text{ MHz, } f_2 = 45 \text{ MHz, } Z_L = 500 \ \Omega, \\ P_{\text{in}} &= -37 \text{ to } -20 \text{ dBm/tone,} \\ V_{\text{out}} &= 2.0 \text{ V}_{\text{P-P}}/\text{tone @ differential output} \\ &\qquad \qquad \textbf{Note 3} \end{split}$	52	dBc
3rd Order Intermodulation Distortion 4	IM <sub>3</sub> 4	$f_1 = 44 \text{ MHz}, f_2 = 45 \text{ MHz}, Z_L = 500 \ \Omega,$ $P_{in} = -40 \text{ to } -23 \text{ dBm/tone},$ $V_{out} = 1.4 \text{ V}_{P\text{-P/tone}} \text{ @ differential output}$ $\text{Note 3}$	58	dBc
2nd Order Intermodulation Distortion 1	IM <sub>2</sub> 1	$f_1 = 44 \text{ MHz}, f_2 = 45 \text{ MHz}, Z_L = 500 \ \Omega,$ $P_{in} = -37 \text{ to } -22 \text{ dBm/tone},$ $V_{out} = 2.0 \text{ V}_{P\text{-P/tone}} \text{ @ differential output}$ $\text{Note 3}$	45	dBc
2nd Order Intermodulation Distortion 2	IM <sub>2</sub> 2	$\begin{aligned} f_1 &= 44 \text{ MHz},  f_2 = 45 \text{ MHz},  Z_L = 500  \Omega, \\ P_{\text{in}} &= -40 \text{ to } -23 \text{ dBm/tone}, \\ V_{\text{out}} &= 1.4 \text{ V}_{\text{P-P}} / \text{tone @ differential output} \\ &\qquad \qquad \text{Note 3} \end{aligned}$	47	dBc

Notes 1. By measurement circuit 3

2. By measurement circuit 1

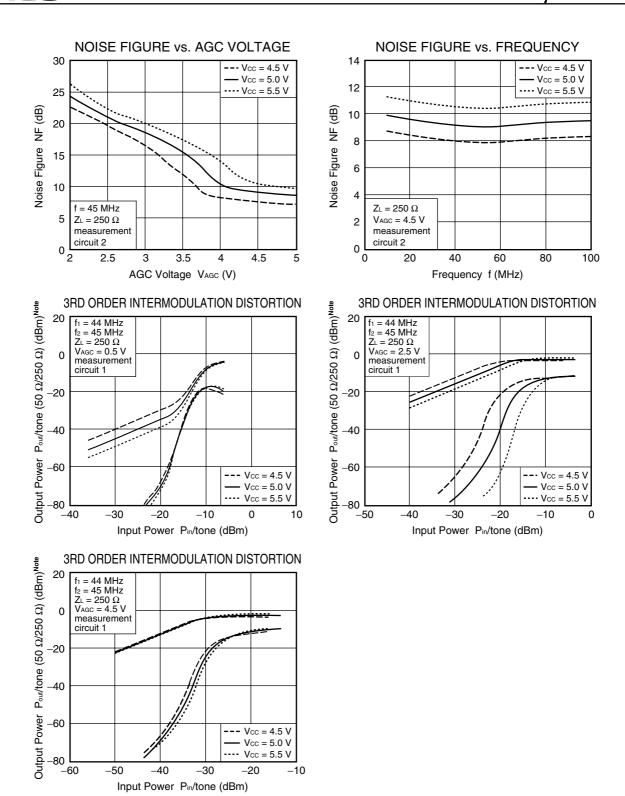
3. By measurement circuit 4

#### TYPICAL CHARACTERISTICS (TA = +25°C, unless otherwise specified)



Note Measurement value with spectrum analyzer.

Remark The graphs indicate nominal characteristics.



**Note** Measurement value with spectrum analyzer.

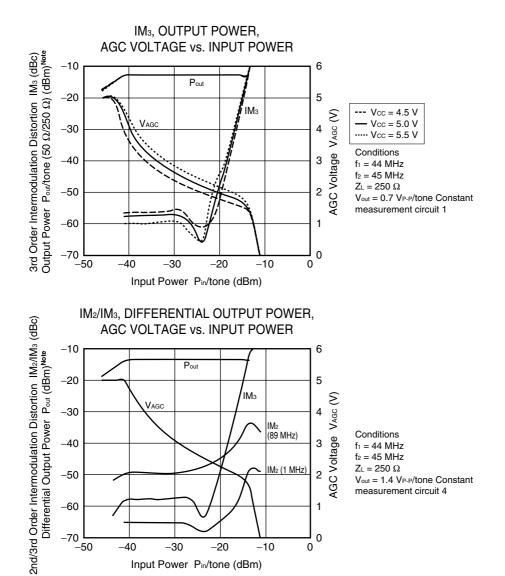
**Remark** The graphs indicate nominal characteristics.

-50

-60

-50

-40



-10

0

Note Measurement value with spectrum analyzer.

Input Power Pin/tone (dBm)

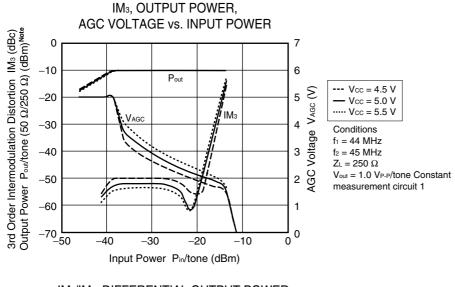
-30

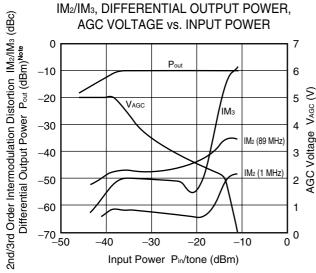
**Remark** The graphs indicate nominal characteristics.

9

f<sub>2</sub> = 45 MHz  $Z_L = 250 \ \Omega$ 

Vout = 1.4 VP-P/tone Constant measurement circuit 4





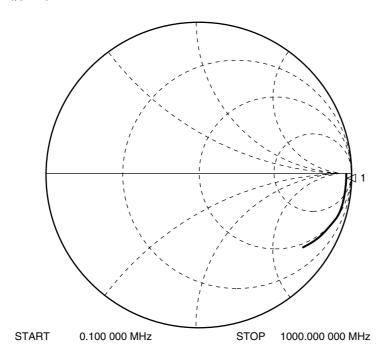
Conditions  $f_1 = 44 \text{ MHz}$   $f_2 = 45 \text{ MHz}$   $Z_L = 250 \Omega$   $V_{out} = 2.0 \text{ V}_{P\text{-P}}/\text{tone Constant}$  measurement circuit 4

Note Measurement value with spectrum analyzer.

**Remark** The graphs indicate nominal characteristics.

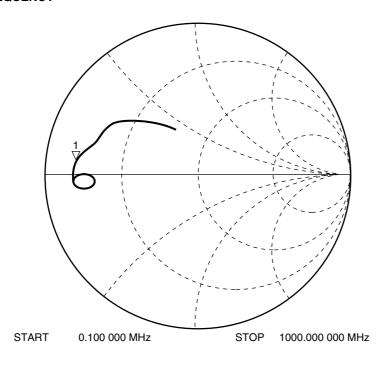
# S-PARAMETERS (Ta = +25°C, Vcc = 5.0 V)

#### S<sub>11</sub>-FREQUENCY



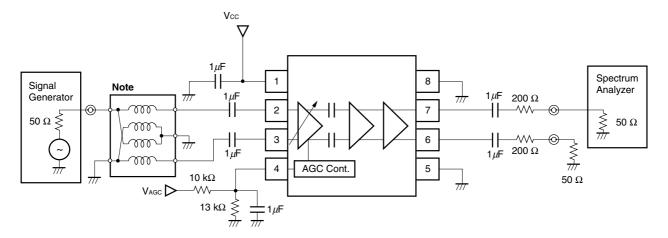
Marker 1 45 MHz  $1.229~k-j~1.522~k\Omega$ 

#### S22-FREQUENCY



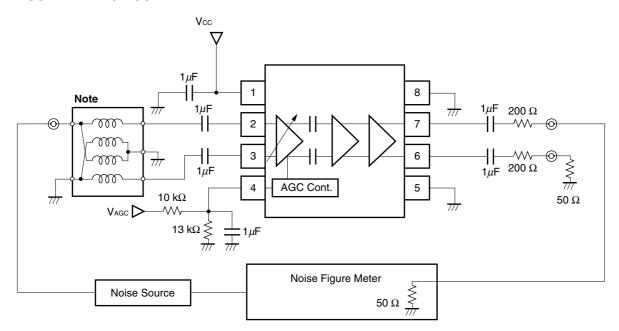
Marker 1 45 MHz  $6.035 + j \ 3.157 \ \Omega$ 

#### **MEASUREMENT CIRCUIT 1**



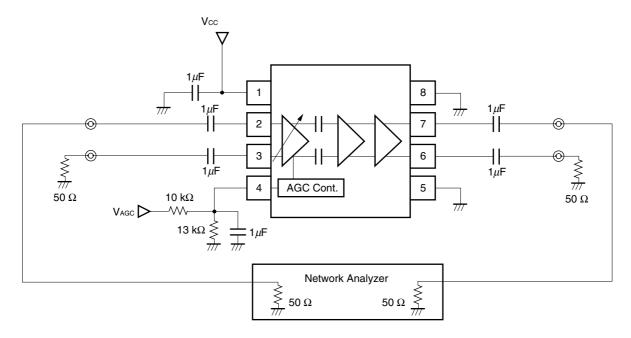
Note Balun Transformer: TOKO 617DB-1010 B4F (Double balanced type)

#### **MEASUREMENT CIRCUIT 2**

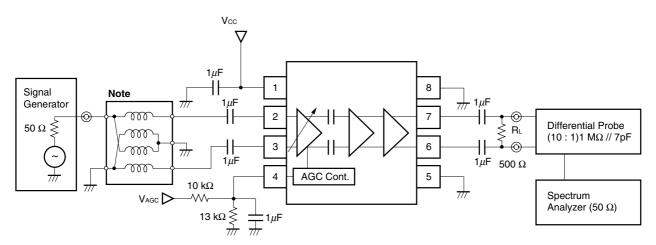


Note Balun Transformer: TOKO 617DB-1010 B4F (Double balanced type)

#### **MEASUREMENT CIRCUIT 3**

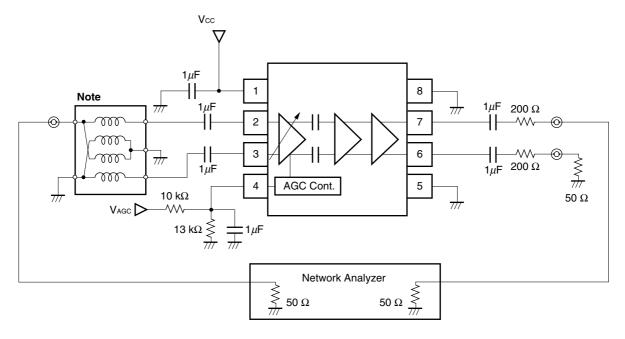


#### **MEASUREMENT CIRCUIT 4**



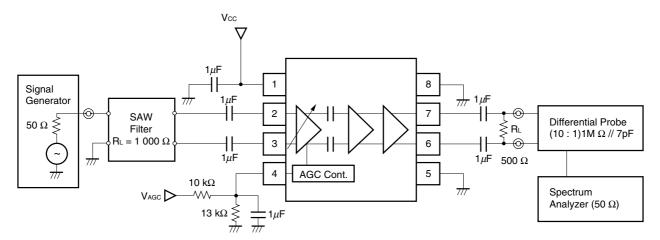
Note Balun Transformer: TOKO 617DB-1010 B4F (Double balanced type)

#### **MEASUREMENT CIRCUIT 5**



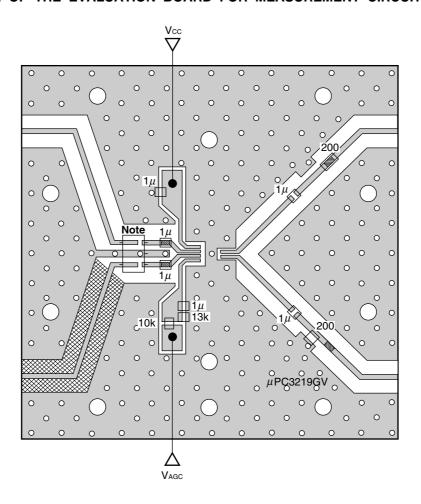
Note Balun Transformer: TOKO 617DB-1010 B4F (Double balanced type)

#### **APPLICATION CIRCUIT EXAMPLE**



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

#### ILLUSTRATION OF THE EVALUATION BOARD FOR MEASUREMENT CIRCUIT 1



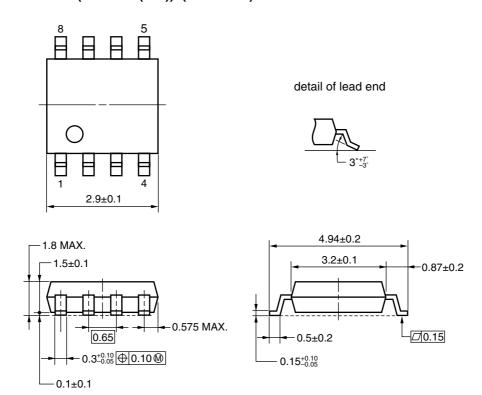
Note Balun Transformer

Remarks

- 1. Back side: GND pattern
- 2. Solder plated on pattern
- 3. oO: Through holes
- 4. represents cutout
- 5. represents short-circuit strip

# **★ PACKAGE DIMENSIONS**

# 8-PIN PLASTIC SSOP (4.45 mm (175)) (UNIT: mm)



#### NOTES ON CORRECT USE

- (1) Observe precautions for handling because of electro-static sensitive devices.
- (2) Form a ground pattern as widely as possible to minimize ground impedance (to prevent undesired oscillation). All the ground pins must be connected together with wide ground pattern to decrease impedance difference.
- (3) The bypass capacitor should be attached to Vcc line.

#### ★ RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
VPS Note	Peak temperature (package surface temperature) Time at temperature of 200°C or higher Preheating time at 120 to 150°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 215°C or below : 25 to 40 seconds : 30 to 60 seconds : 3 times : 0.2%(Wt.) or below	VP215
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (pin temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	H\$350

Note Excluding lead-free products

Caution Do not use different soldering methods together (except for partial heating).

NEC  $\mu$ PC3219GV

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