FDPF18N20FT_G N-Channel UniFETTM FRFET[®] MOSFET

April 2013



FDPF18N20FT G N-Channel UniFET[™] FRFET[®] MOSFET

200 V, 18 A, 140 m

Features

- R_{DS(on)} = 129 mΩ (Typ.) @ V_{GS} = 10 V, I_D = 9 A
- Low Gate Charge (Typ. 20 nC)
- Low C_{rss} (Typ. 24 pF)
- 100% Avalanche Tested
- Improve dv/dt Capability
- · RoHS Compliant

Applications

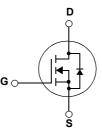
- LCD/LED TV
- Consumer Appliances
- Lighting
- Uninterruptible Power Supply
- AC-DC Power Supply

UniFET™

Description

MOSFET is Fairchild Semiconductor®'s high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. The body diode's reverse recovery performance of UniFET FRFET® has been enhanced by lifetime control. Its tr is less than 100nsec and the reverse dv/dt immunity is 15V/ns while normal planar MOSFETs have over 200nsec and 4.5V/nsec respectively. Therefore, it can remove additional component and improve system reliability in certain applications in which the performance of MOSFET's body diode is significant. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

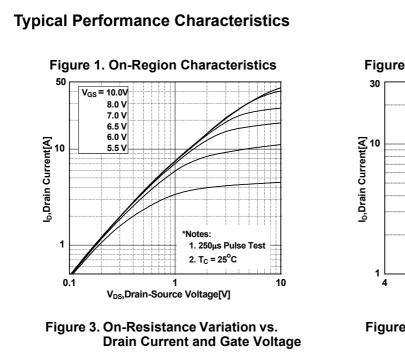
Symbol	Parameter			FDPF18N20FT_G	Unit
V _{DSS}	Drain to Source Voltage			200	V
V _{GSS}	Gate to Source Voltage			±30	V
I _D	DrainCurrent	-Continuous (T _C = 25 ^o C)		18*	
		-Continuous ($T_C = 100^{\circ}C$)		10.8*	— A
I _{DM}	Drain Current	- Pulsed	(Note 1)	72*	А
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		324	mJ	
I _{AR}	Avalanche Current		(Note 1)	18	А
E _{AR}	Repetitive Avalanche Energy		(Note 1)	10	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5	V/ns	
P _D	Dower Dissinction	(T _C = 25 ^o C)		35	W
	Power Dissipation	- Derate above 25°C		0.27	W/ºC
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C
TL	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			300	°C

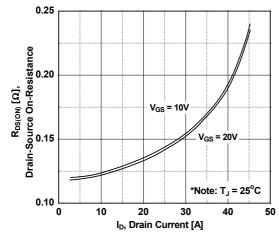
Thermal Characteristics

Symbol	Parameter	FDPF18N20FT_G	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	3.6	
$R_{\theta CS}$	Thermal Resistance, Case to Sink, Typ.	0.5	°C/W
$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

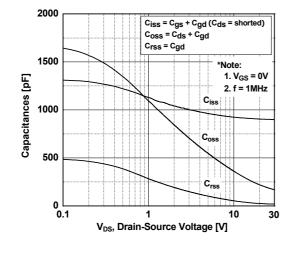
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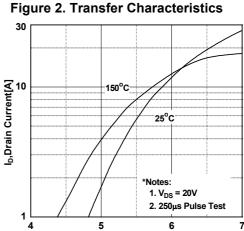
	kingDeviceDFTFDPF18N20F_G	Package TO-220F	Eco Status Green/RoHS	Reel Size	Tape Width -		Quantity 50	
	ild's definition of "green"Eco S	·	<u>http://www.fairchildsem</u> i	.com/company/green/rc	hs_green.	<u>html</u>		
Symbol	Parameter Test Conditions		Min.	Тур.	Max.	Unit		
Off Charac	teristics							
BV _{DSS}	Drain to Source Breakd	own Voltage	I _D = 250μA, V _{GS}	= 0V, T _J = 25 ^o C	200	-	-	V
ΔBV_{DSS} ΔT_J		eakdown Voltage Temperature $I_{D} = 250 \mu A$. Referenced to $25^{\circ}C$		-	0.2	-	V/ºC	
 	Zero Gate Voltago Drain Current			V _{DS} = 200V, V _{GS} = 0V		-	10	μA
IDSS	Zero Gate Voltage Drain Current		V _{DS} = 160V, T _C		I	-	100	μΛ
I _{GSS}	Gate to Body Leakage (Current	V_{GS} = ±30V, V_{DS}	_S = 0V	-	-	±100	nA
On Charac	teristics							
	Gate Threshold Voltage		$V_{00} = V_{00} I_{-} =$	250uA	3.0	-	5.0	V
V _{GS(th)} R _{DS(on)}	Static Drain to Source C			$V_{GS} = V_{DS}, I_D = 250 \mu A$ $V_{GS} = 10V, I_D = 9A$		0.12	0.14	Ω
9FS	Forward Transconducta		00 5	$V_{\rm DS} = 20V, I_{\rm D} = 9A$ (Note 4)		13.6	-	S
				(,				_
•	haracteristics							
C _{iss}	Input Capacitance			= 0)/	-	885	1180	pF
C _{oss}	Output Capacitance		f = 1MHz	- 0 v	-	200	270	pF
C _{rss}	Reverse Transfer Capac					24	35	pF
Q _{g(tot)}	Total Gate Charge at 10			- 10 A	-	20	26	nC
Q _{gs}	Gate to Source Gate Ch		V _{DS} = 160V, I _D : V _{GS} = 10V	= 10A	-	5	-	nC
Q _{gd}	Gate to Drain "Miller" Ch	harge	63	(Note 4, 5)	-	9	-	nC
Switching	Characteristics							
t _{d(on)}	Turn-On Delay Time				-	16	40	ns
t _r	Turn-On Rise Time		V _{DD} = 100V, I _D =	V_{DD} = 100V, I _D = 18A R _G = 25Ω		50	110	ns
t _{d(off)}	Turn-Off Delay Time					50	110	ns
t _f	Turn-Off Fall Time			(Note 4, 5)	-	40	90	ns
				I		l.		
	rce Diode Character						1	
ls	Maximum Continuous Drain to Source Diode Forward Current		t	-	-	18	A	
I _{SM}		um Pulsed Drain to Source Diode Forward Current		-	-	72	A	
V _{SD}	Drain to Source Diode F	0	$V_{GS} = 0V, I_{SD} =$		-	-	1.5	V
t _{rr}	Reverse Recovery Time		$V_{GS} = 0V, I_{SD} = 18A$		-	80	-	ns
Q _{rr}	Reverse Recovery Char	ye	$dI_F/dt = 100A/\mu s$ (Note 4)		-	240	-	nC



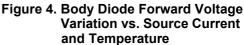








V_{GS},Gate-Source Voltage[V]



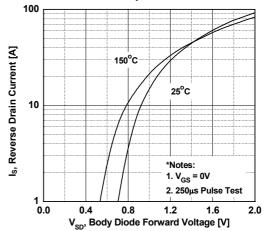
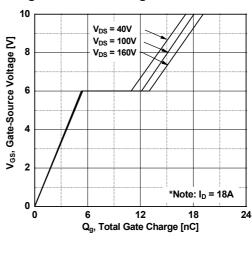
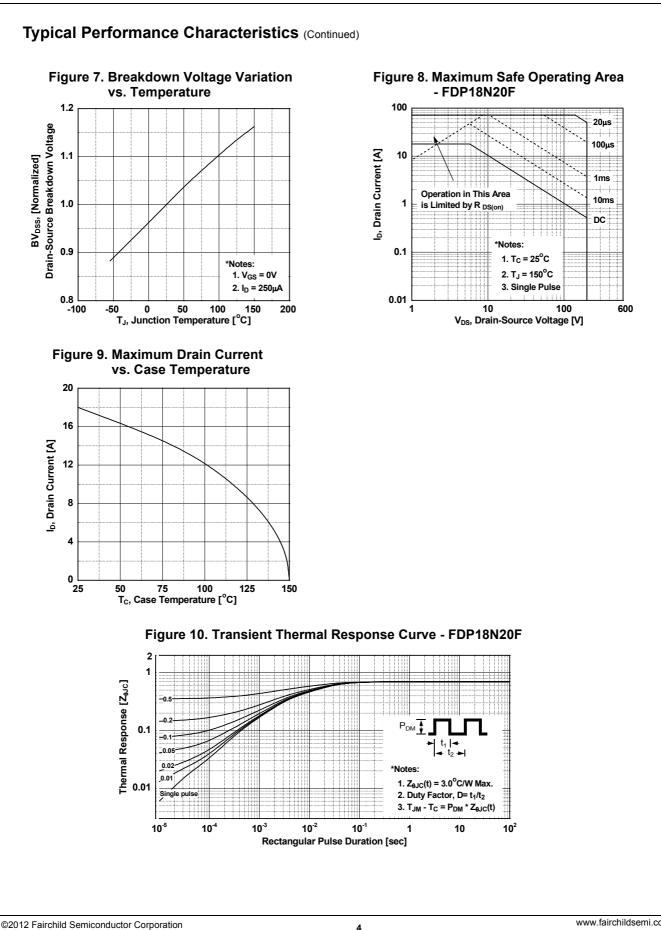


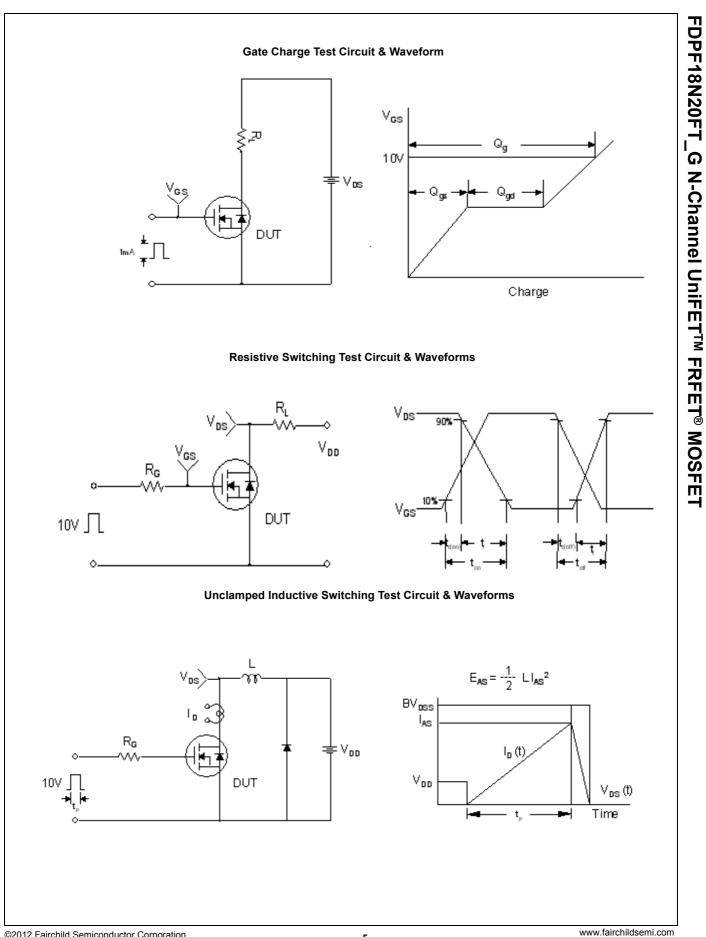
Figure 6. Gate Charge Characteristics



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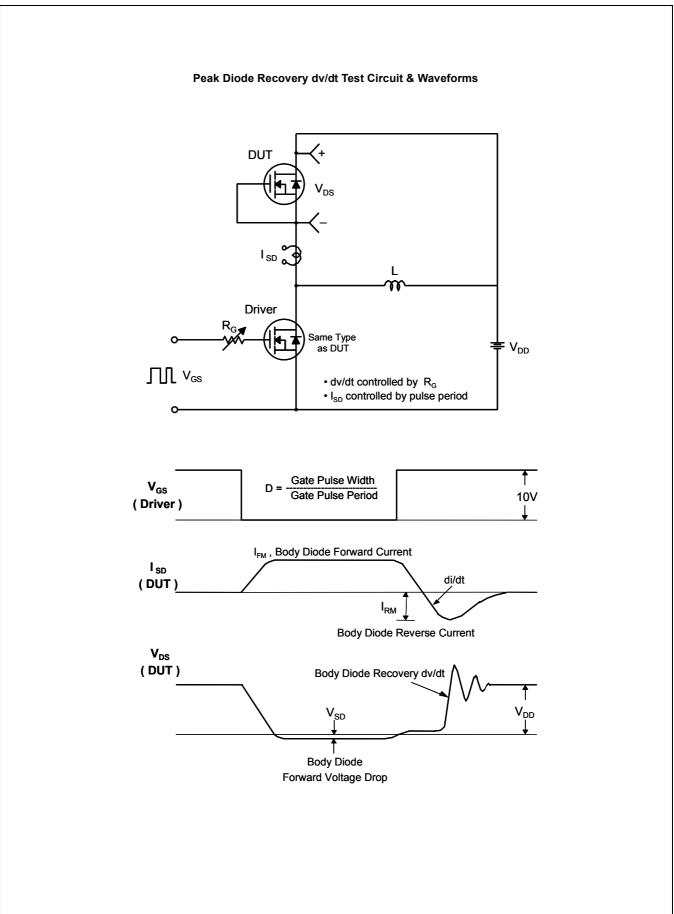


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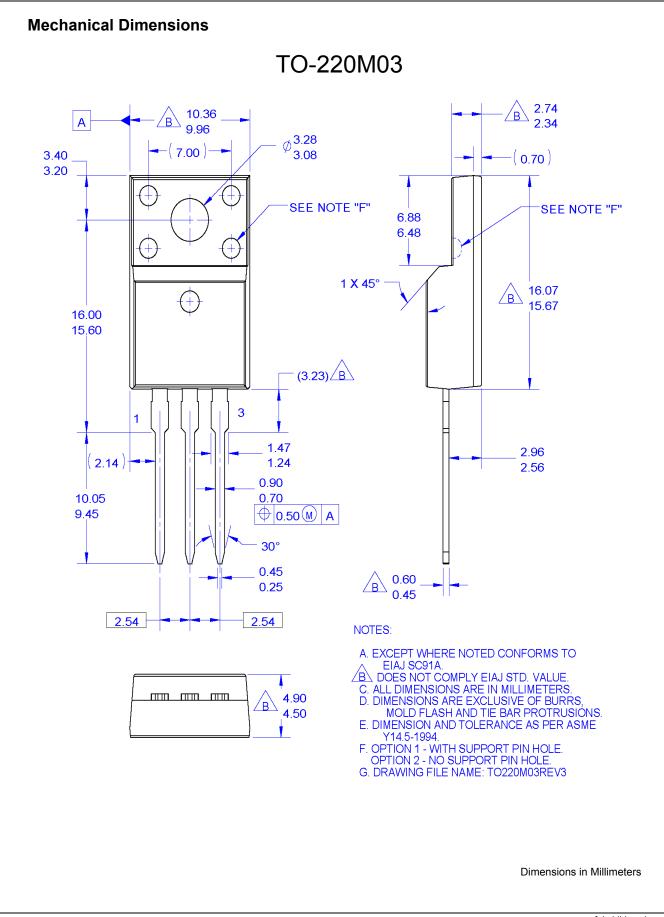


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