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Silicon P-Channel MOS FET



ADE-208-138 (Z) 1st. Edition Aug. 1993

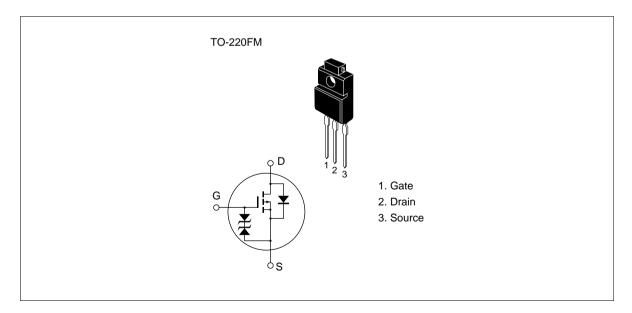
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device can be driven from 5 V source
- Suitable for switching regulator, DC-DC converter

Outline



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-120	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	-6	А
Drain peak current	I*1 D(pulse)	-12	А
Body to drain diode reverse drain current	I _{DR}	-6	А
Channel dissipation	Pch*2	20	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	٥C

Notes: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$

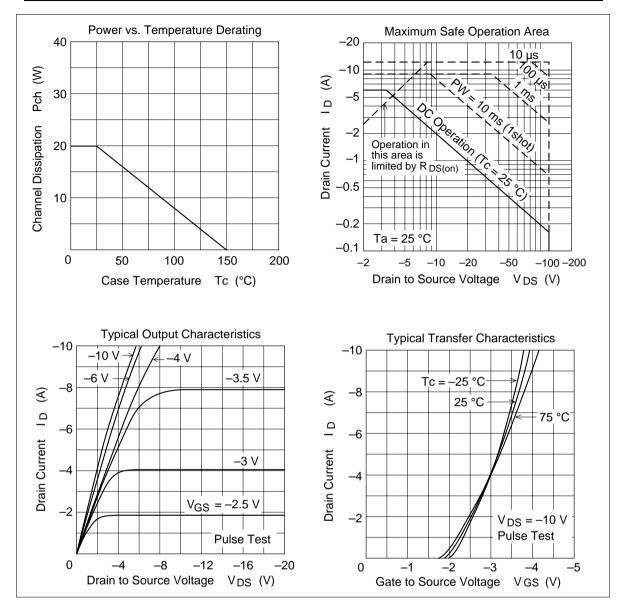
2. Value at $T_c = 25^{\circ}C$

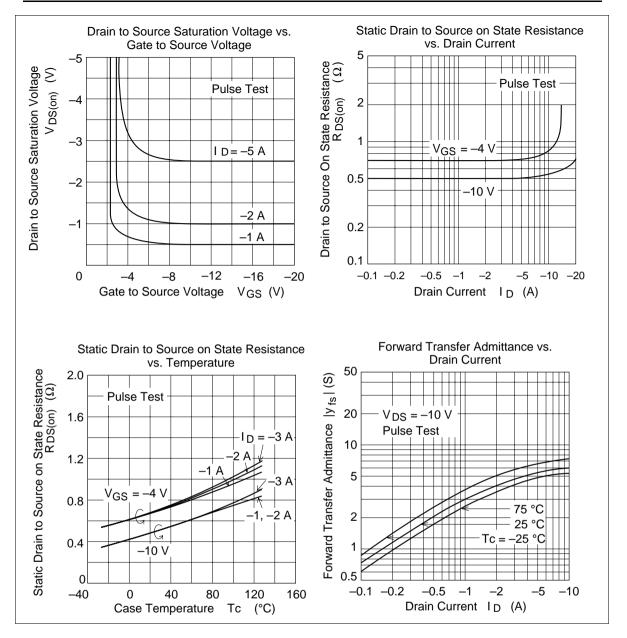
Electrical Characteristics (Ta = 25°C)

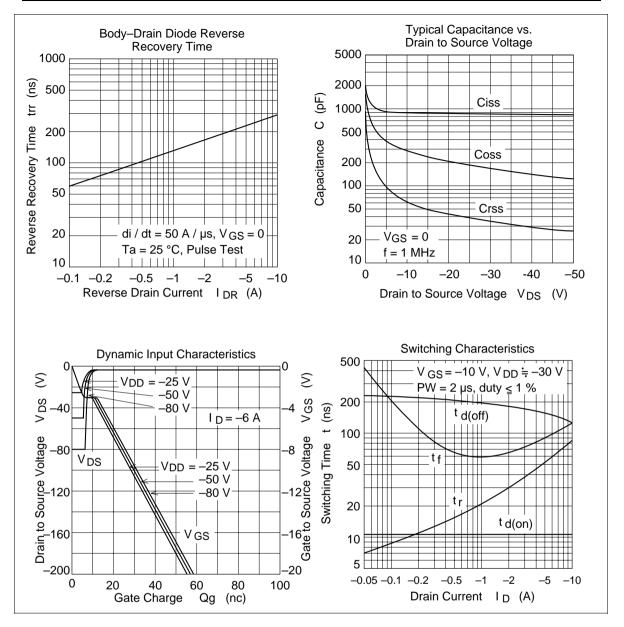
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	-120	_	_	V	$I_{\rm D} = -10$ mA, $V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	—	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	—	_	±10	μA	$V_{GS} = \pm 16 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	-250	μA	$V_{\rm DS} = -100 \text{ V}, \text{ V}_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	_	-2.0	V	$I_{\rm D} = -1 \text{ mA}, V_{\rm DS} = -10 \text{ V}$
Static drain to source on state	$R_{DS(on)}$	—	0.5	0.7	Ω	$I_{\rm D} = -4$ A, $V_{\rm GS} = -10$ V ^{*1}
resistance		_	0.7	0.9	Ω	$I_{\rm D} = -4$ A, $V_{\rm GS} = -4$ V ^{*1}
Forward transfer admittance	y _{fs}	3.0	5.0		S	$I_{\rm D} = -4$ A, $V_{\rm DS} = -10$ V ^{*1}
Input capacitance	Ciss	_	900		pF	$V_{\rm DS} = -10 \ V, \ V_{\rm GS} = 0,$
Output capacitance	Coss	_	265	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	65	_	pF	
Turn-on delay time	t _{d(on)}	—	11		ns	$I_{\rm D} = -4$ A, $V_{\rm GS} = -10$ V,
Rise time	t,	—	45	_	ns	$R_{L} = 7.5 \Omega$
Turn-off delay time	$t_{d(off)}$	_	170	_	ns	
Fall time	t _f	—	80	—	ns	
Body to drain diode forward voltage	V_{DF}	—	-1.2	—	V	$I_{F} = -6 A, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	—	240	—	ns	$I_{F} = -6 \text{ A}, V_{GS} = 0,$ $di_{F}/dt = 50 \text{ A}/\mu \text{s}$
Noto: 1 Dulas test						

Note: 1. Pulse test

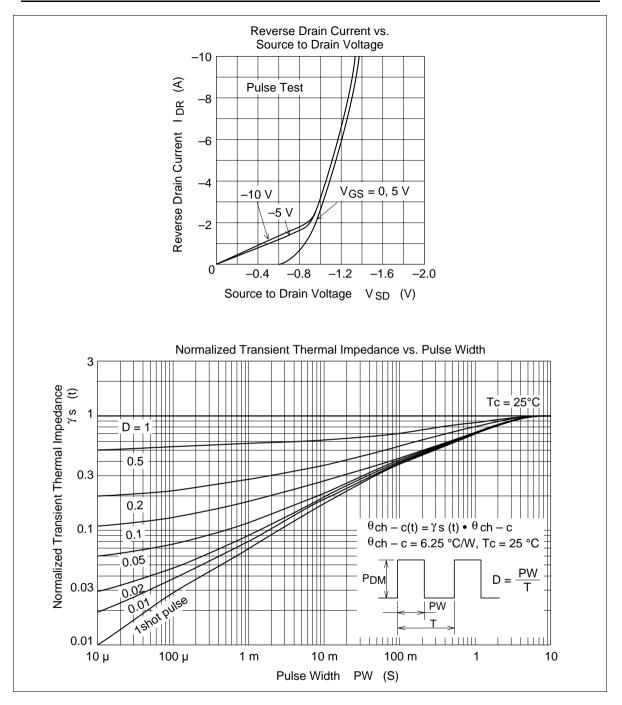




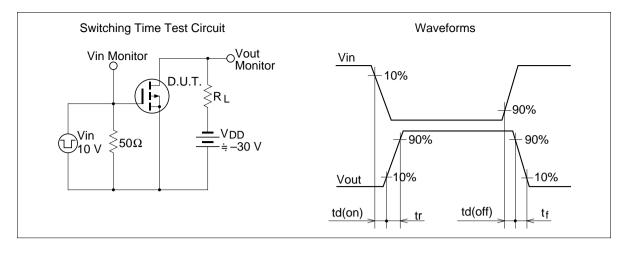




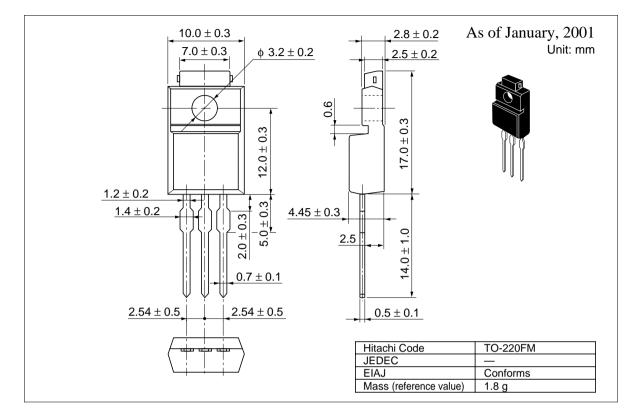




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Package Dimensions



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