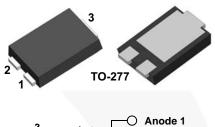


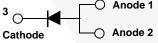
September 2015

FSV2060L 20 A, 60 V Very Low VF Schottky Rectifier

Features

- Very Low Forward Voltage Drop
- Low Thermal Resistance
- Very Low Profile: Typical Height of 1.1 mm
- RoHS Compliant
- · Green Molding Compound as per IEC61249 Standard
- Lead Free in Compliance with EU RoHS 2011/65/EU Directive
- Qualified with Reflow (J-STD-020) and Solder Temperature 260°C Classification





Ordering Information

Part Number	Top Mark	Package	Packing Method
FSV2060L	FSV2060L	TO-277 3L	Tape and Reel

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{RRM}	Peak Repetitive Reverse Voltage	60	V
V_{RWM}	Working Peak Reverse Voltage	60	V
V_{RMS}	RMS Reverse Voltage	42	V
V_{R}	DC Blocking Voltage	60	V
I _{F(AV)}	Average Rectified Peak Forward Surge Current	20	Α
I _{FSM}	Non-Repetitive Peak Forward Surge Current	320	Α
TJ	Operating Junction Temperature Range	-55 to +150	°C
T _{STG}	Storage Temperature Range	-55 to +150	°C

Thermal Characteristics(1)

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Minimum Land Pattern	Maximum Land Pattern	Unit	
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance	100	40	°C/W	
ΨJL	Junction-to-Lead Thermal Characteristics, Thermocouple Soldered to Anode	15	12	°C/W	
	Junction-to-Lead Thermal Characteristics, Thermocouple Soldered to Cathode	6	5		

Note:

1. The thermal resistances ($R_{\theta JA} \& \psi_{JL}$) are characterized with device mounted on the following FR4 printed circuit boards, as shown in Figure 1 and Figure 2. PCB size: 76.2 x 114.3 mm. Minimum land pattern size: 4.9 x 4.8 mm (big pattern, x1), 1.4 x 1.52 mm (small pattern, x2). Maximum land pattern size: 30 x 30 mm (pattern, x2). Force line trace size = 55 mils, sense line trace size = 4 mils.



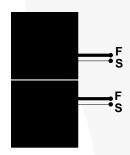


Figure 1. Minimum Land Pattern of 2 oz Copper

Figure 2. Maximum Land Pattern of 2 oz Copper

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV_R	Breakdown Voltage	I _R = 0.5 mA	60	66		V
V _F	Forward Voltage Drop	I _F = 15 A		477		mV
		I _F = 20 A		522	600	
I _R	Reverse Current	V _R = 60 V		78	320	μΑ
CJ	Junction Capacitance	V _R = 4 V, f = 1 MHz		771	/	pF

Typical Performance Characteristics

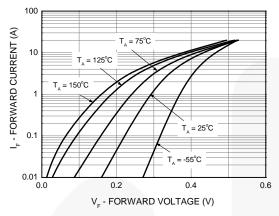


Figure 3. Typical Forward Characteristics

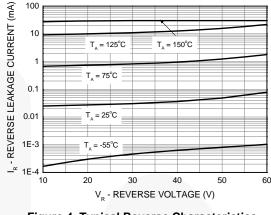


Figure 4. Typical Reverse Characteristics

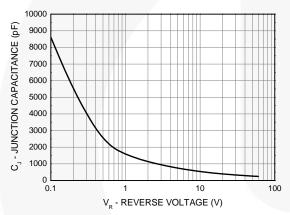


Figure 5. Typical Junction Capacitance

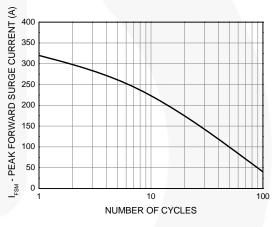


Figure 6. Maximum Non-Repetitive Peak Forward Surge Current

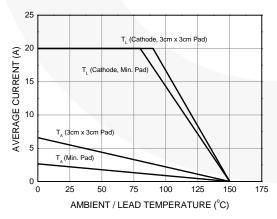
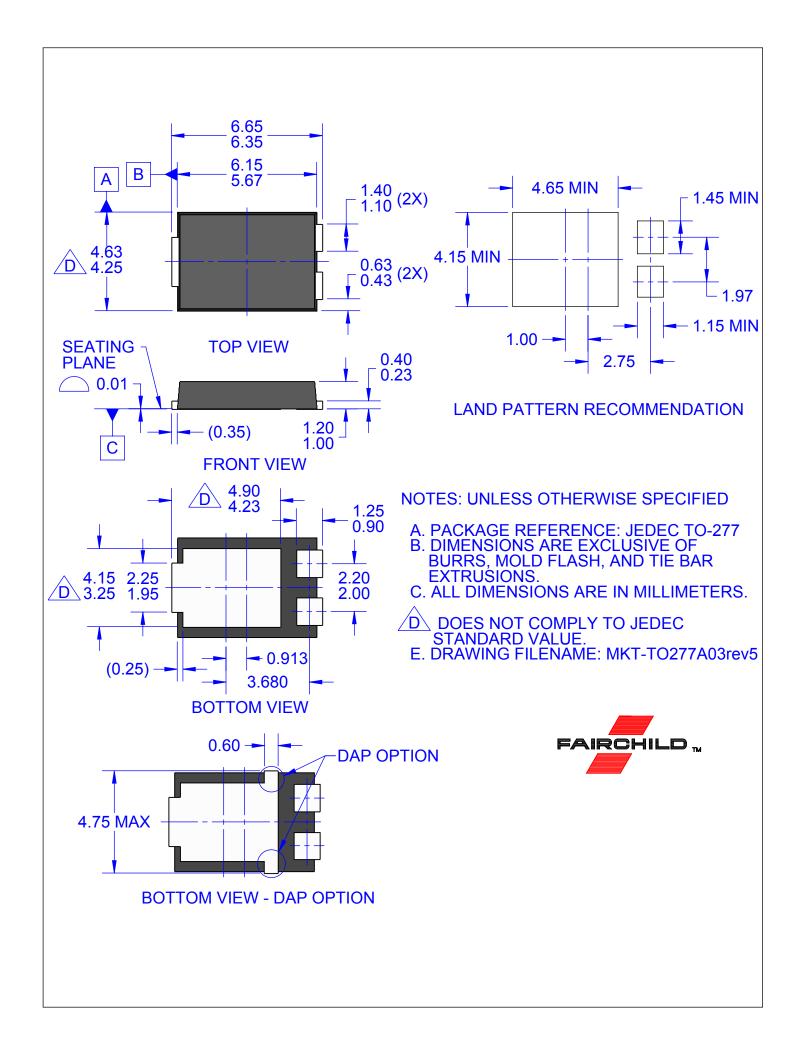


Figure 7. Forward Current Derating Curve







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Definition of Terms

Definition of Terms				
Datasheet Identification	Product Status	Definition		
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.		

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