

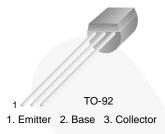
November 2014



SS8050 NPN Epitaxial Silicon Transistor

Features

- 2 W Output Amplifier of Portable Radios in Class B Push-pull Operation.
- Complimentary to SS8550
- Collector Current: I_C = 1.5 A



Ordering Information

Part Number	Top Mark	Package	Packing Method	
SS8050BBU	S8050	TO-92 3L	Bulk	
SS8050CBU	S8050	TO-92 3L	Bulk	
SS8050CTA	S8050	TO-92 3L	Ammo	
SS8050DBU	S8050	TO-92 3L	Bulk	
SS8050DTA	S8050	TO-92 3L	Ammo	

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	40	V
V _{CEO}	Collector-Emitter Voltage	25	V
V _{EBO}	Emitter-Base Voltage	6	V
۱ _C	Collector Current	1.5	А
ТJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-65 to 150	°C

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Thermal Characteristics⁽¹⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Value	Unit
р	Power Dissipation	1	W
PD	Derate Above 25°C	8	mW/°C
R _{θJA}	Thermal Resistance, Junction-to-Ambient	125	°C/W

Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

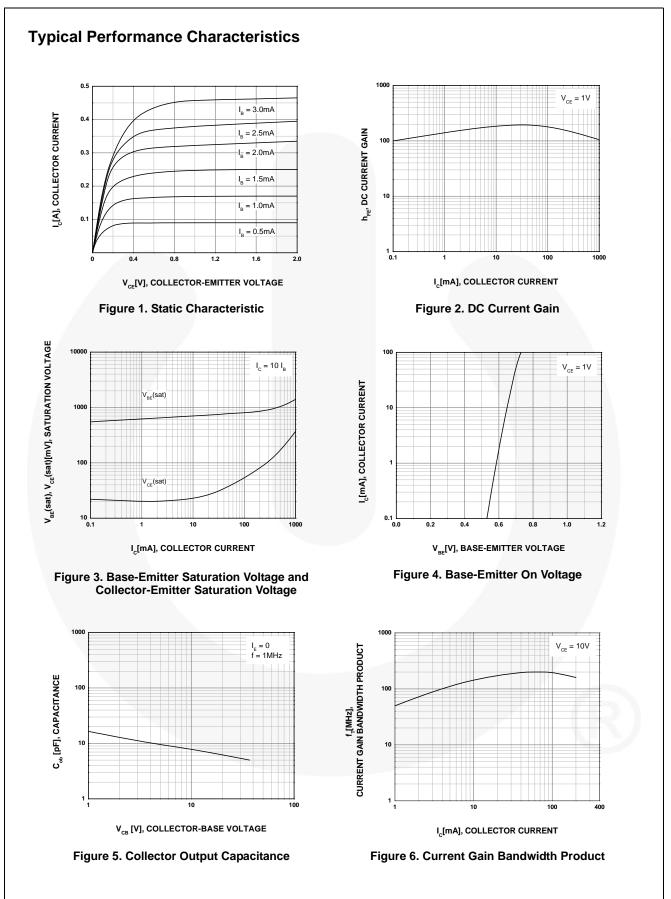
Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

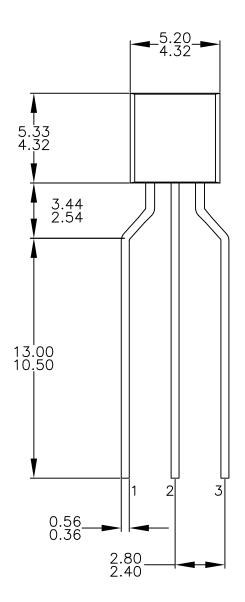
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{C} = 100 \ \mu A, I_{E} = 0$	40			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 2 {\rm mA}, I_{\rm B} = 0$	25			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_{E} = 100 \ \mu A, \ I_{C} = 0$	6			V
I _{CBO}	Collector Cut-Off Current	$V_{CB} = 35 \text{ V}, \text{ I}_{E} = 0$			100	nA
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = 6 V, I_{C} = 0$			100	nA
h _{FE1}		$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA}$	45			
h _{FE2}	DC Current Gain	$V_{CE} = 1 \text{ V}, I_{C} = 100 \text{ mA}$	85		300	
h _{FE3}		$V_{CE} = 1 \text{ V}, I_{C} = 800 \text{ mA}$	40			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 800 mA, I _B = 80 mA			0.5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 800 mA, I _B = 80 mA			1.2	V
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = 1 \text{ V}, I_{C} = 10 \text{ mA}$			1	V
C _{ob}	Output Capacitance	V _{CB} = 10 V, I _E = 0, f = 1 MHz		9.0		pF
f _T	Current Gain Bandwidth Product	$V_{CE} = 10 \text{ V}, I_{C} = 50 \text{ mA}$	100			MHz

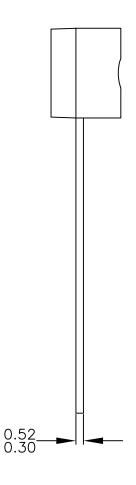
h_{FE} Classification

Classification	В	С	D
h _{FE2}	85 ~ 160	120 ~ 200	160 ~ 300



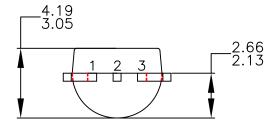
SS8050 — NPN Epitaxial Silicon Transistor

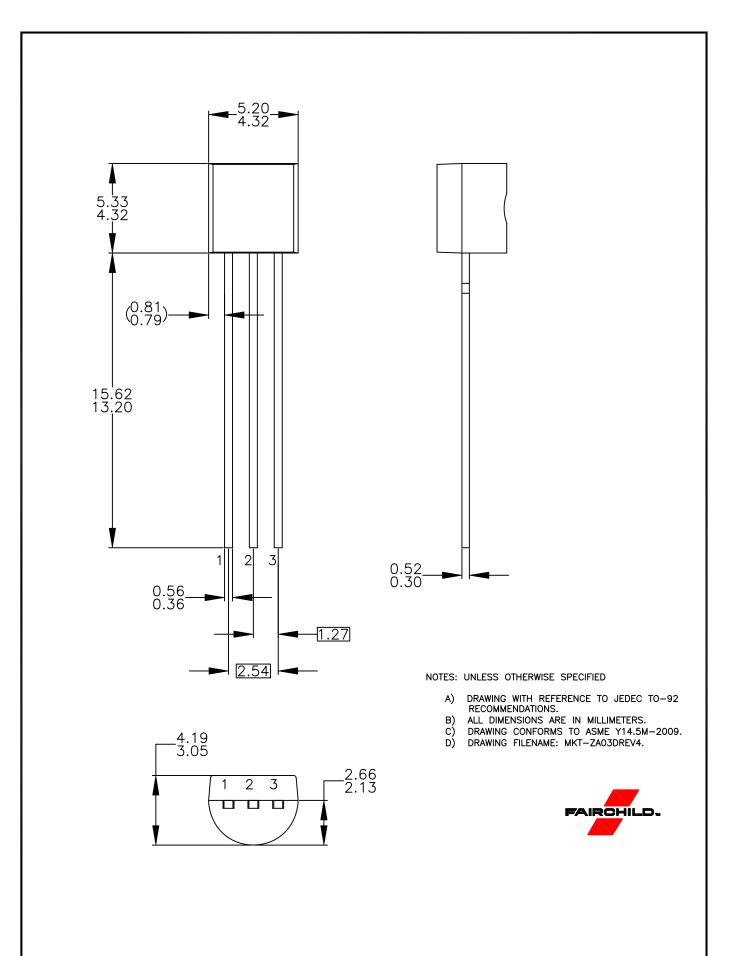




NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZA03FREV3. FAIRCHILD SEMICONDUCTOR. Α.
- В. С. D. Е.







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Rev. 177