Triacs

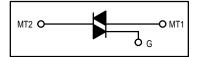
Silicon Bidirectional Triode Thyristors

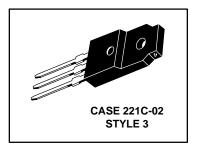
... designed primarily for full-wave ac control applications, such as solid-state relays, motor controls, heating controls and power supplies; or wherever full-wave silicon gate controlled solid-state devices are needed. Triac type thyristors switch from a blocking to a conducting state for either polarity of applied anode voltage with positive or negative gate triggering.

- Blocking Voltage to 400 Volts
- All Diffused and Glass Passivated Junctions for Greater Parameter Uniformity and Stability
- Small, Rugged, Isolated Construction for Low Thermal Resistance, High Heat Dissipation and Durability

T2500DFP

ISOLATED TRIACS THYRISTORS 6 AMPERES RMS 400 VOLTS





MAXIMUM RATINGS ($T_J = 25^{\circ}C$ unless otherwise noted.)

Rating	Symbol	Value	Unit
Repetitive Peak Off-State Voltage ⁽¹⁾ (T _J = -40 to +100°C, Gate Open)	V _{DRM}	400	Volts
On-State RMS Current (T _C = +80°C)(2) (Full Cycle Sine Wave 50 to 60 Hz)	I _T (RMS)	6	Amps
Peak Non-repetitive Surge Current (One Full Cycle, 60 Hz, T _C = +80°C)	ITSM	60	Amps
Circuit Fusing Considerations (t = 8.3 ms)	l ² t	40	A ² s
Peak Gate Power (T _C = +80°C, Pulse Width = 1 μs)	P _{GM}	1	Watt
Average Gate Power (T _C = +80°C, t = 8.3 ms)	P _G (AV)	0.2	Watt
Peak Gate Trigger Current (Pulse Width = 10 μs)	IGТМ	4	Amps
RMS Isolation Voltage (T _A = 25°C, Relative Humidity ≤ 20%)	VISO	1500	Volts
Operating Junction Temperature Range	TJ	-40 to +100	°C
Storage Temperature Range	T _{stg}	-40 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case ⁽²⁾ Case to Sink	R ₀ JC R ₀ CS	2.7 2.2(typ)	°C/W
Junction to Ambient	$R_{\theta JA}$	60	

^{1.} V_{DRM} for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

^{2.} The case temperature reference point for all T_C measurements is a point on the center lead of the package as close as possible to the plastic body.



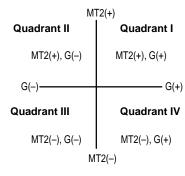
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ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted.)

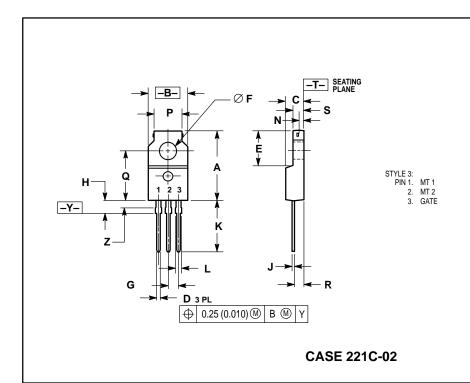
Characteristic	Symbol	Min	Тур	Max	Unit
Peak Off–State Current (Either Direction) (V _D = Rated V _{DRM} , T _J = 100°C, Gate Open)	IDRM	_	_	2	mA
Maximum On-State Voltage (Either Direction)* (IT = 30 A Peak)	V _{TM}	_	_	2	Volts
Gate Trigger Current (Continuous dc) (V _D = 12 Vdc, R _L = 12 Ohms) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-) MT2(-), G(+)	^I GT		10 20 15 30	25 60 25 60	mA
Gate Trigger Voltage (Continuous dc) (All Quadrants) (VD = 12 Vdc, RL = 12 Ohms) (VD = VDROM, RL = 125 Ohms, TC = 100°C, All Trigger Models)	VGT	_ 0.2	1.25 —	2.5 —	Volts
Holding Current (Either Direction) (Main Terminal Voltage = 12 Vdc, Gate Open, Initiating Current = 150 mA, T _C = 25°C)	lн	_	15	30	mA
Gate Controlled Turn-On Time (V_D = Rated V_{DRM} , I_T = 10 A, I_{GT} = 160 mA, Rise Time \leq 0.1 μ s)	^t gt	_	1.6	_	μs
Critical Rate–of–Rise of Commutation Voltage (V_D = Rated V_{DRM} , $I_{T(RMS)}$ = 6 A, Commutating di/dt = 3.2 A/ms, Gate Unenergized, I_C = 80°C)	dv/dt(c)	_	10	_	V/µs
Critical Rate-of-Rise of Off-State Voltage (VD = Rated VDRM, Exponential Voltage Rise, Gate Open, TC = 100°C)	dv/dt		100	_	V/μs

^{*}Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

Quadrant Definitions



PACKAGE DIMENSIONS



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. LEAD DIMENSIONS UNCONTROLLED WITHIN DIMENSION Z.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.680	0.700	17.28	17.78	
В	0.388	0.408	9.86	10.36	
С	0.175	0.195	4.45	4.95	
D	0.025	0.040	0.64	1.01	
Е	0.340	0.355	8.64	9.01	
F	0.140	0.150	3.56	3.81	
G	0.100	BSC	2.54 BSC		
Н	0.110	0.155	2.80	3.93	
J	0.018	0.028	0.46	0.71	
K	0.500	0.550	12.70	13.97	
L	0.045	0.070	1.15	1.77	
N	0.049		1.25		
Р	0.270	0.290	6.86	7.36	
Q	0.480	0.500	12.20	12.70	
R	0.090	0.120	2.29	3.04	
S	0.105	0.115	2.67	2.92	
Z	0.070	0.090	1.78	2.28	

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