# Old Company Name in Catalogs and Other Documents

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## MOS FIELD EFFECT TRANSISTOR

2SJ355

# P-CHANNEL MOS FET FOR HIGH SWITCHING

The 2SJ355 is a P-channel MOS FET of a vertical type and is a switching element that can be directly driven by the output of an IC operating at 5 V.

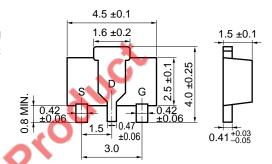
This product has a low ON resistance and superb switching characteristics and is ideal for driving the actuators and DC/DC converters.

#### **FEATURES**

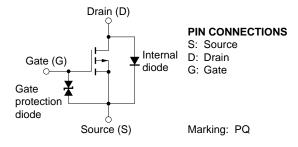
- · Can be directly driven by 5-V IC
- · Low ON resistance

RDS(on) =  $0.60~\Omega$  MAX. @VGS = -4 V, ID = -1.0~A RDS(on) =  $0.35~\Omega$  MAX. @VGS = -10 V, ID = -1.0~A

#### PACKAGE DIMENSIONS (in mm)



#### **EQUIVALENT CIRCUIT**



#### ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

| PARAMETER               | SYMBOL                | TEST CONDITIONS  | RATING      | UNIT |
|-------------------------|-----------------------|--|-------------|------|
| Drain to Source Voltage | VDSS                  | V <sub>G</sub> S = 0   | -30         | V    |
| Gate to Source Voltage  | Vgss                  | V <sub>DS</sub> = 0  | -20/+10     | V    |
| Drain Current (DC)      | I <sub>D(DC)</sub>    |  | ±2.0        | Α    |
| Drain Current (Pulse)   | I <sub>D(pulse)</sub> | PW ≤ 10 ms   | ±4.0        | Α    |
|                         |                       | Duty cycle ≤ 1 %   |             |      |
| Total Power Dissipation | Рт                    | $16 \text{ cm}^2 \times 0.7 \text{ mm}$ , ceramic substrate used | 2.0         | W    |
| Channel Temperature     | Tch                   |  | 150         | °C   |
| Storage Temperature     | Tstg                  |  | –55 to +150 | °C   |

The internal diode connected between the gate and source of this product is to protect the product from static electricity. If the product is used in a circuit where the rated voltage of the product may be exceeded, connect a protection circuit.

Take adequate preventive measures against static electricity when handling this product.

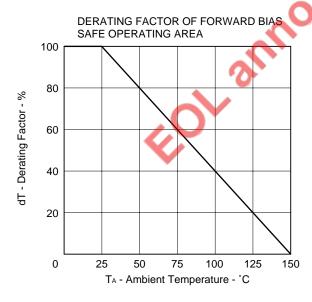
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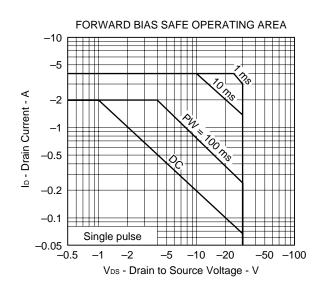


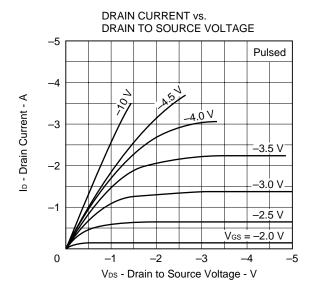
### ELECTRICAL CHARACTERISTICS (TA = 25 °C)

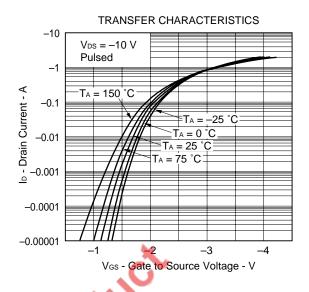
| PARAMETER                              | SYMBOL               | TEST CONDITIONS   | MIN. | TYP. | MAX.     | UNIT |
|--|----------------------|---|------|------|----------|------|
| Drain Cut-Off Current                  | IDSS                 | V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0  |      |      | -10      | μΑ   |
| Gate Leakage Current                   | Igss                 | Vgs = -16/+10 V, Vps = 0  |      |      | ±10      | μΑ   |
| Gate Cut-Off Voltage                   | Vgs(off)             | V <sub>DS</sub> = -10 V, I <sub>D</sub> = -1 mA   | -1.0 | -1.5 | -2.0     | V    |
| Forward Transfer Admittance            | yfs                  | V <sub>DS</sub> = -10 V, I <sub>D</sub> = -1.0 A  | 1.0  |      |          | S    |
| Drain to Source On-State Resistance    | RDS(on)1             | Vgs = -4 V, ID = -1.0 A   |      | 0.50 | 0.60     | Ω    |
| Drain to Source On-State Resistance    | R <sub>DS(on)2</sub> | Vgs = -10 V, ID = -1.0 A  |      | 0.26 | 0.35     | Ω    |
| Input Capacitance                      | Ciss                 | V <sub>DS</sub> = -10 V, V <sub>GS</sub> = 0,<br>f = 1.0 MHz  |      | 300  |          | pF   |
| Output Capacitance                     | Coss                 |   |      | 245  |          | pF   |
| Reverse Transfer Capacitance           | Crss                 |   |      | 120  |          | pF   |
| Turn-On Delay Time                     | td(on)               | $V_{DD} = -25 \text{ V}, \text{ ID} = -1.0 \text{ A}$ $V_{GS(on)} = -10 \text{ V}$ $R_G = 10 \Omega, R_L = 25 \Omega$ |      | 5.5  | <u> </u> | ns   |
| Rise Time                              | tr                   |   |      | 32   |          | ns   |
| Turn-Off Delay Time                    | td(off)              |   |      | 110  |          | ns   |
| Fall Time                              | <b>t</b> f           |   | O    | 130  |          | ns   |
| Gate Input Charge                      | QG                   | V <sub>DS</sub> = -24 V,  | O    | 12.2 |          | nC   |
| Gate to Source Charge                  | Qgs                  | V <sub>GS</sub> = -10 V,<br>I <sub>D</sub> = -1.8 A, I <sub>G</sub> = -2 mA   | •    | 1.2  |          | nC   |
| Gate to Drain Charge                   | Q <sub>GD</sub>      |   |      | 4.6  |          | nC   |
| Internal Diode Reverse Recovery Time   | trr                  | IF = 2.0 A,   |      | 95   |          | ns   |
| Internal Diode Reverse Recovery Charge | Qrr                  | di/dt = 50 A/μs   |      | 85   |          | nC   |

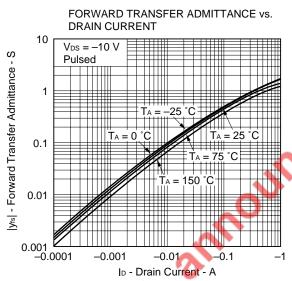
# TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C)

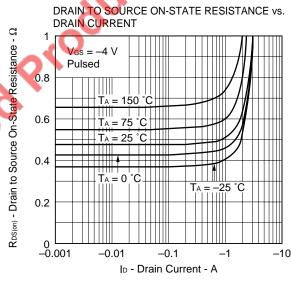


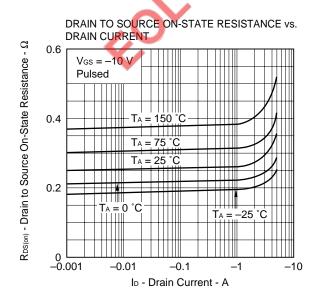


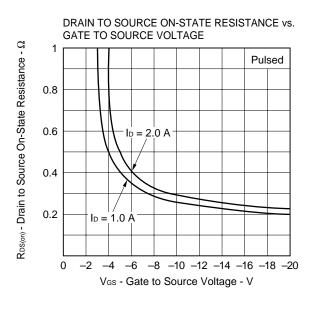




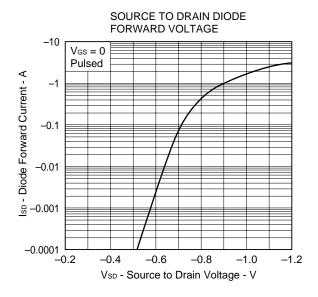


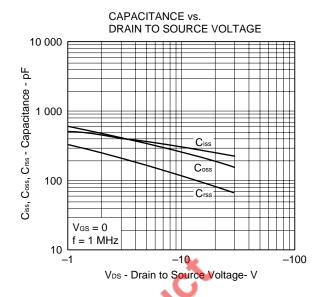


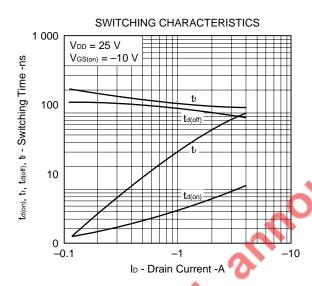


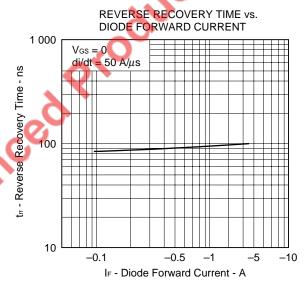


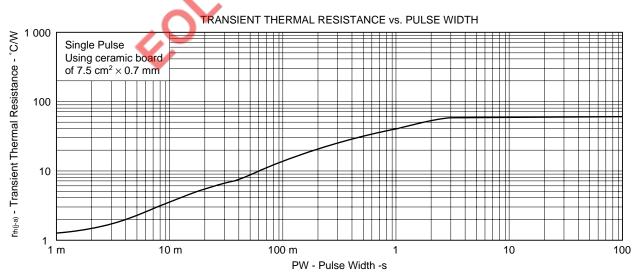














#### **REFERENCE**

| Document Name   | Document No. |  |  |
|---|--------------|--|--|
| NEC semiconductor device reliability/quality control system | TEI-1202     |  |  |
| Quality grade on NEC semiconductor devices                  | IEI-1209     |  |  |
| Semiconductor device mounting technology manual             | C10535E      |  |  |
| Guide to quality assurance for semiconductor devices        | MEI-1202     |  |  |
| Semiconductor selection guide                               | X10679E      |  |  |

EOL announced Product

5

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Standard: Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots

Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)

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