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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74HC241

Octal Buffers/Line Drivers/Line Receivers (with noninverted 3-state outputs)

REJ03D0595-0200 (Previous ADE-205-472) Rev.2.00 Jan 31, 2006

Description

The HD74HC241 is a noninverting buffer and has one active low enable and one active high enable. Each enable independently controls 4 buffers.

This device does not have schmitt trigger inputs.

Features

High Speed Operation: t_{pd} = 11 ns typ (C_L = 50 pF)
 High Output Current: Fanout of 15 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2$ to 6 V

• Low Input Current: 1 µA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC241FPEL	SOP-20 pin (JEITA)	PRSP0020DD-B (FP-20DAV)	FP	EL (2,000 pcs/reel)
HD74HC241RPEL	SOP-20 pin (JEDEC)	PRSP0020DC-A (FP-20DBV)	RP	EL (1,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

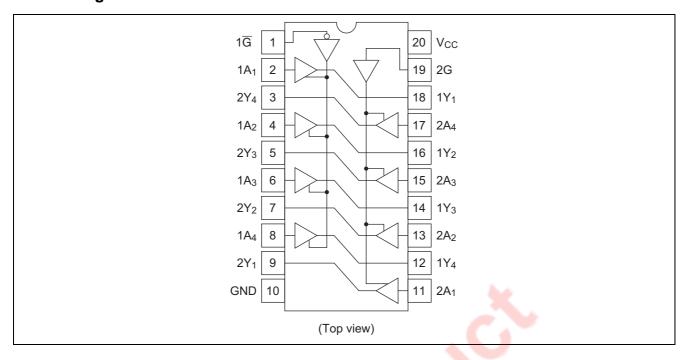
Function Table

	Output		
1 <u>G</u>	2G	Α	Y
Н	L	X	Z
L	Н	Н	Н
L	Н	L	L

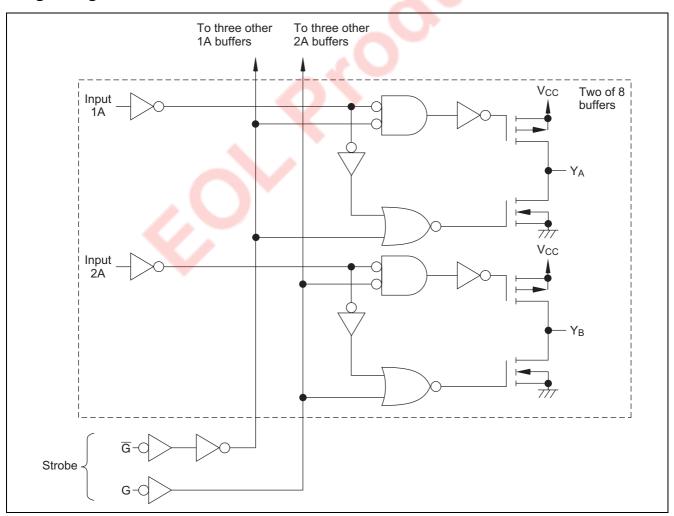
H: high levelL: low levelX: irrelevant

Z : off (high-impedance) state of a 3-state output

Pin Arrangement



Logic Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
Input / Output voltage	V _{IN} , V _{OUT}	-0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IK} , I _{OK}	±20	mA
Output current	lo	±35	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±75	mA
Power dissipation	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	2 to 6	V	
Input / Output voltage	V _{IN} , V _{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
Input rise / fall time*1	t _r , t _f	0 to 1000	ns	V _{CC} = 2.0 V
		0 to 500	. 6 1	$V_{CC} = 4.5 \text{ V}$
		0 to 400		$V_{CC} = 6.0 \text{ V}$

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

Electrical Characteristics

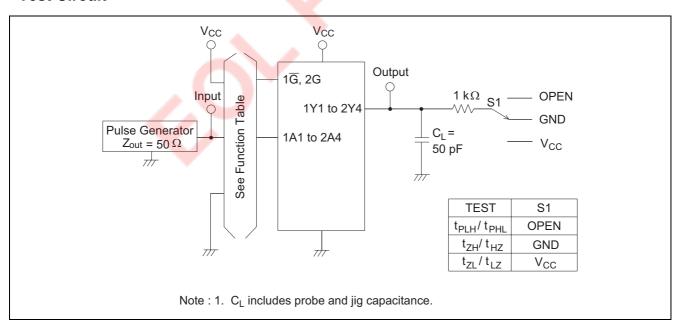
ltom	Cumbal	V 00	Т	a = 25°	С	Ta = -40	to+85°C	Unit	Test Conditions	
Item	Symbol	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit		
Input voltage	V_{IH}	2.0	1.5		7	1.5	_	V		
		4.5	3.15		_	3.15	_			
		6.0	4.2	_	1	4.2	_			
	V_{IL}	2.0		ı	0.5		0.5	V		
		4.5	1	J	1.35		1.35			
		6.0	-	_	1.8	_	1.8			
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5		4.4				
		6.0	5.9	6.0		5.9				
		4.5	4.18	_	_	4.13	_			$I_{OH} = -6 \text{ mA}$
		6.0	5.68	_	_	5.63	_			$I_{OH} = -7.8 \text{ mA}$
	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \mu A$
		4.5		0.0	0.1		0.1			
		6.0		0.0	0.1		0.1			
		4.5		1	0.26		0.33			$I_{OL} = 6 \text{ mA}$
		6.0	_	_	0.26		0.33			$I_{OL} = 7.8 \text{ mA}$
Off-state output	l _{OZ}	6.0	_	_	±0.5	_	±5.0	μΑ	$Vin = V_{IH} \text{ or } V_{IL},$	
current									Vout = V_{CC} or GND	
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V _{CC} or GND	
Quiescent supply	I _{CC}	6.0	_	_	4.0	_	40	μΑ	Vin = V_{CC} or GND, lout = 0 μ A	
current										

Switching Characteristics

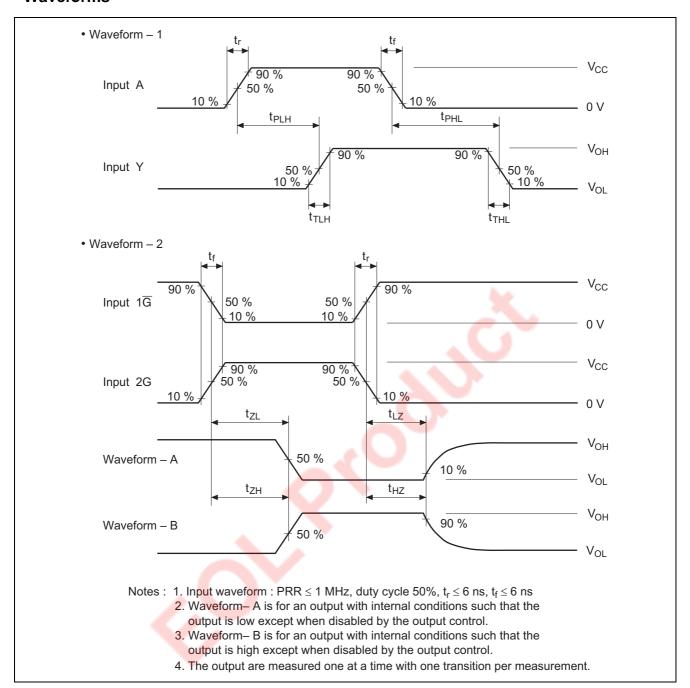
 $(C_L = 50 \text{ pF, Input } t_r = t_f = 6 \text{ ns})$

ltom	Symbol	V 00	Т	a = 25°	С	Ta = -40 to +85°C		Unit	Test Conditions
Item	Syllibol	V _{CC} (V)	Min	Тур	Max	Min	Max	Oilit	rest Conditions
Propagation delay	t _{PHL}	2.0	_	_	90	_	115	ns	
time		4.5	_	12	18	_	23		
		6.0	_	_	15	_	20		
	t _{PLH}	2.0	_	_	90	_	115	ns	
		4.5	_	10	18	_	23		
		6.0	_	_	15	_	20		
Output enable time	t _{ZL}	2.0	_	_	150	_	190	ns	
		4.5	_	11	30	_	38		
		6.0	_	_	26	_	33		
	t _{ZH}	2.0	_	_	150	_	190	ns	
		4.5	_	12	30	_	38		
		6.0	_	_	26	_	33		
Output disable	t _{LZ}	2.0	_	_	150	_	190	ns	
time		4.5	_	16	30	_	38	-	
		6.0	_	_	26	_	33		
	t _{HZ}	2.0	_	_	150	_	190	ns	
		4.5	_	19	30	_	38		
		6.0	_	_	26	_	33	3	
Output rise/fall	t _{TLH}	2.0	_	_	60	- 4	75	ns	
time	t _{THL}	4.5	_	4	12		15		
		6.0		_	10		13		
Input capacitance	Cin	_	_	5	10		10	pF	

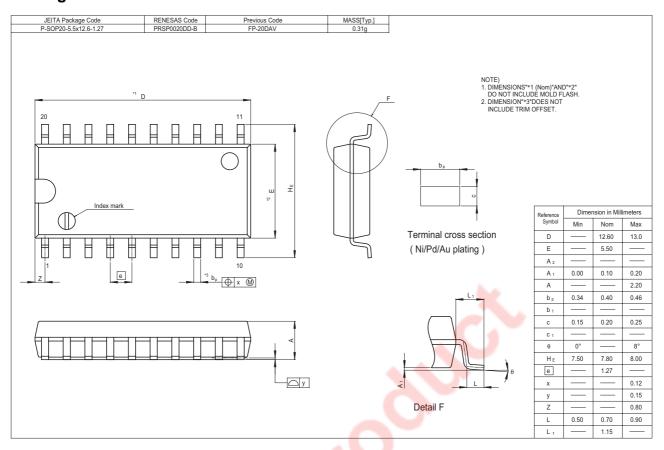
Test Circuit

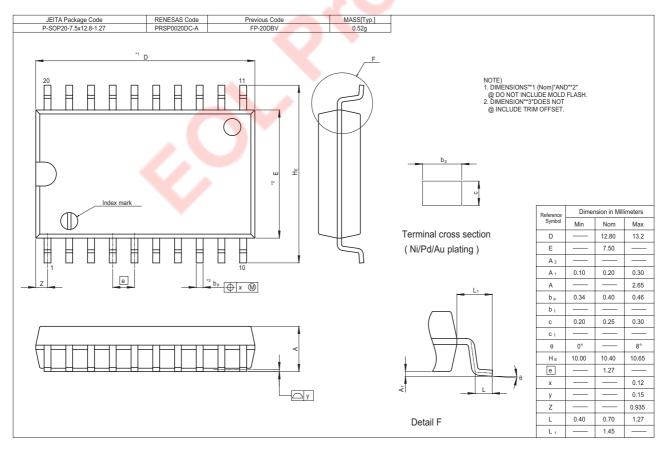


Waveforms



Package Dimensions





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