

December 2014

# FCPF260N65FL1 N-Channel SuperFET<sup>®</sup> II FRFET<sup>®</sup> MOSFET

650 V, 15 A, 260 m $\Omega$ 

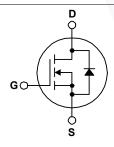
### Features

- 700 V @T<sub>J</sub> = 150°C
- R<sub>DS(on)</sub> = 220 mΩ (Typ.)
- Ultra Low Gate Charge (Typ. Q<sub>g</sub> = 46 nC)
- Low Effective Output Capacitance (Typ. C<sub>oss(eff.)</sub> = 223 pF)
- 100% Avalanche Tested
- RoHS Compliant

# Applications

- LCD / LED / PDP TV Telecom / Server Power Supplies
- Solar Inverter
- GDS TO-220F

· AC - DC Power Supply



component and improve system reliability.

SuperFET<sup>®</sup> II MOSFET is Fairchild Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing

charge balance technology for outstanding low on-resistance

and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching

performance, dv/dt rate and higher avalanche energy.

Consequently, SuperFET II MOSFET is very suitable for the switching power applications such as PFC, server/telecom

power, FPD TV power, ATX power and industrial power applications. SuperFET II  $\mathsf{FRFET}^{\textcircled{B}}$  MOSFET's optimized body

diode reverse recovery performance can remove additional

Description

### Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol		FCPF260N65FL1	Unit		
V <sub>DSS</sub>	Drain to Source Voltage	650	V		
V <sub>GSS</sub>		- DC	±20	- V	
	Gate to Source Voltage	- AC (f > 1 Hz)	±30		
ID	Drain Current	- Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)	15		
		- Continuous (T <sub>C</sub> = 100 <sup>o</sup> C)	9.5	A	
I <sub>DM</sub>	Drain Current	- Pulsed (Note 1)	45	А	
E <sub>AS</sub>	Single Pulsed Avalanche Ene	293	mJ		
I <sub>AR</sub>	Avalanche Current	3	А		
E <sub>AR</sub>	Repetitive Avalanche Energy	0.36	mJ		
dv/dt	MOSFET dv/dt	100	1//20		
	Peak Diode Recovery dv/dt	50	V/ns		
P <sub>D</sub>	Dewer Dissignation	(T <sub>C</sub> = 25 <sup>o</sup> C)	36	W	
	Power Dissipation	- Derate Above 25°C	0.29	W/ºC	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Tempe	-55 to +150	°C		
TL	Maximum Lead Temperature	300	°C		

## **Thermal Characteristics**

Symbol	Parameter	FCPF260N65FL1	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	3.5	°C/W
$R_{\thetaJA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	-0/10

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	nber	Top Mark	Package	Packing Method	Reel Size	Тар	e Width	Qua	ntity
FCPF260N			TO-220F	Tube	N/A		N/A	50 units	
Electrica	l Char	acteristics T <sub>o</sub> =	25°C unless o	otherwise noted				1	
Symbol	Parameter			Test Conditions		Min.	Тур.	Max.	Unit
Off Charac	teristic	s							
		•		V <sub>GS</sub> = 0 V, I <sub>D</sub> = 10 mA	T = 25°C	650	-	-	V
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage		'oltage	$V_{GS} = 0 V, I_D = 10 mA, T_J = 25 °C$ $V_{GS} = 0 V, I_D = 10 mA, T_J = 150°C$		700	_	_	V
ΔBV <sub>DSS</sub> / ΔΤ.Ι	Breakdown Voltage Temperature		ure	$V_{GS} = 0.0$ , $I_D = 10$ mA, $I_J = 150$ °C $I_D = 10$ mA, Referenced to 25°C		-	0.72	-	V/°C
21)	Cocilier			V <sub>DS</sub> = 650 V, V <sub>GS</sub> = 0 V		-	-	10	
I <sub>DSS</sub>	Zero Ga	ate Voltage Drain Curr	ent	$V_{DS} = 520 \text{ V}, \text{ V}_{GS} = 0$		-	40	-	μA
I <sub>GSS</sub>	Gate to	Body Leakage Currer	nt	$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0$	-	-	-	±100	μA
	torictic								
On Charac	-				•	2			14
V <sub>GS(th)</sub>		reshold Voltage		$V_{GS} = V_{DS}, I_D = 1.5 m$ $V_{GS} = 10 V, I_D = 7.5 A$		3	-	5	V
R <sub>DS(on)</sub>		rain to Source On Re	sistance	00 5		-	220	260	mΩ
9 <sub>FS</sub>	Forward Transconductance			V <sub>DS</sub> = 20 V, I <sub>D</sub> = 7.5 A	<b>`</b>	-	14.2	-	S
Dynamic C	haracte	eristics							
C <sub>iss</sub>	Input Capacitance Output Capacitance					-	1760	2340	pF
C <sub>oss</sub>				$V_{\rm DS} = 100 \text{ V}, \text{ V}_{\rm GS} = 0 \text{ V},$	-	59	80	pF	
C <sub>rss</sub>	-	Reverse Transfer Capacitance		f = 1 MHz		-	1.0	-	pF
C <sub>oss</sub>	Output (	Output Capacitance		V <sub>DS</sub> = 380 V, V <sub>GS</sub> = 0 V, f = 1 MHz		-	34	-	pF
C <sub>oss(eff.)</sub>	Effective Output Capacitance			$V_{DS} = 0 V \text{ to } 400 V, V_{GS} = 0 V$		-	223	-	pF
Q <sub>g(tot)</sub>	Total Ga	Total Gate Charge at 10V		$V_{DS} = 380 \text{ V}, \text{ I}_{D} = 7.5 \text{ A},$ $V_{GS} = 10 \text{ V}$		-	46	60	nC
Q <sub>gs</sub>	Gate to Source Gate Charge					-	9.6	-	nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge			(Note 4)		-	20	-	nC
ESR	Equivalent Series Resistance			f = 1 MHz		-	0.52	-	Ω
Switching	Charac	teristics							
-		Delay Time					21.7	54	ns
t <sub>d(on)</sub> t <sub>r</sub>		Rise Time		V <sub>DD</sub> = 380 V, I <sub>D</sub> = 7.5 A,			10.5	32	ns
	Turn-Off Delay Time			$V_{GS} = 10 \text{ V}, \text{ R}_{g} = 4.7 \Omega$ (Note 4)			54	118	ns
t <sub>d(off)</sub> t <sub>f</sub>	Turn-Off Fall Time						5.8	22	ns
					(1010 4)		0.0		110
		le Characteristic							
l <sub>S</sub>	Maximum Continuous Drain to Source Di					-	-	15	A
ISM	Maximum Pulsed Drain to Source Diode F					-	-	45	A
V <sub>SD</sub>		Source Diode Forwar	d voltage	$V_{GS} = 0 V, I_{SD} = 7.5 A$		-	-	1.2	V
t <sub>rr</sub>		everse Recovery Time everse Recovery Charge		V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 7.5 A, dI <sub>F</sub> /dt = 100 A/μs		-	98	-	ns
Q <sub>rr</sub>	Reverse	Recovery Charge				-	450	-	nC

8

1.6

25°C

1.0

V<sub>DS</sub> = 130V

30

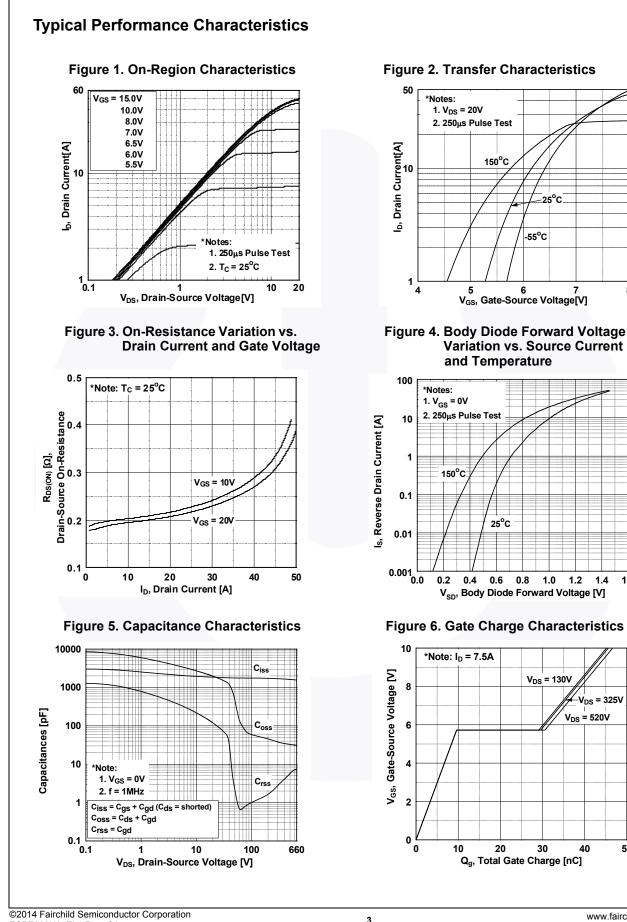
1.2 1.4

-V<sub>DS</sub> = 325V

V<sub>DS</sub> = 520V

40

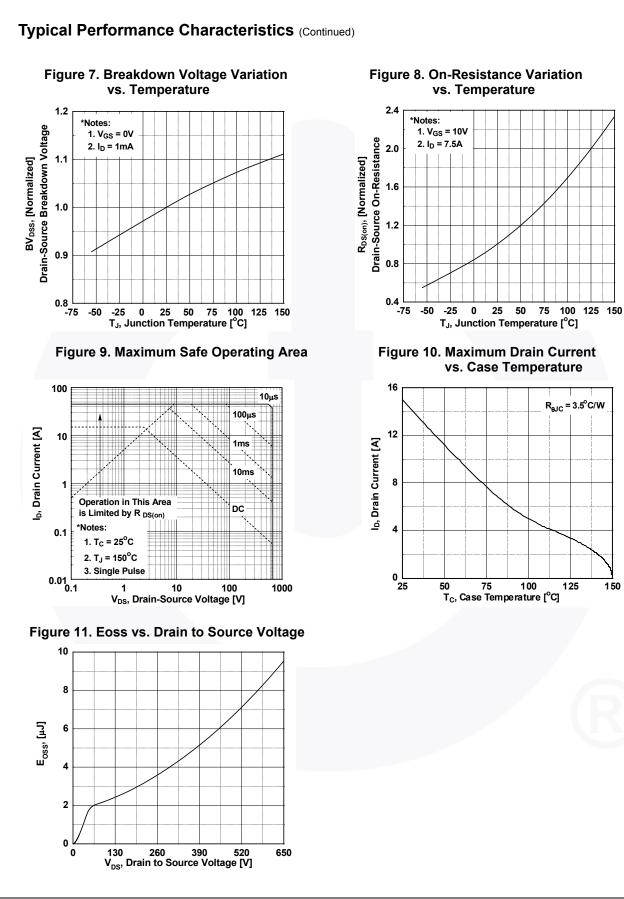
-55°C



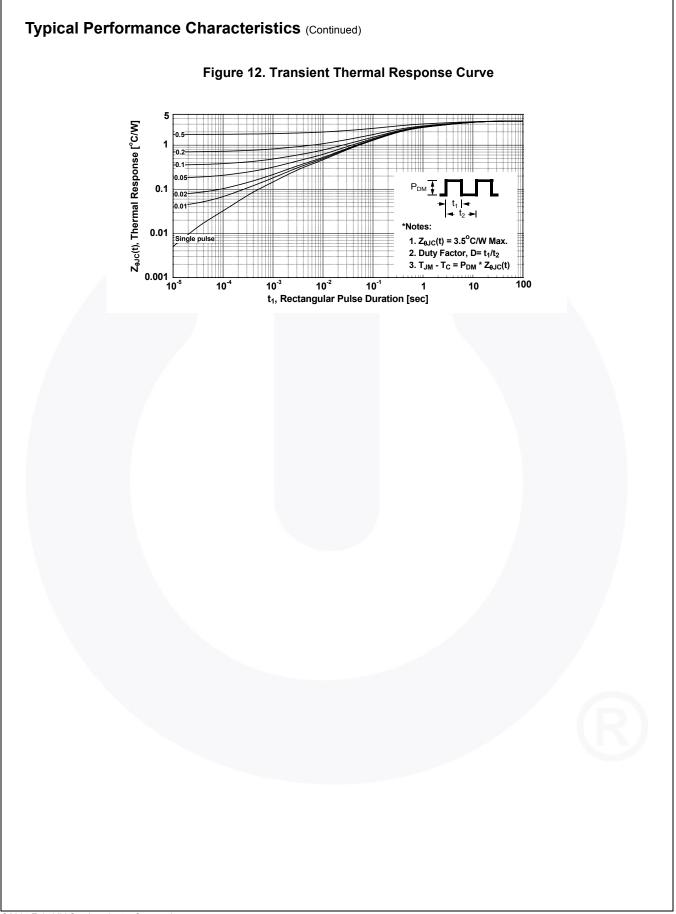
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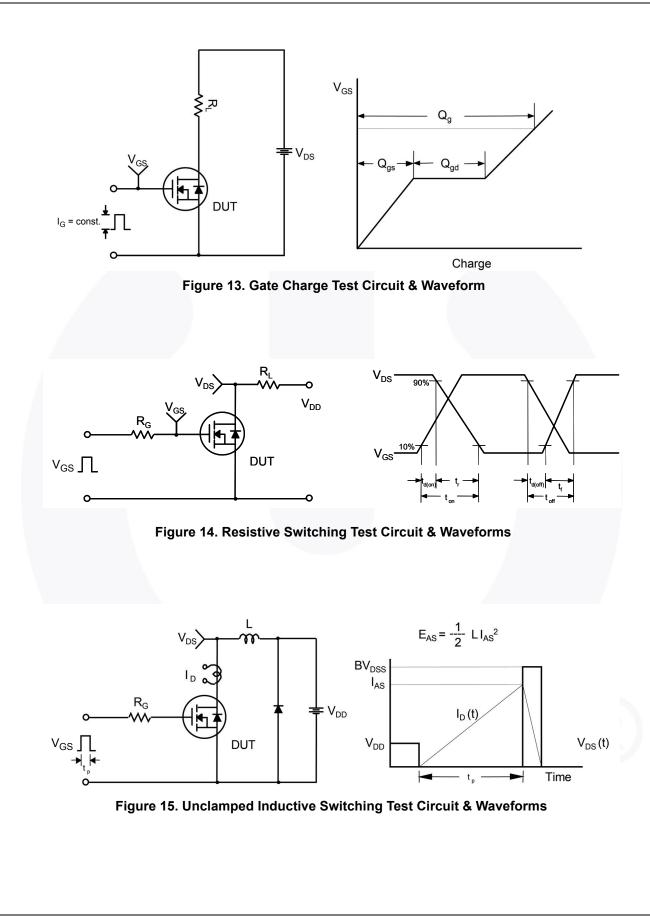
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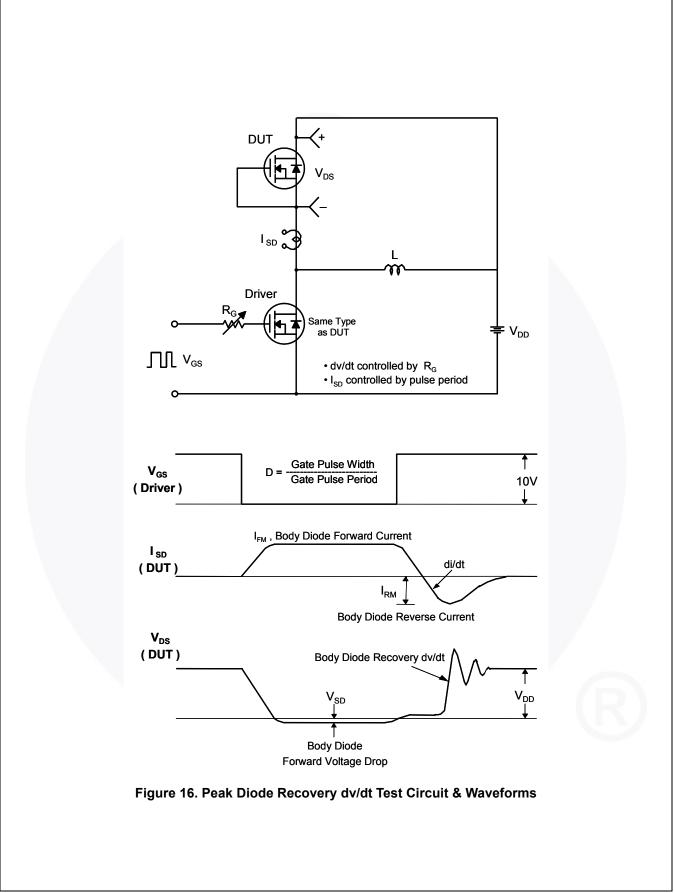
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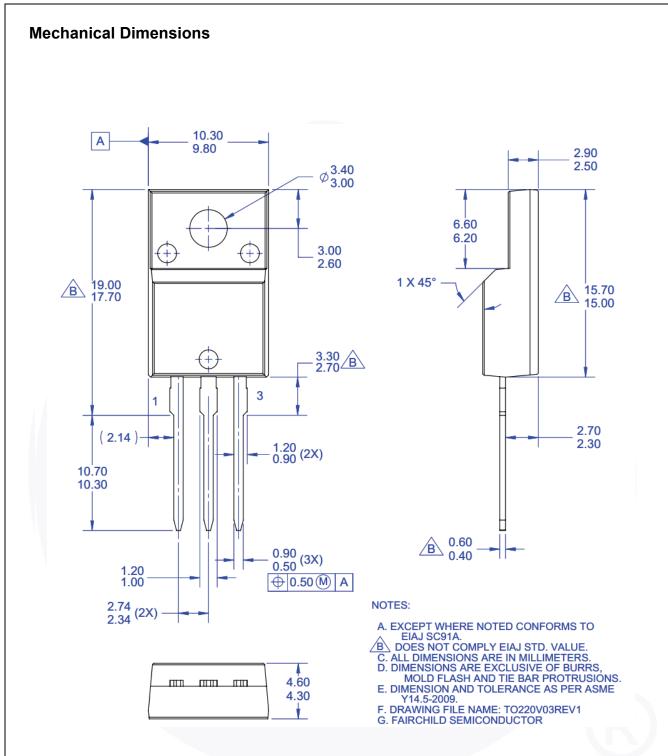


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### Figure 17. TO220, Molded, 3LD, Full Pack, EIAJ SC91, Takcheong

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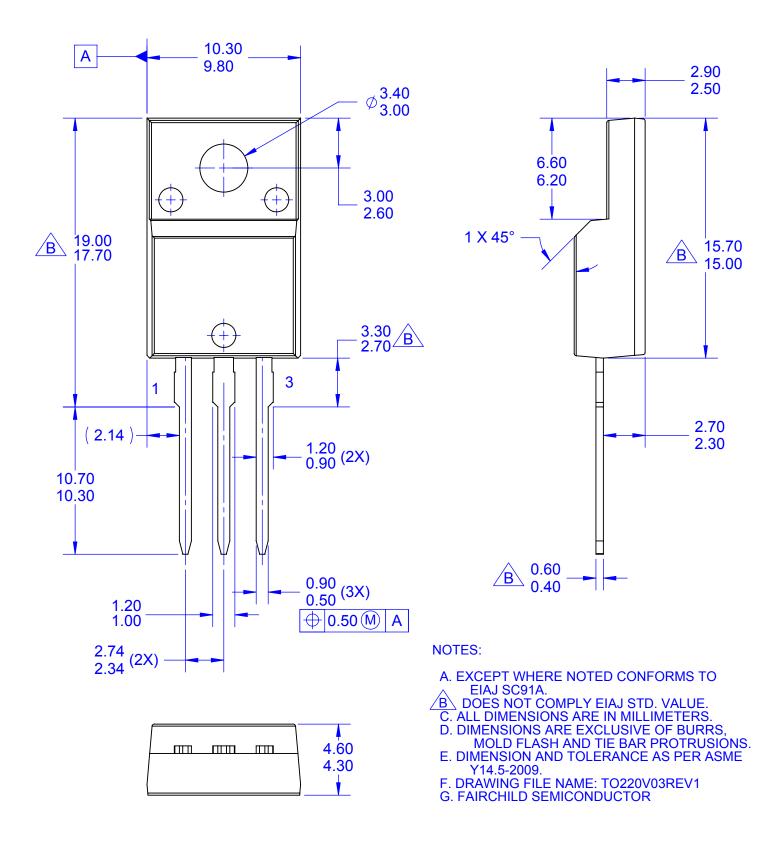
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