

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.

**LOW NOISE AND HIGH GAIN AMPLIFIER
FOR IMPEDANCE CONVERTER OF MICROPHONE**

DESCRIPTION

The μPD5742T6J is a silicon MOS monolithic integrated circuit designed as high gain impedance converter for electret condenser microphone. This device exhibits low noise and high voltage gain characteristics.

The package is 3-pin thin-type lead-less minimold, suitable for surface mount.

FEATURES

- Low Noise : $N_v = -98$ dBV TYP. @ $V_{DD} = 2$ V, $C_{in} = 3$ pF, $R_L = 2.2$ kΩ
: $N_v = -99$ dBV TYP. @ $V_{DD} = 2$ V, $C_{in} = 5$ pF, $R_L = 2.2$ kΩ
- High Gain : $G_v = +9.0$ dB TYP. @ $V_{DD} = 2$ V, $C_{in} = 3$ pF, $R_L = 2.2$ kΩ
: $G_v = +11.0$ dB TYP. @ $V_{DD} = 2$ V, $C_{in} = 5$ pF, $R_L = 2.2$ kΩ
- Low Consumption Current : $I_{DD} = 370$ μA TYP. @ $V_{DD} = 2$ V, $R_L = 2.2$ kΩ
- Built-in the capacitor for RF noise immunity
- High ESD voltage
- 3-pin thin-type lead-less minimold (1.2 × 1.0 × 0.33 mm)

APPLICATIONS

- Microphone, Sensor, etc.

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPD5742T6J-E4	μPD5742T6J-E4-A	3-pin thin-type lead-less minimold (Pb-Free)	6V	<ul style="list-style-type: none"> • Embossed tape 8 mm wide • Pin 3 (GND) face the perforation side of the tape • Qty 10 kpcs/reel

Remark To order evaluation samples, contact your nearby sales office.

Part number for sample order: μPD5742T6J

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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ABSOLUTE MAXIMUM RATINGS (T_A = +25°C)

Parameter	Symbol	Ratings	Unit
Input Voltage (IN-GND)	V _{in}	-0.5 to +0.5	V
Input Current (IN-GND)	I _{in}	0.5	mA
Output Voltage (OUT-GND)	V _{out}	0 to +5	V
Output Current (OUT-GND)	I _{out}	1.0	mA
Channel Temperature	T _{ch}	130	°C
Operating Ambient Temperature	T _A	-40 to +85	°C
Storage Temperature	T _{stg}	-65 to +150	°C

RECOMMENDED OPERATING CONDITIONS (T_A = +25°C)

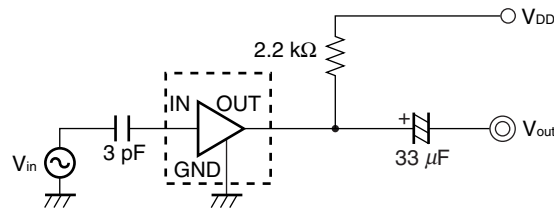
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage ^{Note}	V _{DD}	1.4	2.0	5.0	V

Note R_L = 2.2 kΩ

ELECTRICAL CHARACTERISTICS (T_A = +25°C, unless otherwise specified)

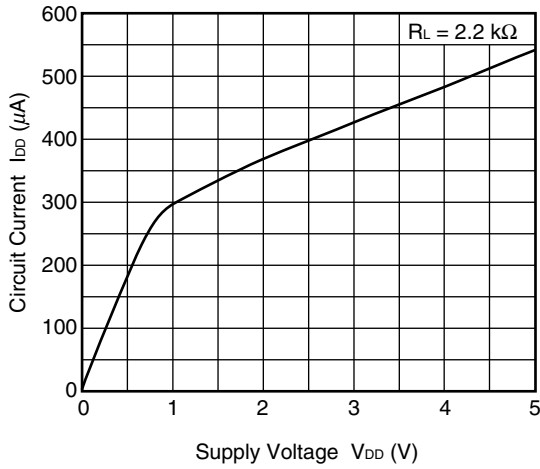
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Circuit Current	I _{DD}	V _{DD} = 2 V, V _{in} = 0 V, R _L = 2.2 kΩ	260	370	480	μA
Input Capacitance	C _{input}	V _{DD} = 2 V, R _L = 2.2 kΩ, f = 1 MHz	–	2.0	–	pF
Voltage Gain	G _v	V _{DD} = 2 V, V _{in} = 10 mV, R _L = 2.2 kΩ, C _{in} = 3 pF, f = 1 kHz, see Test Circuit	8.0	9.0	10.0	dB
Reduced Voltage Gain Characteristics	ΔG _v	V _{DD} = 2 → 1.5 V, V _{in} = 10 mV, R _L = 2.2 kΩ, C _{in} = 3 pF, f = 1 kHz, see Test Circuit	–	1.0	–	dB
Frequency Characteristics	ΔG _v f	V _{DD} = 2 V, V _{in} = 10 mV, R _L = 2.2 kΩ, C _{in} = 3 pF, f = 1 kHz → 110 Hz, see Test Circuit	–	0	–	dB
Output Noise Voltage	N _v	V _{DD} = 2 V, V _{in} = 0 V, R _L = 2.2 kΩ, C _{in} = 3 pF, A-Curve, see Test Circuit	–	–98	–	dBV
Total Harmonic Distortion	THD	V _{DD} = 2 V, V _{out} = 50 mV, R _L = 2.2 kΩ, C _{in} = 3 pF, f = 1 kHz, see Test Circuit	–	0.2	–	%

TEST CIRCUIT (Voltage Gain, Frequency Characteristics, Output Noise Voltage, Total Harmonic Distortion)

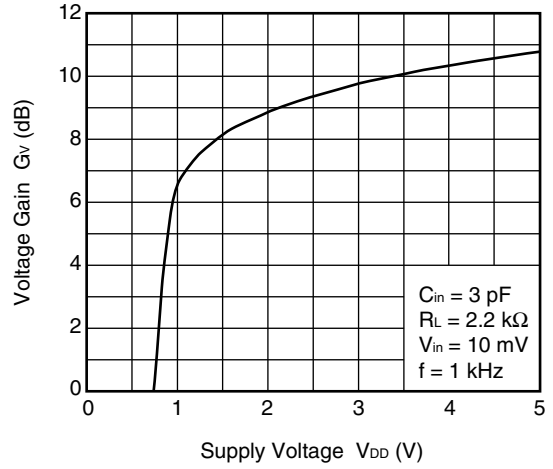


TYPICAL CHARACTERISTICS (T_A = +25°C, unless otherwise specified)

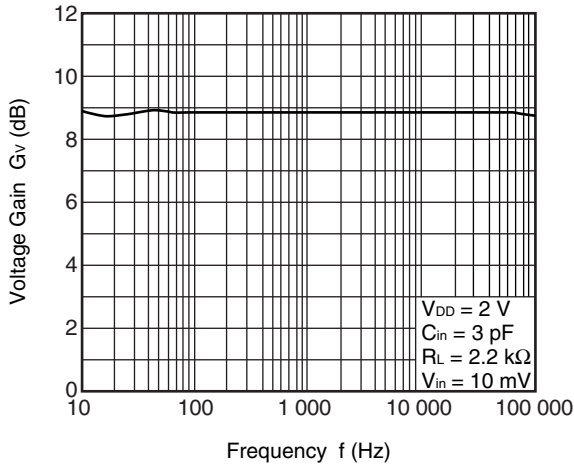
CIRCUIT CURRENT vs. SUPPLY VOLTAGE



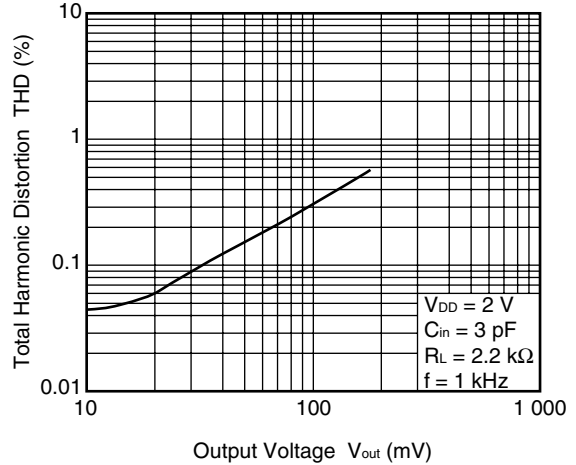
VOLTAGE GAIN vs. SUPPLY VOLTAGE



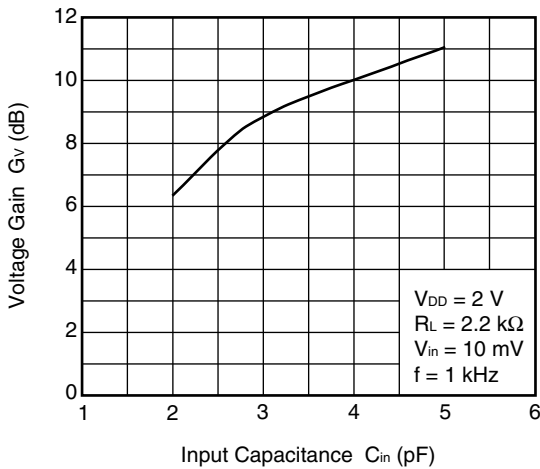
VOLTAGE GAIN vs. FREQUENCY



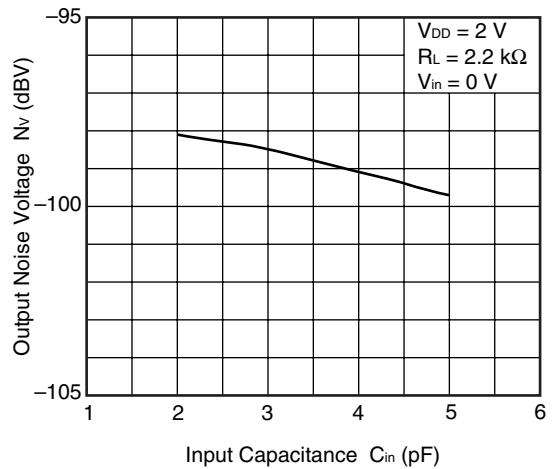
TOTAL HARMONIC DISTORTION vs. OUTPUT VOLTAGE



VOLTAGE GAIN vs. INPUT CAPACITANCE



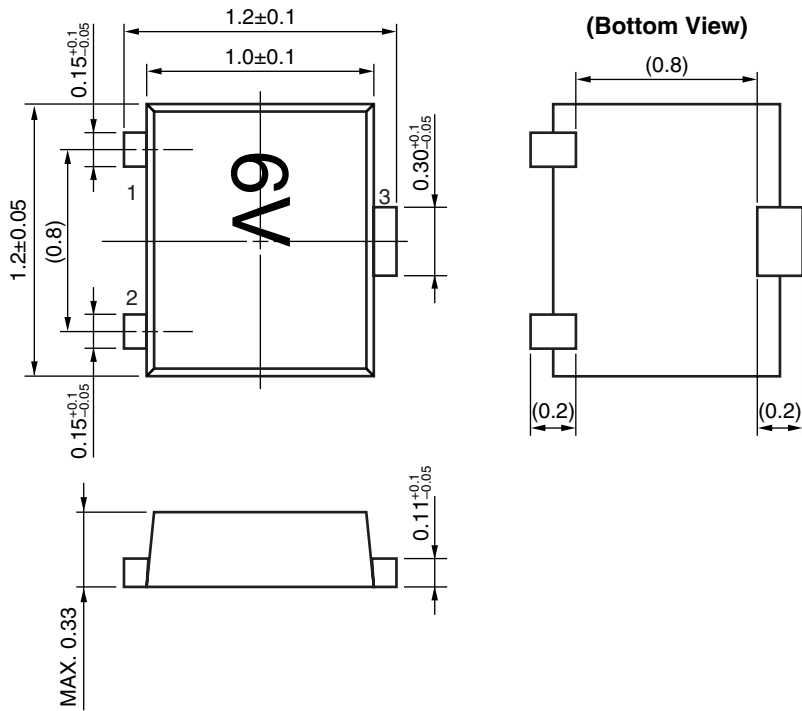
OUTPUT NOISE VOLTAGE vs. INPUT CAPACITANCE



Remark The graphs indicate nominal characteristics.

PACKAGE DIMENSIONS

3-PIN THIN-TYPE LEAD-LESS MINIMOLD (UNIT: mm)



PIN CONNECTIONS

- 1. OUT
- 2. IN
- 3. GND

Remark () : Reference value

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	IR260
Wave Soldering	Peak temperature (molten solder temperature) : 260°C or below Time at peak temperature : 10 seconds or less Preheating temperature (package surface temperature) : 120°C or below Maximum number of flow processes : 1 time Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (terminal temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

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