SCBS005D - OCTOBER 1987 - REVISED APRIL 1994

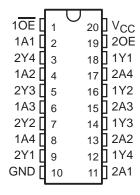
- State-of-the-Art BiCMOS Design Significantly Reduces I<sub>CCZ</sub>
- ESD Protection Exceeds 2000 V Per MIL-STD-883C, Method 3015
- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- Package Options Include Plastic Small-Outline (DW) and Shrink Small-Outline (DB) Packages, Ceramic Chip Carriers (FK) and Flatpacks (W), and Standard Plastic and Ceramic 300-mil DIPs (J, N)

## description

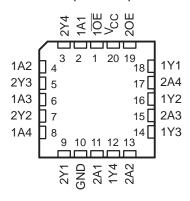
These octal buffers and line drivers are designed specifically to improve both the performance and density of 3-state memory address drivers, clock drivers, and bus-oriented receivers and transmitters. Taken together with the 'BCT240 and 'BCT244, these devices provide the choice of selected combinations of inverting and noninverting outputs, symmetrical  $\overline{OE}$  (active-low output-enable) inputs, and complementary OE and  $\overline{OE}$  inputs.

The SN54BCT241 is characterized for operation over the full military temperature range of  $-55^{\circ}$ C to 125°C. The SN74BCT241 is characterized for operation from 0°C to 70°C.

#### SN54BCT241 . . . J OR W PACKAGE SN74BCT241 . . . DB, DW OR N PACKAGE (TOP VIEW)



# SN54BCT241 . . . FK PACKAGE (TOP VIEW)

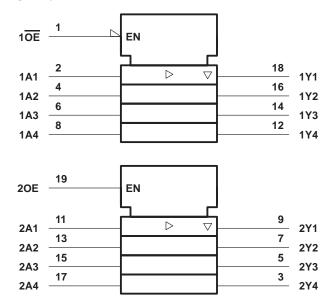


### **FUNCTION TABLES**

INP	JTS	OUTPUT
10E	1A	1Y
L	Н	Н
L	L	L
Н	Χ	Z

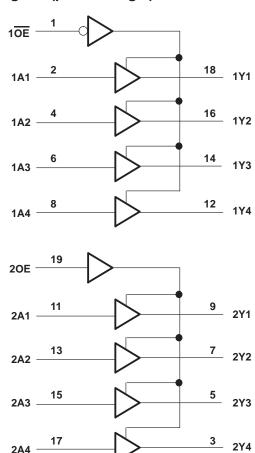
INP	JTS	OUTPUT
20E	2A	2Y
Н	Н	Н
Н	L	L
L	Χ	Z

# logic symbol†



<sup>&</sup>lt;sup>†</sup> This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

# logic diagram (positive logic)



# absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage range, V <sub>CC</sub>		
Voltage range applied to any output in		
Voltage range applied to any output in	the high state, VO	– 0.5 V to V <sub>CC</sub>
Input clamp current, I <sub>IK</sub>		
Current into any output in the low state	: SN54BCT241	96 mA
•	SN74BCT241	
Operating free-air temperature range:	SN54BCT241	– 55°C to 125°C
	SN74BCT241	0°C to 70°C
Storage temperature range		– 65°C to 150°C

<sup>&</sup>lt;sup>‡</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

# recommended operating conditions

		SN	54BCT2	41	SN	LINUT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
V <sub>IL</sub>	Low-level input voltage			0.8			8.0	V
liK	Input clamp current			-18			-18	mA
lOH	High-level output current			-12			-15	mA
loL	Low-level output current			48			64	mA
TA	Operating free-air temperature			125	0		70	°C

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED			SN	54BCT2	41	SN					
PA	RAMETER	TE	ST CONDITIONS	MIN	TYP <sup>†</sup>	MAX	MIN	TYP <sup>†</sup>	MAX	UNIT	
VIK		V <sub>CC</sub> = 4.5 V,	I <sub>I</sub> = -18 mA			-1.2			-1.2	V	
			$I_{OH} = -3 \text{ mA}$	2.4	3.3		2.4	3.3			
Vон		V <sub>CC</sub> = 4.5 V	$I_{OH} = -12 \text{ mA}$	2	3.2					V	
		$I_{OH} = -15 \text{ mA}$				2	3.1				
VOL		I <sub>OL</sub> = 48 mA		0.38	0.55				.,		
	V <sub>CC</sub> = 4.5 V	$I_{OL} = 64 \text{ mA}$					0.42	0.55	V		
l <sub>l</sub>		$V_{CC} = 5.5 V$ ,	V <sub>I</sub> = 7 V			0.1			0.1	mA	
lіН		$V_{CC} = 5.5 \text{ V},$	V <sub>I</sub> = 2.7 V			20			20	μΑ	
1OE or 2OE		V 55V				-1			-1		
I <sub>IL</sub>	Any A input	$V_{CC} = 5.5 \text{ V},$	$V_{I} = 0.5 V$			-1.6			-1.6	mA	
lozh		$V_{CC} = 5.5 V$ ,	V <sub>O</sub> = 2.7 V			50			50	μΑ	
lozL		$V_{CC} = 5.5 V$ ,	V <sub>O</sub> = 0.5 V			-50			-50	μΑ	
los‡		$V_{CC} = 5.5 \text{ V},$	V <sub>O</sub> = 0	-100		-225	-100		-225	mA	
ICCH		V <sub>CC</sub> = 5.5 V,	Outputs open		23	43		23	43	mA	
ICCL		V <sub>CC</sub> = 5.5 V,	Outputs open		53	85		53	85	mA	
ICCZ		V <sub>CC</sub> = 5.5 V,	Outputs open		4	10		4	10	mA	
Ci		V <sub>CC</sub> = 5 V,	V <sub>I</sub> = 2.5 V or 0.5 V		6			6		pF	
Со		V <sub>CC</sub> = 5 V,	V <sub>O</sub> = 2.5 V or 0.5 V		11			11		pF	

<sup>†</sup> All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C. ‡ Not more than one output should be tested at a time, and the duration of the test should not exceed one second.

# SN54BCT241, SN74BCT241 OCTAL BUFFERS/DRIVERS WITH 3-STATE OUTPUTS SCBS005D - OCTOBER 1987 - REVISED APRIL 1994

# switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC}$ = 5 V, $C_{L}$ = 50 pF, R1 = 500 Ω, R2 = 500 Ω, $T_{A}$ = 25°C			$V_{CC}$ = 4.5 V to 5.5 V, $C_L$ = 50 pF, R1 = 500 Ω, R2 = 500 Ω, $T_A$ = MIN to MAX $\dagger$				UNIT				
			′BCT241			SN54B	CT241	SN74BCT241						
			MIN	TYP	MAX	MIN	MAX	MIN	MAX					
t <sub>PLH</sub>	А	Y	0.5	2.5	4.5	0.5	5.2	0.5	4.9					
t <sub>PHL</sub>			1	1	3	5.4	1	6.3	1	5.9	ns			
<sup>t</sup> PZH	OE or OE	Y	1	5.7	7.8	1	9.1	1	8.7	20				
tPZL	OE 01 OE		1	5.2	8.6	1	10	1	9.4	ns				
<sup>t</sup> PHZ	OE or OE	Y	V	V	V		1	5.8	6.8	1	8.4	1	8.1	ne
tPLZ	OE OF OE	ſ	1	7 8.1 1	11	1	9.9	ns						

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. NOTE 2: Load circuits and voltage waveforms are shown in Section 1.





11-Jul-2015

#### **PACKAGING INFORMATION**

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
5962-9074301M2A	LIFEBUY	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	5962- 9074301M2A SNJ54BCT 241FK	
5962-9074301MRA	LIFEBUY	CDIP	J	20	1	TBD	A42	N / A for Pkg Type	-55 to 125	5962-9074301MR A SNJ54BCT241J	
5962-9074301MSA	LIFEBUY	CFP	W	20	1	TBD	A42	N / A for Pkg Type	-55 to 125	5962-9074301MS A SNJ54BCT241W	
SN74BCT241DBLE	OBSOLETE	SSOP	DB	20		TBD	Call TI	Call TI	0 to 70		
SN74BCT241N	ACTIVE	PDIP	N	20	20	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	0 to 70	SN74BCT241N	Samples
SN74BCT241NSR	OBSOLETE	so so	NS	20		TBD	Call TI	Call TI	0 to 70		
SN74BCT241NSRE4	OBSOLETE	so so	NS	20		TBD	Call TI	Call TI	0 to 70		
SN74BCT241NSRG4	OBSOLETE	so so	NS	20		TBD	Call TI	Call TI	0 to 70		
SNJ54BCT241FK	LIFEBUY	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	5962- 9074301M2A SNJ54BCT 241FK	
SNJ54BCT241J	LIFEBUY	CDIP	J	20	1	TBD	A42	N / A for Pkg Type	-55 to 125	5962-9074301MR A SNJ54BCT241J	
SNJ54BCT241W	LIFEBUY	CFP	W	20	1	TBD	A42	N / A for Pkg Type	-55 to 125	5962-9074301MS A SNJ54BCT241W	

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

# PACKAGE OPTION ADDENDUM



com 11-Jul-2015

TBD: The Pb-Free/Green conversion plan has not been defined.

**Pb-Free** (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free** (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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#### OTHER QUALIFIED VERSIONS OF SN54BCT241, SN74BCT241:

Catalog: SN74BCT241

Military: SN54BCT241

NOTE: Qualified Version Definitions:

- Catalog TI's standard catalog product
- Military QML certified for Military and Defense Applications

# 14 LEADS SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package is hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
- E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

# W (R-GDFP-F20)

# CERAMIC DUAL FLATPACK



- A. All linear dimensions are in inches (millimeters).
- This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.

  D. Index point is provided on cap for terminal identification only.

  E. Falls within Mil—Std 1835 GDFP2—F20



# FK (S-CQCC-N\*\*)

# LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a metal lid.
- D. Falls within JEDEC MS-004



# N (R-PDIP-T\*\*)

# PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- The 20 pin end lead shoulder width is a vendor option, either half or full width.



# DB (R-PDSO-G\*\*)

# PLASTIC SMALL-OUTLINE

## **28 PINS SHOWN**



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-150

# **MECHANICAL DATA**

# NS (R-PDSO-G\*\*)

# 14-PINS SHOWN

## PLASTIC SMALL-OUTLINE PACKAGE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



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