Product data sheet

1. General description

Planar passivated high commutation three quadrant triac in a SOT78 (TO-220AB) plastic package. This "series F" triac balances the requirements of commutation performance and gate sensitivity and is intended for interfacing with low power drivers and logic ICs including microcontrollers in higher noise environments.

2. Features and benefits

- 3Q technology for improved noise immunity
- Direct triggering from low power drivers and logic ICs
- High blocking voltage capability
- High commutation capability
- Intermediate sensitivity for maximum noise immunity and logic level triggering
- Planar passivated for voltage ruggedness and reliability
- Triggering in three quadrants only

3. Applications

- AC solenoids
- General purpose motor control circuits
- Home appliances

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DRM}	repetitive peak off- state voltage		-	-	600	V
I _{TSM}	non-repetitive peak on- state current	full sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 20 \text{ ms}$; Fig. 4; Fig. 5	-	-	25	A
I _{T(RMS)}	RMS on-state current	full sine wave; $T_{mb} \le 107 \text{ °C}$; Fig. 1; Fig. 2; Fig. 3	-	-	4	A
Static charac	cteristics					
I _{GT}	gate trigger current	$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2+ \text{ G+;}$ $T_j = 25 \text{ °C; } \underline{\text{Fig. 7}}$	-	-	25	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ \text{ G-};$ $T_j = 25 \text{ °C}; Fig. 7$	-	-	25	mA





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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		V _D = 12 V; I _T = 0.1 A; T2- G-;	-	-	25	mA
		T _j = 25 °C; <u>Fig. 7</u>				

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	mb	T2—T1
2	T2	main terminal 2	 	G sym051
3	G	gate		·
mb	T2	mounting base; main terminal 2		
			TO-220AB (SOT78)	

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BTA204-600F	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

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7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	600	V
I _{T(RMS)}	RMS on-state current	full sine wave; T _{mb} ≤ 107 °C; <u>Fig. 1;</u> <u>Fig. 2; Fig. 3</u>	-	4	Α
I _{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 20 \text{ ms}$; Fig. 4; Fig. 5	-	25	Α
		full sine wave; $T_{j(init)} = 25 ^{\circ}C$; $t_p = 16.7 \text{ms}$	-	27	Α
l ² t	I2t for fusing	t _p = 10 ms; SIN	-	3.1	A ² s
dI _T /dt	rate of rise of on-state current	$I_T = 6 \text{ A}$; $I_G = 0.2 \text{ A}$; $dI_G/dt = 0.2 \text{ A/}\mu\text{s}$	-	100	A/µs
I _{GM}	peak gate current		-	2	Α
P_{GM}	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C

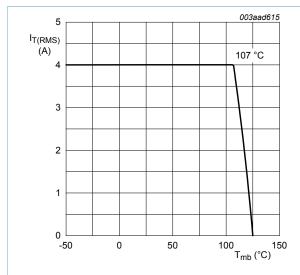
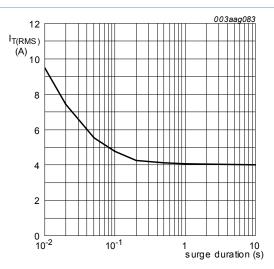


Fig. 1. RMS on-state current as a function of mounting base temperature; maximum values



 $f = 50 \text{ Hz}; T_{mb} = 107 \text{ }^{\circ}\text{C}$

Fig. 2. RMS on-state current as a function of surge duration; maximum values

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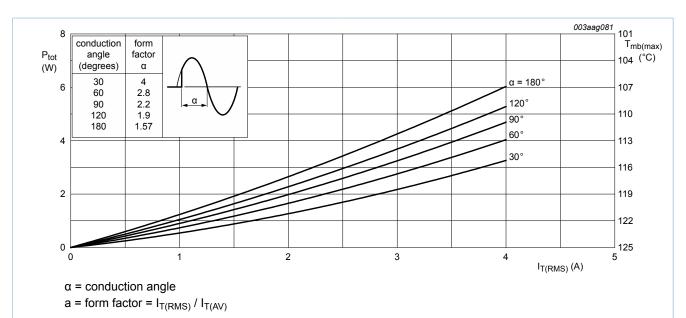


Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values

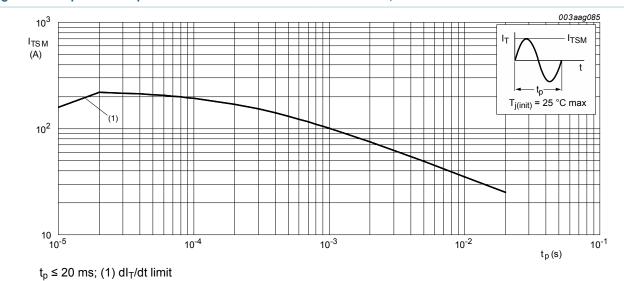


Fig. 4. Non-repetitive peak on-state current as a function of pulse width; maximum values

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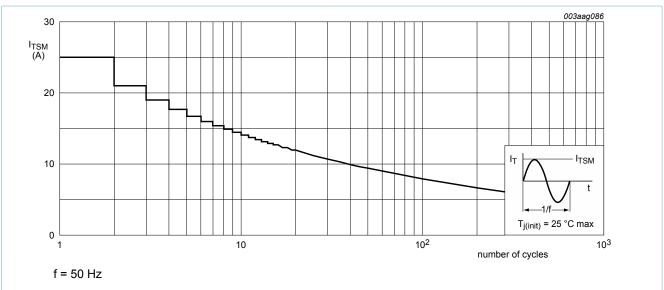


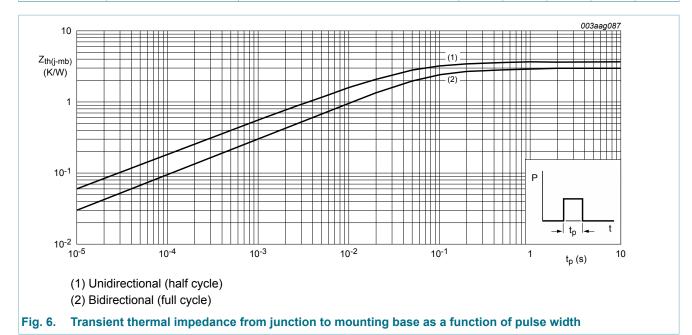
Fig. 5. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values

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8. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance	full cycle; Fig. 6	-	-	3	K/W
from junction to mounting base		half cycle; Fig. 6	-	-	3.7	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	-	60	-	K/W



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9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static char	acteristics		,			
I _{GT} gate trigger current	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ G+;$ $T_j = 25 \text{ °C}; Fig. 7$	-	-	25	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2 + \text{ G-;}$ $T_j = 25 \text{ °C; } Fig. 7$	-	-	25	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2\text{- G-;}$ $T_j = 25 \text{ °C; } Fig. 7$	-	-	25	mA
I _L latching current	$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G+;$ $T_j = 25 \text{ °C}; Fig. 8$	-	-	20	mA	
	$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G-;$ $T_j = 25 \text{ °C}; Fig. 8$	-	-	30	mA	
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 ^{\circ}\text{C}; \text{Fig. 8}$	-	-	20	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>	-	-	20	mA
V _T	on-state voltage	I _T = 5 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.4	1.7	V
V _{GT}	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 11	-	0.7	1	V
		V _D = 400 V; I _T = 0.1 A; T _j = 125 °C; Fig. 11	0.25	0.4	-	V
I _D	off-state current	V _D = 600 V; T _j = 125 °C	-	0.1	0.5	mA
Dynamic cl	haracteristics		1			
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T_j = 125 °C; (V_{DM} = 67% of V_{DRM}); exponential waveform; gate open circuit	50	-	-	V/µs
dl _{com} /dt	rate of change of commutating current	V_D = 400 V; T_j = 125 °C; $I_{T(RMS)}$ = 4 A; dV_{com}/dt = 10 V/ μ s; gate open circuit	3	-	-	A/ms
		V_D = 400 V; T_j = 125 °C; $I_{T(RMS)}$ = 4 A; dV_{com}/dt = 0.1 V/µs; gate open circuit	15	-	-	A/ms

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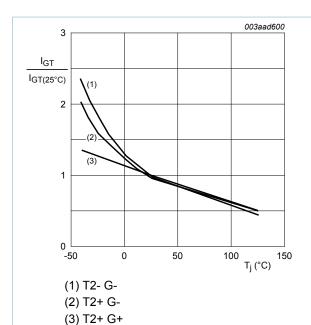


Fig. 8. Normalized latching current as a function of junction temperature

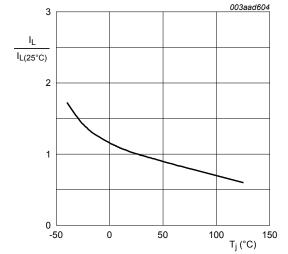


Fig. 7. Normalized gate trigger current as a function of junction temperature

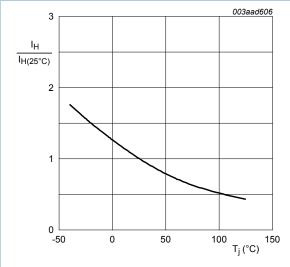
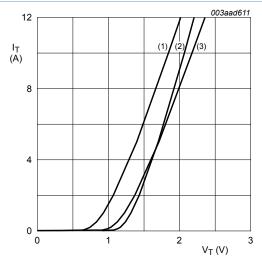


Fig. 9. Normalized holding current as a function of junction temperature



 V_{o} = 1.27 V; R_{s} = 0.091 Ω

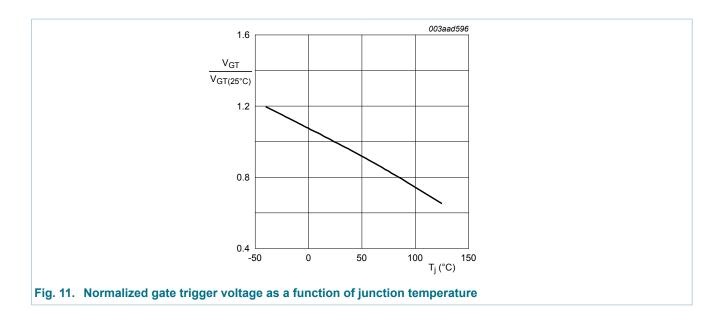
(1) T_j = 125 °C; typical values

(2) T_i = 125 °C; maximum values

(3) T_i = 25 °C; maximum values

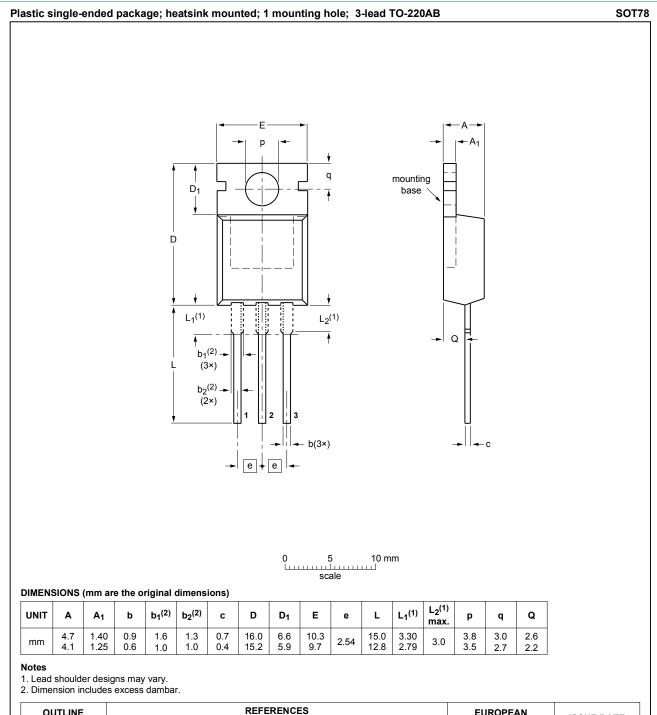
Fig. 10. On-state current as a function of on-state voltage

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10. Package outline



OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA	PROJECTION	1330E DATE
SOT78		3-lead TO-220AB	SC-46		08-04-23 08-06-13

Fig. 12. Package outline TO-220AB (SOT78)

BTA204-600F

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