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April 1st, 2010 Renesas Electronics Corporation

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RENESAS

MOS FIELD EFFECT TRANSISTOR 2SJ210

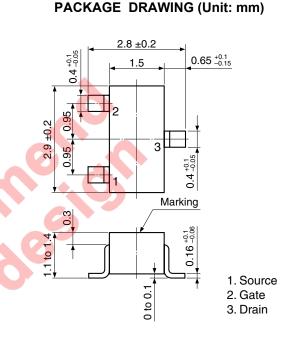
P-CHANNEL MOSFET FOR SWITCHING

The 2SJ210, P-channel vertical type MOSFET, is a switching device which can be driven directly by the output of ICs having a 5 V power source.

The 2SJ210 has excellent switching characteristics and is suitable as a high-speed switching device in digital circuits.

FEATURES

- Directly driven by the output of ICs having a 5 V power source.
- Not necessary to consider driving current because of its high input impedance.
- Possible to reduce the number of parts by omitting the bias resistor.



<R> ORDERING INFORMATION

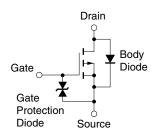
PART NUMBER	PACKAGE
2SJ210	SC-59 (Mini Mold)

Marking: H16

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	-60	V
Gate to Source Voltage (Vps = 0 V)	Vgss	∓20	V
Drain Current (DC)	D(DC)	∓200	mA
Drain Current (pulse) ^{Note}	D(pulse)	∓400	mA
Total Power Dissipation	Рт	200	mW
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C

EQUIVALENT CIRCUIT



Note PW \leq 10 ms, Duty Cycle \leq 50%

<R>

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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The mark <R> shows major revised points.

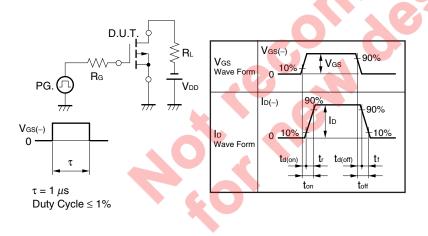
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

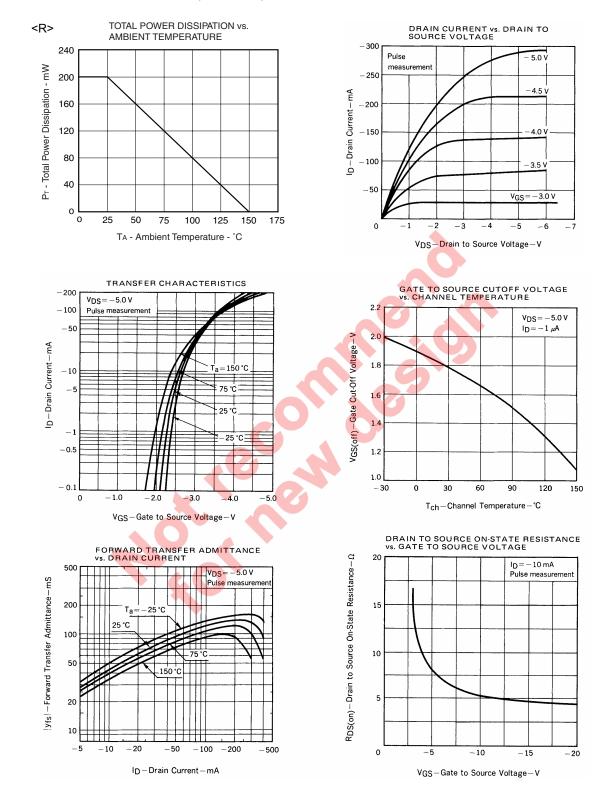
ELECTRICAL CHARACTERISTICS (T_A = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = -60 V, V _{GS} = 0 V			-1.0	μA
Gate Leakage Current	lgss	$V_{GS} = \mp 20 V$, $V_{DS} = 0 V$			∓1.0	μA
Gate Cut-off Voltage	V _{GS(off)}	$V_{DS} = -5.0 \text{ V}, \text{ ID} = -1.0 \mu\text{A}$	-1.4	-1.8	-2.4	V
Forward Transfer Admittance Note	y _{fs}	$V_{DS} = -5.0 \text{ V}, \text{ I}_{D} = -10 \text{ mA}$	20	45		mS
Drain to Source On-state Resistance Note	RDS(on)1	V_{GS} = -4.0 V, I _D = -10 mA		10	15	Ω
	RDS(on)2	V_{GS} = -10 V, I _D = -10 mA		6	10	Ω
Input Capacitance	Ciss	V _{DS} = -5.0 V		27		pF
Output Capacitance	Coss	V _{GS} = 0 V		21		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		3		pF
Turn-on Delay Time	td(on)	Vgs = -4.0 V, Rg = 10 Ω		120		ns
Rise Time	tr	V _{DD} = -5.0 V		190		ns
Turn-off Delay Time	td(off)	l₀ = –10 mA		150		ns
Fall Time	tr			180		ns

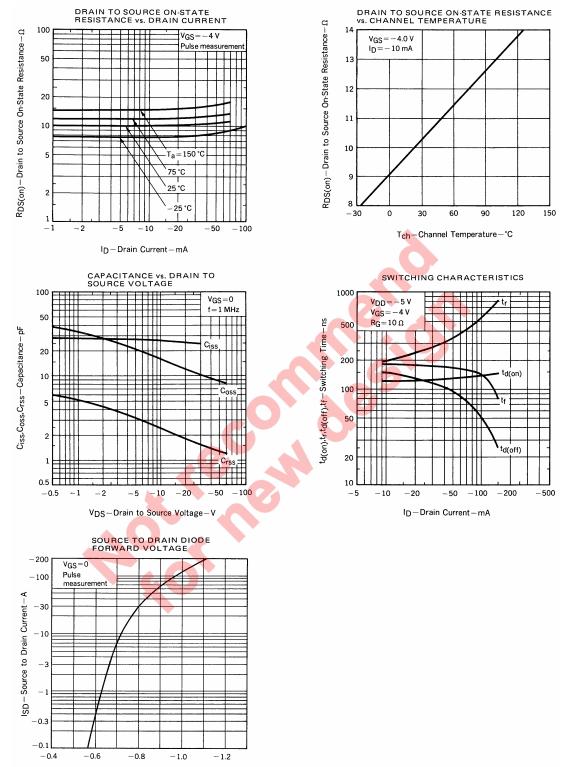
Note Pulsed

TEST CIRCUIT SWITCHING TIME





TYPICAL CHARACTERISTICS (TA = 25°C)



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