Not Recommended For New Designs HARRIS

HD-6433

CMOS Quad Bus Separator/Driver

> **Pinout** TOP VIEW

Features

- SINGLE POWER SUPPLY
- HIGH NOISE IMMUNITY
- INDUSTRIAL AND MILITARY GRADES
- DRIVE CAPACITY
- SOURCE CURRENT
- SINK CURRENT
- PROPAGATION DELAY

300pF 4mA

6mA

50nsec MAX.

2 → 🛮 2 A ĔвГ 10 |]38

Description

The HD-6433 is a self-aligned silicon gate CMOS bus separator/driver. This circuit consists of 8 drivers organized as 4 pairs of bus separators which allow a unidirectional input bus and a unidirectional output bus to be interfaced with a bi-directional bus.

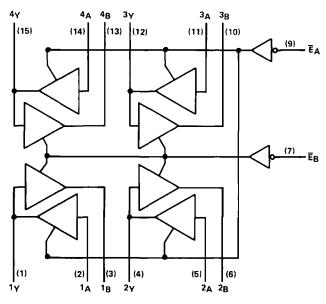
Outputs guaranteed valid at VCC 2.0V for Battery Backup Applications.

Truth Table

CONTROL INPUTS		FUNCTION		
ĒA	ĒB	А	В	Υ
L	L	- 1	О	0
L	н	1	D	0
н	L	D	O	1
		ISOLATED		

I = Input | O = Output, D = Disconnected

Functional Diagram



CAUTION Electronic devices are sensitive to electrostatic discharge. Proper I.C. handling procedures should be followed