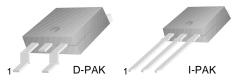


MJD32/32C

General Purpose Amplifier Low Speed Switching Applications D-PAK for Surface Mount Applications Load Formed for Surface Mount Application (No Suffix)

- Straight Lead (I-PAK, "- I" Suffix)
- Electrically Similar to Popular TIP32 and TIP32C



1.Base 2.Collector 3.Emitter

PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage	- 40	V	
	: MJD32	- 100	V	
	: MJD32C			
V_{CEO}	Collector-Emitter Voltage	- 40	V	
	: MJD32	- 100	V	
	: MJD32C			
V_{EBO}	Emitter-Base Voltage	- 5	V	
I _C	Collector Current (DC)	- 3	А	
I _{CP}	Collector Current (Pulse)	- 5	А	
I _B	Base Current	- 1	А	
P _C	Collector Dissipation (T _C =25°C)	15	W	
	Collector Dissipation (T _a =25°C)	1.56	W	
T _J	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	- 65 ~ 150	°C	

Electrical Characteristics $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units	
V _{CFO} (sus)	* Collector-Emitter Sustaining Voltage					
020	: MJD32	$I_C = -30 \text{mA}, I_B = 0$	-40		V	
	: MJD32C		-100		V	
I _{CEO}	Collector Cut-off Current					
	: MJD32	$V_{CE} = -40V, I_{B} = 0$		-50	μΑ	
	: MJD32C	$V_{CE} = -60V, I_{B} = 0$		-50	μΑ	
I _{CES}	Collector Cut-off Current					
	: MJD32	$V_{CE} = -40V, V_{BE} = 0$		-20	μΑ	
	: MJD32C	$V_{CE} = -100V, V_{BE} = 0$		-20	μΑ	
I _{EBO}	Emitter Cut-off Current	V _{BE} = - 5V, I _C = 0		-1	mA	
h _{FE}	* DC Current Gain	$V_{CE} = -4V, I_{C} = -1A$	25			
		$V_{CE} = -4V, I_{C} = -3A$	10	50		
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	I _C = - 3, I _B = - 375mA		-1.2	V	
V _{BE} (on)	* Base-Emitter ON Voltage	V _{CE} = - 4A, I _C = - 3A		-1.8	V	
f _T	Current Gain Bandwidth Product	$V_{CE} = -10V, I_{C} = -500mA$	3		MHz	

^{*} Pulse Test: PW≤300μs, Duty Cycle≤2%

Typical Characteristics

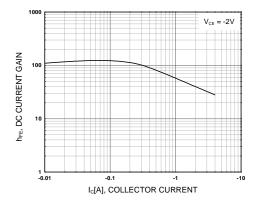


Figure 1. DC current Gain

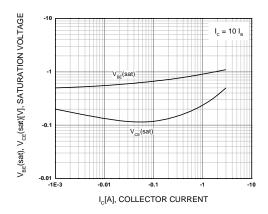


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

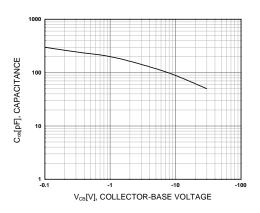


Figure 3. Collector Capacitance

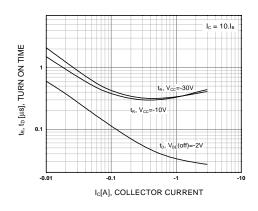


Figure 4. Turn On Time

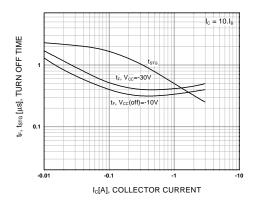


Figure 5. Turn Off Time

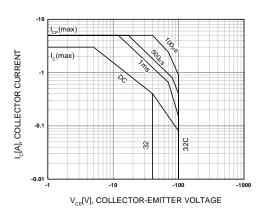


Figure 6. Safe Operating Area

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Typical Characteristics (Continued)

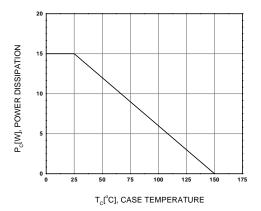
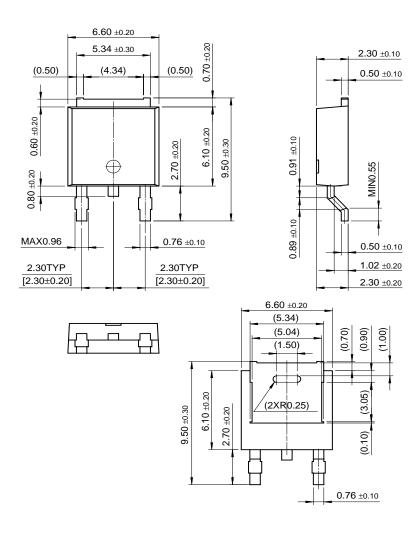


Figure 7. Power Derating

Package Demensions

D-PAK



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MJD32C

PNP Epitaxial Silicon Transistor

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Product status/pricing/packaging

BUY

Product	Product status	Pb-free Status	Pricing*	Package type	Leads	Packing method	Package Marking Convention**	
MJD32CTF	Full Production	Full Production	\$0.364	TO-252(DPAK)	2		Line 1: \$Y (Fairchild logo) & Z (Asm. Plant Code) & 4 (4-Digit Date Code) Line 3: MJD32C	
MJD32CTF_SBDD002A	Full Production	Full Production	N/A	TO-252(DPAK)	2	TAPE REEL	Line 1: \$Y (Fairchild logo) & Z (Asm. Plant Code) & 4 (4-Digit Date Code) Line 3: MJD32C	

^{*} Fairchild 1,000 piece Budgetary Pricing

^{**} A sample button will appear if the part is available through Fairchild's on-line samples program. If there is no sample button, please contact a Fairchild distributor to obtain samples



Indicates product with Pb-free second-level interconnect. For more information click here.

Package marking information for product MJD32C is available. Click here for more information.

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Models

Package & leads	Condition	Temperature range Software version R		Revision date
		PSPICE		
TO-252(DPAK)-2	Electrical/Thermal	-25°C to 100°C	9.2	Mar 7, 2001

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Qualification Support

Click on a product for detailed qualification data

Product
MJD32CTF
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