

IGBT

SGR6N60UF

Ultra-Fast IGBT

General Description

Fairchild's UF series of Insulated Gate Bipolar Transistors (IGBTs) provides low conduction and switching losses. The UF series is designed for applications such as motor control and general inverters where high speed switching is a required feature.

Features

- High speed switching
- Low saturation voltage : $V_{CE(sat)} = 2.1 \text{ V} @ I_C = 3A$
- · High input impedance

Applications

AC & DC motor controls, general purpose inverters, robotics, and servo controls.





Absolute Maximum Ratings T_C = 25°C unless otherwise noted

| Symbol | Description | | SGR6N60UF | Units | |
|---------------------|--|--------------------------|-------------|-------|--|
| V _{CES} | Collector-Emitter Voltage | | 600 | V | |
| V _{GES} | Gate-Emitter Voltage | | ± 20 | V | |
| I _C | Collector Current | @ T _C = 25°C | 6 | А | |
| | Collector Current | @ T _C = 100°C | 3 | Α | |
| I _{CM (1)} | Pulsed Collector Current | | 25 | А | |
| P_{D} | Maximum Power Dissipation | @ T _C = 25°C | 30 | W | |
| | Maximum Power Dissipation | @ T _C = 100°C | 12 | W | |
| T _J | Operating Junction Temperature | | -55 to +150 | °C | |
| T _{stg} | Storage Temperature Range | | -55 to +150 | °C | |
| TL | Maximum Lead Temp. for Soldering Purposes, 1/8" from Case for 5 Secon | nds | 300 | °C | |

(1) Repetitive rating : Pulse width limited by max. junction temperature

Thermal Characteristics

| Symbol | Parameter | Тур. | Max. | Units |
|-----------------|---|------|------|-------|
| $R_{\theta JC}$ | Thermal Resistance, Junction-to-Case | | 4.0 | °C/W |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (PCB Mount) (2) | 1 | 50 | °C/W |

Notes:
(2) Mounted on 1" squre PCB (FR4 or G-10 Material)

| Symbol | Parameter | Test Conditions | Min. | Тур. | Max. | Units |
|--|--|--|--------------------------------------|--|--|---|
| Off Cha | racteristics | , | * | , | • | |
| BV _{CES} | Collector-Emitter Breakdown Voltage | V _{GE} = 0V, I _C = 250uA | 600 | | | V |
| ΔB _{VCES} / | Temperature Coefficient of Breakdown | | | | | • |
| ΔT _J | Voltage | $V_{GE} = 0V, I_C = 1mA$ | | 0.6 | | V/°C |
| I _{CES} | Collector Cut-Off Current | $V_{CE} = V_{CES}, V_{GE} = 0V$ | | | 250 | uA |
| I _{GES} | G-E Leakage Current | $V_{GE} = V_{GES}, V_{CE} = 0V$ | | | ± 100 | nA |
| O Ob | | | | | | |
| | racteristics | 1 2 m 1 1/ | 2.5 | 4.5 | 0.5 | 17 |
| V _{GE(th)} | G-E Threshold Voltage | $I_C = 3\text{mA}, V_{CE} = V_{GE}$ | 3.5 | 4.5 | 6.5 | V |
| $V_{CE(sat)}$ | Collector to Emitter | $I_C = 3A$, $V_{GE} = 15V$ | | 2.1 | 2.6 | V |
| () | Saturation Voltage | $I_C = 6A$, $V_{GE} = 15V$ | | 2.6 | | V |
| Dynami | c Characteristics | | | | | |
| C _{ies} | Input Capacitance | | | 220 | | pF |
| C _{oes} | Output Capacitance | $V_{CE} = 30V, V_{GE} = 0V,$ | | 22 | | pF |
| C _{res} | Reverse Transfer Capacitance | f = 1MHz | | 7 | | pF |
| t _{d(on)} | ng Characteristics Turn-On Delay Time | | | 15 | | ns |
| t _r | Rise Time | - | | 25 | | 113 |
| t _{d(off)} | Turn-Off Delay Time | | | | | ns |
| ·u(OII) | | Voc = 300 V Io = 3A | | | 130 | ns ns |
| | , | $V_{CC} = 300 \text{ V}, I_{C} = 3A,$ $R_{C} = 800 \text{ V}, V_{CC} = 15 \text{ V}$ | | 60 | 130 | ns |
| t _f | Fall Time | $R_G = 80\Omega, V_{GE} = 15V,$ | | | | ns ns |
| t _f | Fall Time Turn-On Switching Loss | | | 60 70 | 130 150 | ns |
| t _f E _{on} E _{off} | Fall Time | $R_G = 80\Omega, V_{GE} = 15V,$ | | 60 70 57 | 130 150 | ns ns uJ |
| t _f E _{on} E _{off} E _{ts} | Fall Time Turn-On Switching Loss Turn-Off Switching Loss | $R_G = 80\Omega, V_{GE} = 15V,$ | | 60 70 57 25 | 130 150 | ns ns uJ uJ |
| t_f E_{on} E_{off} E_{ts} $t_{d(on)}$ | Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss | $R_G = 80\Omega, V_{GE} = 15V,$ | | 60 70 57 25 82 | 130 150 120 | ns ns uJ uJ |
| $\begin{aligned} & t_{\rm f} \\ & E_{\rm on} \\ & E_{\rm off} \\ & E_{\rm ts} \\ & t_{\rm d(on)} \\ & t_{\rm r} \end{aligned}$ | Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time | $R_G = 80\Omega$, $V_{GE} = 15V$, Inductive Load, $T_C = 25$ °C | | 60 70 57 25 82 22 | 130 150 120 | ns ns uJ uJ uJ ns |
| $\begin{aligned} & t_{\rm f} \\ & E_{\rm on} \\ & E_{\rm off} \\ & E_{\rm ts} \\ & t_{\rm d(on)} \\ & t_{\rm r} \\ & t_{\rm d(off)} \end{aligned}$ | Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time | $R_G = 80\Omega, V_{GE} = 15V,$ | | 60 70 57 25 82 22 32 | 130 150 120 | ns ns uJ uJ uJ ns |
| t _f E _{on} E _{off} Et _{ts} t _{d(on)} t _r t _{d(off)} | Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time | $R_G = 80\Omega$, $V_{GE} = 15V$, Inductive Load, $T_C = 25^{\circ}C$ $V_{CC} = 300 \text{ V}$, $I_C = 3A$, | | 60 70 57 25 82 22 32 80 | 130 150 120 200 | ns ns uJ uJ uJ ns ns |
| $\begin{array}{l} t_f \\ E_{on} \\ E_{off} \\ E_{ts} \\ \end{array}$ $\begin{array}{l} t_d(on) \\ t_r \\ t_d(off) \\ t_f \\ E_{on} \end{array}$ | Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time | $R_G = 80\Omega, \ V_{GE} = 15V,$ Inductive Load, $T_C = 25^{\circ}C$ $V_{CC} = 300 \ V, \ I_C = 3A,$ $R_G = 80\Omega, \ V_{GE} = 15V,$ | | 60 70 57 25 82 22 32 80 122 | 130 150 120 200 300 | ns ns uJ uJ ns ns ns ns |
| t _f Eon Eoff Ets td(on) tr td(off) t _f Eon Eoff | Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss | $R_G = 80\Omega, \ V_{GE} = 15V,$ Inductive Load, $T_C = 25^{\circ}C$ $V_{CC} = 300 \ V, \ I_C = 3A,$ $R_G = 80\Omega, \ V_{GE} = 15V,$ | | 60 70 57 25 82 22 32 80 122 65 | 130 150 120 200 300 | ns uJ uJ ns ns ns us uJ uJ |
| t _f Eon Eoff Ets td(on) tr td(off) t _f Eon Eoff Ets | Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Turn-Off Switching Loss | $R_G=80\Omega,\ V_{GE}=15V,$ Inductive Load, $T_C=25^{\circ}C$ $V_{CC}=300\ V,\ I_C=3A,$ $R_G=80\Omega,\ V_{GE}=15V,$ Inductive Load, $T_C=125^{\circ}C$ | | 60 70 57 25 82 22 32 80 122 65 | 130 150 120 200 300 | ns ns uJ uJ ns ns ns ns us |
| t _f Eon Eoff Ets td(on) t _r td(off) t _f Eon Eoff Eon Eoff Ets Q _q | Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss | $R_G = 80\Omega, \ V_{GE} = 15V,$ Inductive Load, $T_C = 25^{\circ}C$ $V_{CC} = 300 \ V, \ I_C = 3A,$ $R_G = 80\Omega, \ V_{GE} = 15V,$ Inductive Load, $T_C = 125^{\circ}C$ $V_{CE} = 300 \ V, \ I_C = 3A,$ | | 60 70 57 25 82 22 32 80 122 65 46 | 130 150 120 200 300 170 | ns ns uJ uJ ns ns ns ns us |
| t _f Eon Eoff Ets t _d (on) t _r t _d (off) t _f Eon Eon Q _g Q _{gc} | Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Turn-On Delay Time Rise Time Turn-Off Delay Time Fall Time Turn-On Switching Loss Turn-Off Switching Loss Total Switching Loss Total Gate Charge | $R_G=80\Omega,\ V_{GE}=15V,$ Inductive Load, $T_C=25^{\circ}C$ $V_{CC}=300\ V,\ I_C=3A,$ $R_G=80\Omega,\ V_{GE}=15V,$ Inductive Load, $T_C=125^{\circ}C$ | | 60 70 57 25 82 22 32 80 122 65 46 111 15 | 130 150 120 200 300 170 22 | ns ns uJ uJ ns ns ns us us ns ns us |

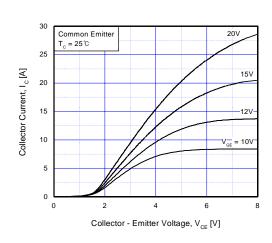


Fig 1. Typical Output Characteristics

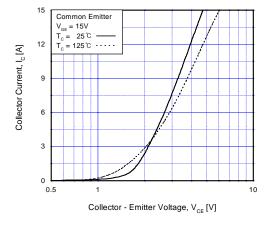


Fig 2. Typical Saturation Voltage Characteristics

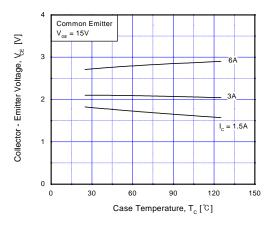


Fig 3. Saturation Voltage vs. Case
Temperature at Variant Current Level

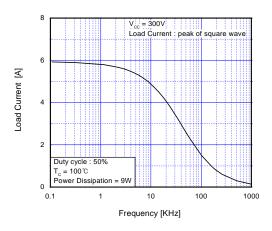


Fig 4. Load Current vs. Frequency

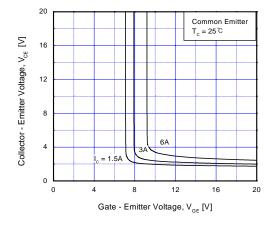


Fig 5. Saturation Voltage vs. V_{GE}

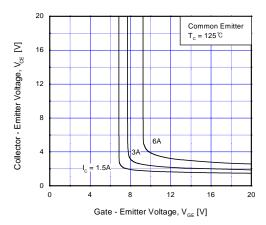


Fig 6. Saturation Voltage vs. $V_{\rm GE}$

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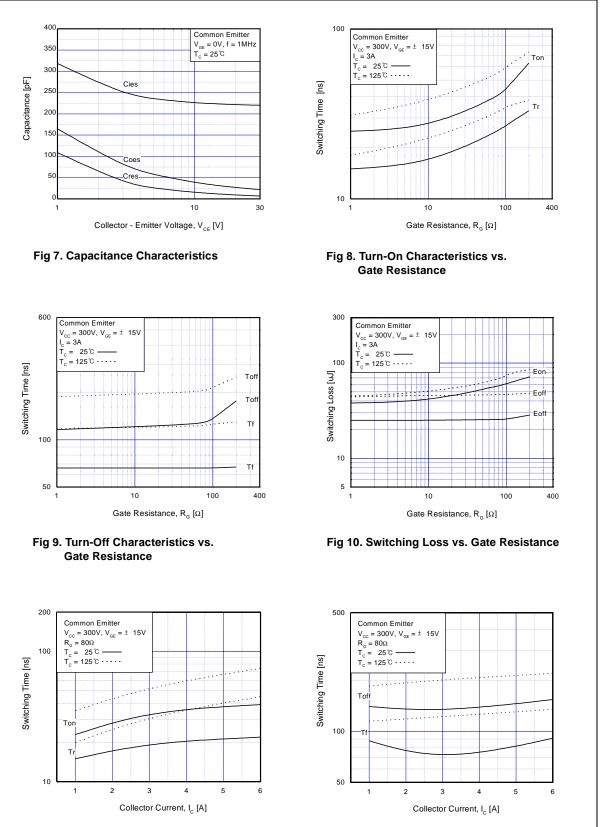
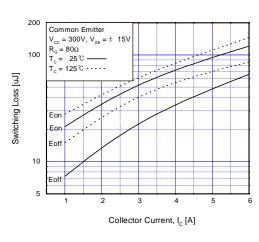


Fig 11. Turn-On Characteristics vs.

Collector Current

Fig 12. Turn-Off Characteristics vs. Collector Current



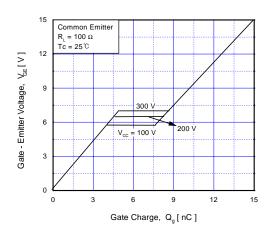
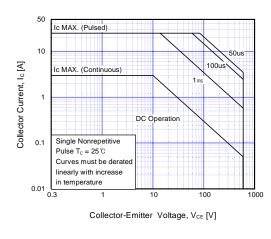


Fig 13. Switching Loss vs. Collector Current

Fig 14. Gate Charge Characteristics



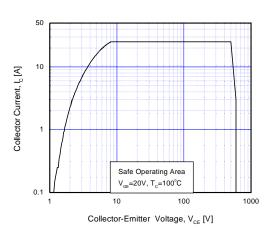


Fig 15. SOA Characteristics

Fig 16. Turn-Off SOA Characteristics

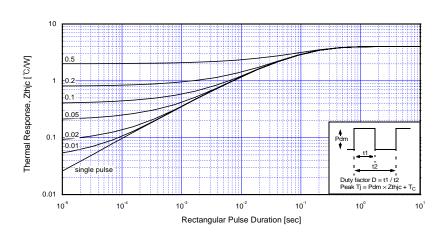
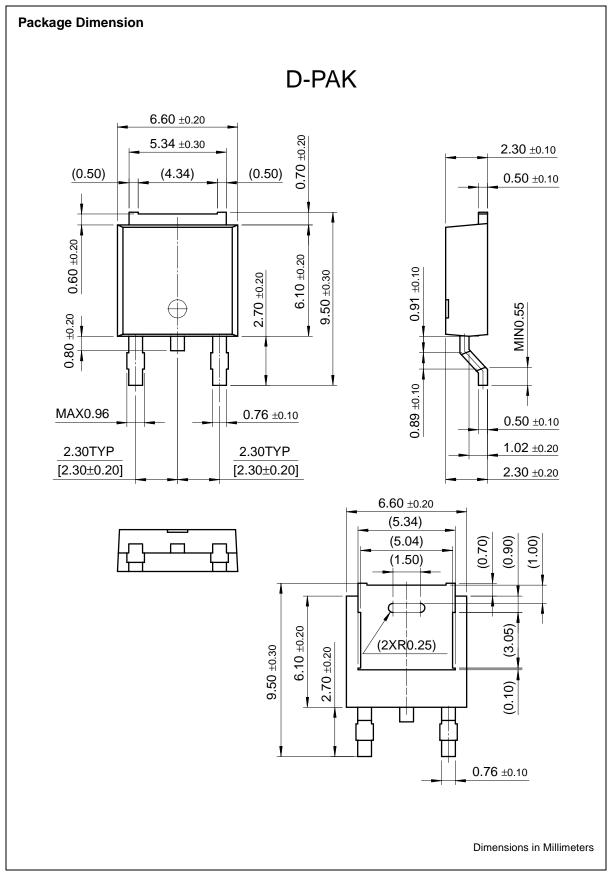


Fig 17. Transient Thermal Impedance of IGBT



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Product status Pricing* Package type Leads Packing method

Product status/pricing/packaging

Product Folder - Fairchild P/N SGR6N60UF - Discrete, High Performance IGBT

| SGR6N60UFTF | Full Production | \$0.59 | TO-252(DPAK) | 2 | TAPE REEL |
|-------------|-----------------|--------|--------------|---|-----------|
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^{* 1,000} piece Budgetary Pricing

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