

DM74LS136MX

Quad 2-Input Exclusive-OR Gate with Open-Collector Outputs

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This device contains four independent gates, each of which performs the logic exclusive-OR function.

Rochester Electronics Manufactured Components	Quality Overview ISO-9001 AS9120 certification
Rochester branded components are manufactured using either die/wafers purchased from the original suppliers	 Qualified Manufacturers List (QML) MIL-PRF-35835 Class Q Military Class V Space Level
or Rochester wafers recreated from the original IP. All re-creations are done with the approval of the Original Component Manufacturer (OCM).	 Qualified Suppliers List of Distributors (QSLD) Rochester is a critical supplier to DLA and meets all industry and DLA standards.
Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.	Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OCM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

FOR REFERENCE ONLY

October 1988 Revised March 2000

DM74LS136 Quad 2-Input Exclusive-OR Gate with Open-Collector Outputs

General Description

FAIRCHILD

SEMICONDUCTOR

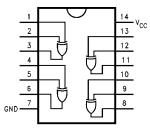
This device contains four independent gates, each of which performs the logic exclusive-OR function.

Ordering Code:

Order Number Package Number Package Description			
DM74LS136M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow	
DM74LS136N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide	

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Truth Table

Inp	uts	Output	
Α	В	Z	
L	L	L	
L	н	н	
н	L	н	
н	н	L	

H = HIGH Voltage Level L = LOW Voltage Level

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Absolute Maximum Ratings(Note 1)

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	-65°C to +150°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
/ _{IL}	LOW Level Input Voltage			0.8	V
OL	LOW Level Output Current			8	mA
Γ _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

Over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.5	V
I _{CEX}	HIGH Level Output Current	$V_{CC} = Min, V_O = 5.5V$			100	μΑ
V _{OL}	LOW Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min$		0.35	0.5	V
		$I_{OL} = 4 \text{ mA}, V_{CC} = Min$		0.25	0.4	
I _I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			0.2	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			40	μΑ
IIL	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.6	mA
I _{CC}	Supply Current	V _{CC} = Max			10	mA

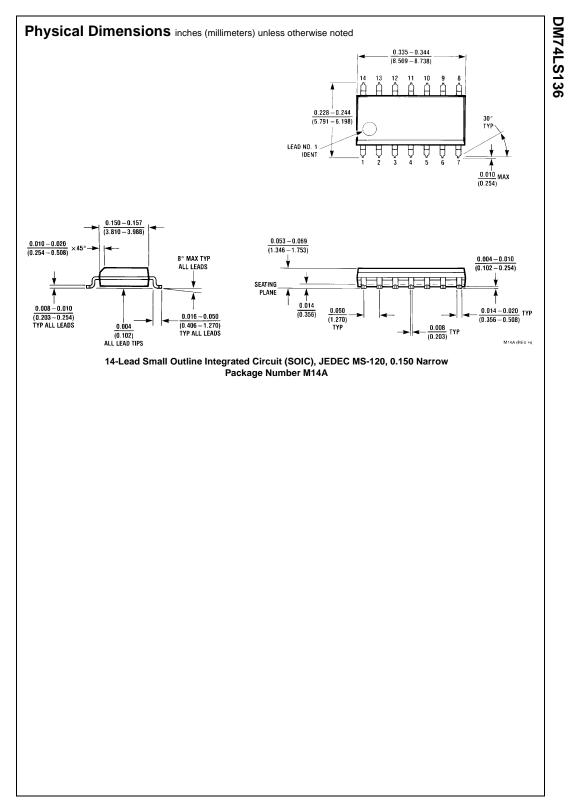
Note 2: All typicals are at V_{CC} = 5V, T_A = 25^{\circ}C.

Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Switching Characteristics

at $V_{CC}=5V$ and $T_A=25^\circ C$

		R _L =		
Symbol	Symbol Parameter		C _L = 15 pF	
		Min	Max	
t _{PLH}	Propagation Delay Time		23	ns
	LOW-to-HIGH Level Output		25	
t _{PHL}	Propagation Delay Time		23	ns
	HIGH-to-LOW Level Output		23	115



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