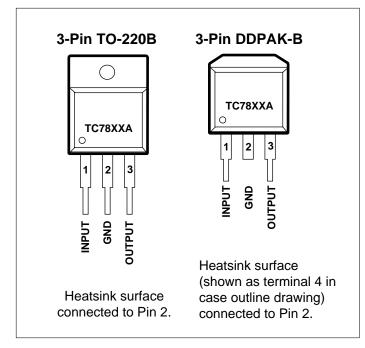


# **Three-Terminal Positive Voltage Regulators**

### FEATURES

- Output Current in Excess of 1.0A
- No External Components Required
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe–Area Compensation
- Output Voltage Offered in 2% Tolerance
- Available in Surface Mount DDPAK and Standard 3–Lead Transistor Packages
- Previous Commercial Temperature Range has been Extended to a Junction Temperature Range of -40°C to +125°C



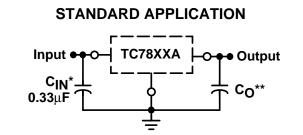
### **GENERAL DESCRIPTION**

These voltage regulators are monolithic integrated circuits designed as fixed–voltage regulators for a wide variety of applications including local, on–card regulation. These regulators employ internal current limiting, thermal shutdown, and safe–area compensation. With adequate heatsinking they can deliver output currents in excess of 1.0A. Although designed primarily as a fixed voltage regulator, these devices can be used with external components to obtain adjustable voltages and currents.

### ORDERING INFORMATION

Package	Range
3-Pin TO-220B	–40° to + 125°C
3-Pin TO-220B	–40° to + 125°C
3-Pin TO-220B	–40° to + 125°C
3-Pin DDPAK-B	–40° to + 125°C
3-Pin DDPAK-B	–40° to + 125°C
3-Pin DDPAK-B	–40° to + 125°C
3-Pin DDPAK-B	–40° to + 125°C
	3-Pin TO-220B 3-Pin TO-220B 3-Pin DDPAK-B 3-Pin DDPAK-B 3-Pin DDPAK-B

Note: Contact company about other voltage and package options.



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.

- XX, These two digits of the type number indicate nominal voltage.
- C<sub>IN</sub> is required if regulator is located an appreciable distance from power supply filter.
- \*\* C<sub>O</sub> is not needed for stability; however, it does improve transient response. Values of less than 0.1µF could cause instability.

### **ABSOLUTE MAXIMUM RATINGS\***

(T <sub>A</sub> = 25°C, unless otherwise noted.) Input Voltage	
(5.0 – 18V)	$V_{IN} = 35V_{DC}$
(24V)	
Power Dissipation ( $T_A = 25^{\circ}C$ ) $P_D = International Content of the second sec$	Illy Limited W
Case TO-220B	
Thermal Resistance,	
Junction-to-Ambient	$\theta_{JA} = 65^{\circ}C/W$
Junction-to-Case θ	$J_{\rm C} = 5.0^{\circ} {\rm C/W}$

Power Dissipation ( $T_A = 25^{\circ}C$ ) PD = Internally Limited W DDPAK-B
Thermal Resistance,
Junction-to-Ambient $\theta_{JA} = (See Figure 13) C/W$
Junction-to-Case $\theta_{JA} = 5.0^{\circ}$ C/W
Storage Junction
Temperature RangeT <sub>STG</sub> = -65°C to +150°C
Operating Junction Temperature $T_J = +150^{\circ}C$
*Note: ESD Data Available upon request.

## **ELECTRICAL CHARACTERISTICS:** (V<sub>IN</sub> = 10V, I<sub>OUT</sub> = 1.0A, T<sub>J</sub> = T<sub>LOW</sub> to T<sub>HIGH</sub> [Note 1], unless otherwise noted.)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
TC7805A			1	1		1
Vout	Output Voltage	$T_J = 25^{\circ}C$	4.9	5.0	5.1	V <sub>DC</sub>
V <sub>OUT</sub>	Output Voltage	$\begin{array}{l} 5.0mA \leq I_{OUT} \leq 0.1A, \ P_D \leq 15W \\ 7.5V_{DC} \leq V_{IN} \leq 20V_{DC} \end{array}$	4.8	5.0	5.2	V <sub>DC</sub>
REG <sub>LINE</sub>	Line Regulation	Note 2				mV
		$7.5V_{DC} \le V_{IN} \le 25V_{DC}$ , $I_{OUT} = 500mA$	_	0.5	10	
		$8.0V_{DC} \le V_{IN} \le 12V_{DC}$ , $I_{OUT} = 1.0A$	-	0.8	12	
		$8.0V_{DC} \le V_{IN} \le 12V_{DC}$ , $I_{OUT} = 1.0A$ , $T_{J} = 25^{\circ}C$	—	1.3	4.0	
		$7.3V_{DC} \leq V_{IN} \leq 20V_{DC}, \ I_{OUT} = 1.0A, T_J = 25^{\circ}C$	-	4.5	10	
REG <sub>LOAD</sub>	Load Regulation	Note 2				mV
		$5.0 \text{mA} \le I_{OUT} \le 1.5 \text{A}, \text{ T}_{J} = 25^{\circ} \text{C}$	_	1.3	25	
		$5.0\text{mA} \le I_{OUT} \le 1.0\text{A}$	_	0.8	25	
		$250mA \le I_{OUT} \le 750mA$	—	0.53	15	
I <sub>B</sub>	Quiescent Current		—	3.2	6.0	mA
$\Delta I_B$	Quiescent Current Change	$8.0V_{DC} \le V_{IN} \le 25V_{DC}$ , $I_{OUT} = 500mA$	_	0.3	0.8	mA
		$7.5V_{DC} \le V_{IN} \le 20V_{DC}, T_J = 25^{\circ}C$	_	—	0.8	
		$5.0\text{mA} \le I_{OUT} \le 1.0\text{A}$	—	0.08	0.5	
RR	Ripple Rejection	$8.0V_{DC} \le V_{IN} \le 18V_{DC}, f = 120Hz, I_{OUT} = 500mA$	68	83	_	dB
$V_{IN} - V_{OUT}$	Dropout Voltage	$I_{OUT} = 1.0A, T_{J} = 25^{\circ}C$	—	2.0	—	V <sub>DC</sub>
V <sub>N</sub>	Output Noise Voltage	$T_A = 25^{\circ}C$ 10Hz $\leq f \leq 100$ kHz		10	—	μV/V <sub>OUT</sub>
Rout	Output Resistance	f = 1.0kHz	_	0.9	_	mΩ
I <sub>SC</sub>	Short Circuit Current Limit	T <sub>A</sub> = 25°C		0.2		A
		$V_{IN} = 35V_{DC}$				
I <sub>MAX</sub>	Peak Output Current	$T_J = 25^{\circ}C$	—	2.2	_	A
TCV <sub>OUT</sub>	Average Temperature					
	Coefficientof Output Voltage		—	-0.3	_	mV/°C

**NOTES:** 1.  $T_{LOW} = -40^{\circ}C$  for TC78XXA,  $T_{HIGH} = +125^{\circ}C$  for TC78XX

2. Load and line regulation are specified at constant junction temperature. Changes in V<sub>OUT</sub> due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
TC7812A					1	
V <sub>OUT</sub>	Output Voltage	$T_J = 25^{\circ}C$	11.75	12	12.25	V <sub>DC</sub>
V <sub>OUT</sub>	Output Voltage	$\begin{array}{l} 5.0\text{mA} \leq I_{OUT} \leq 0.1\text{A}, \ P_D \leq 15\text{W} \\ 14.8\text{V}_{DC} \leq \text{V}_{IN} \leq 27\text{V}_{DC} \end{array}$	11.5	12	12.5	V <sub>DC</sub>
REG <sub>LINE</sub>	Line Regulation			3.8 2.2 6.0	18 20 120	mV
REG <sub>LOAD</sub>	Load Regulation	Note 2 5.0mA $\leq I_{OUT} \leq$ 1.5A, T <sub>J</sub> = 25°C 5.0mA $\leq I_{OUT} \leq$ 1.0A			25 25	mV
I <sub>B</sub>	Quiescent Current		—	3.4	6.0	mA
$\Delta I_B$	Quiescent Current Change	$\begin{array}{l} 15V_{DC} \leq V_{IN} \leq 30V_{DC}, \ I_{OUT} = 500 mA \\ 14.8V_{DC} \leq V_{IN} \leq 27V_{DC}, \ T_J = 25^{\circ}C \\ 5.0mA \leq I_{OUT} \leq 1.0A, \ T_J = 25^{\circ}C \end{array}$			0.8 0.8 0.5	mA
RR	Ripple Rejection	$15V_{DC} \le V_{IN} \le 25V_{DC}, f = 120Hz, I_{OUT} = 500mA$	55	60		dB
VIN-VOUT	Dropout Voltage	I <sub>OUT</sub> = 1.0A, T <sub>J</sub> = 25°C	_	2.0	_	V <sub>DC</sub>
V <sub>N</sub>	Output Noise Voltage	$T_A = 25^{\circ}C$ 10Hz $\leq f \leq 100$ kHz	_	10	—	μV/V <sub>OUT</sub>
R <sub>OUT</sub>	Output Resistance	f = 1.0kHz	—	1.1	—	mΩ
I <sub>SC</sub>	Short Circuit Current Limit	$T_A = 25^{\circ}C$ $V_{IN} = 35V_{DC}$	_	0.2		A
I <sub>MAX</sub>	Peak Output Current	$T_J = 25^{\circ}C$	—	2.2	_	A
TCV <sub>OUT</sub>	Average Temperature Coefficient of Output Voltage		_	-0.8	_	mV/°C

NOTES: 1. T<sub>LOW</sub> = -40°C for TC78XXA, T<sub>HIGH</sub> = +125°C for TC78XX 2. Load and line regulation are specified at constant junction temperature. Changes in V<sub>OUT</sub> due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
TC7815A				1		1
Vout	Output Voltage	$T_J = 25^{\circ}C$	14.7	15	15.3	V <sub>DC</sub>
V <sub>OUT</sub>	Output Voltage	$\begin{array}{l} 5.0mA \leq I_{OUT} \leq 0.1A, \ P_D \leq 15W \\ 17.9V_{DC} \leq V_{IN} \leq 30V_{DC} \end{array}$	14.4	15	15.6	V <sub>DC</sub>
REG <sub>LINE</sub>	Line Regulation	Note 2				mV
		$\begin{array}{l} 17.9V_{DC} \leq V_{IN} \leq 30V_{DC}, \ I_{OUT} = 500 \text{mA} \\ 20V_{DC} \leq V_{IN} \leq 26V_{DC} \end{array}$	_	8.5 3.0	20 22	
		$17.5V_{DC} \le V_{IN} \le 30V_{DC}, I_{OUT} = 1.0A, T_J = 25^{\circ}C$		7.0	20	
REG <sub>LOAD</sub>	Load Regulation	Note 2 $5.0\text{mA} \le I_{\text{OUT}} \le 1.5\text{A}, T_{\text{J}} = 25^{\circ}\text{C}$ $5.0\text{mA} \le I_{\text{OUT}} \le 1.0\text{A}$ $250\text{mA} \le I_{\text{OUT}} \le 750\text{mA}$		1.8 1.5 1.2	25 25 15	mV
I <sub>B</sub>	Quiescent Current			3.5	6.0	mA
$\Delta I_B$	Quiescent Current Change	$\begin{array}{l} 17.5V_{DC} \leq V_{IN} \leq 30V_{DC}, \ I_{OUT} = 500 mA \\ 17.5V_{DC} \leq V_{IN} \leq 30V_{DC}, \ I_{OUT} = 1.0A, \ T_{J} = 25^{\circ}C \\ 5.0 mA \leq I_{OUT} \leq 1.0A \end{array}$			0.8 0.8 0.5	mA
RR	Ripple Rejection	$18.5V_{DC} \le V_{IN} \le 28.5V_{DC}$ , f = 120Hz, I <sub>OUT</sub> = 500mA	60	80	_	dB
VIN - VOUT	Dropout Voltage	I <sub>OUT</sub> = 1.0A, T <sub>J</sub> = 25°C		2.0	_	V <sub>DC</sub>
V <sub>N</sub>	Output Noise Voltage	$T_A = 25^{\circ}C$ 10Hz $\leq f \leq 10kHz$	_	10	—	μV/V <sub>OUT</sub>
Rout	Output Resistance	f = 1.0kHz	_	1.2	_	mΩ
I <sub>SC</sub>	Short Circuit Current Limit	$T_A = 25^{\circ}C$ $V_{IN} = 35V_{DC}$	_	0.2	—	A
I <sub>MAX</sub>	Peak Output Current	$T_J = 25^{\circ}C$	_	2.2	-	A
TCV <sub>OUT</sub>	Average Temperature Coefficient of Output Voltage	e	_	-1.0	_	mV/°C

**NOTES:** 1.  $T_{LOW} = -40^{\circ}C$  for TC78XXA,  $T_{HIGH} = +125^{\circ}C$  for TC78XX

 Load and line regulation are specified at constant junction temperature. Changes in V<sub>OUT</sub> due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

# **Three-Terminal Positive Voltage Regulators**

## **TC7800A Series**

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
TC7824A			1			
V <sub>OUT</sub>	Output Voltage	$T_J = 25^{\circ}C$	23.5	24	24.5	V <sub>DC</sub>
V <sub>OUT</sub>	Output Voltage	$\begin{array}{l} \text{5.0mA} \leq I_{OUT} \leq 0.1\text{A}, \ \text{P}_{D} \leq 15\text{W} \\ \text{27.3V}_{DC} \leq \text{V}_{\text{IN}} \leq 38\text{V}_{DC} \end{array}$	23.2	24	25.8	V <sub>DC</sub>
REG <sub>LINE</sub>	Line Regulation	$\begin{array}{l} \mbox{Note 2} \\ 27V_{DC} \leq V_{IN} \leq 38V_{DC}, \ \mbox{I}_{OUT} = 500 \mbox{mA} \\ 30V_{DC} \leq V_{IN} \leq 36V_{DC}, \ \mbox{I}_{OUT} = 1.0 \mbox{A} \\ 30V_{DC} \leq V_{IN} \leq 36V_{DC}, \ \mbox{T}_{J} = 25^{\circ} \mbox{C} \\ 26.7V_{DC} \leq V_{IN} \leq 38V_{DC}, \ \mbox{I}_{OUT} = 1.0 \mbox{A}, \ \mbox{T}_{J} = 25^{\circ} \mbox{C} \end{array}$		11.5 3.8 3.8 10	25 28 12 25	mV
REG <sub>LOAD</sub>	Load Regulation	Note 2 $5.0\text{mA} \le I_{\text{OUT}} \le 1.5\text{A}$ , $T_J = 25^{\circ}\text{C}$ $5.0\text{mA} \le I_{\text{OUT}} \le 1.0\text{A}$ $250\text{mA} \le I_{\text{OUT}} \le 750\text{mA}$		2.1 2.0 1.8	15 25 15	mV
I <sub>B</sub>	Quiescent Current		_	3.6	6.0	mA
$\Delta I_B$	Quiescent Current Change	$\begin{array}{l} 27.3V_{DC} \leq V_{IN} \leq 38V_{DC}, \ I_{OUT} = 500 \text{mA} \\ 27V_{DC} \leq V_{IN} \leq 38V_{DC}, \ T_J = 25^\circ\text{C} \\ 5.0\text{mA} \leq I_{OUT} \leq 1.0\text{A} \end{array}$			0.8 0.8 0.5	mA
RR	Ripple Rejection	$28V_{DC} \le V_{IN} \le 38V_{DC}$ , f = 120Hz, I <sub>OUT</sub> = 500mA	45	54	_	dB
VIN-VOUT	Dropout Voltage	I <sub>OUT</sub> = 1.0A, T <sub>J</sub> = 25°C		2.0	_	V <sub>DC</sub>
V <sub>N</sub>	Output Noise Voltage	$T_A = 25^{\circ}C$ 10Hz $\leq f \leq 100$ kHz	_	10	_	μV/V <sub>OUT</sub>
R <sub>OUT</sub>	Output Resistance	f = 1.0kHz		1.4	_	mΩ
I <sub>SC</sub>	Short Circuit Current Limit	$T_A = 25^{\circ}C$ V <sub>IN</sub> = 35V <sub>DC</sub>	_	0.2	—	A
I <sub>MAX</sub>	Peak Output Current	$T_J = 25^{\circ}C$		2.2	_	A
TCV <sub>OUT</sub>	Average Temperature Coefficient of Output Voltage		_	-2.0	_	mV/°C

**NOTES:** 1.  $T_{LOW} = -40^{\circ}C$  for TC78XXA,  $T_{HIGH} = +125^{\circ}C$  for TC78XX

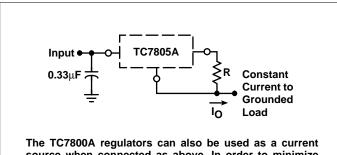
2. Load and line regulation are specified at constant junction temperature. Changes in V<sub>OUT</sub> due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

## **APPLICATIONS INFORMATION**

#### **Design Considerations**

The TC7800A Series of fixed voltage regulators are designed with Thermal Overload Protection that shuts down the circuit when subjected to an excessive power overload condition, Internal Short Circuit Protection that limits the maximum current the circuit will pass, and Output Transistor Safe–Area Compensation that reduces the output short circuit current as the voltage across the pass transistor is increased.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long wire lengths, or if the output load capacitance is large. An input bypass capacitor should be selected to provide good high–frequency characteristics to insure stable operation under all load conditions. A  $0.33\mu$ F or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with the shortest possible leads directly across the regulators input terminals. Normally good construction techniques should be used to minimize ground loops and lead resistance drops since the regulator has no external sense lead.



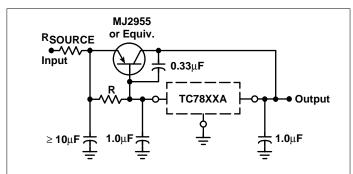
source when connected as above. In order to minimize dissipation the TC7805A is chosen in this application. Resistor R determines the current as follows:

$$I_{OUT} = \frac{5.0V}{R} = I_B$$

 $I_B \cong 3.2mA$  over line and load charges.

For example, a 1.0A current source would require R to be a 5.0 $\Omega$ , 10W resistor and the output voltage compliance would be the input voltage less 7.0V.

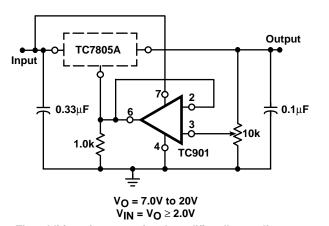
Figure 1. Current Regulator



XX = 2 digits of type number indicating voltage.

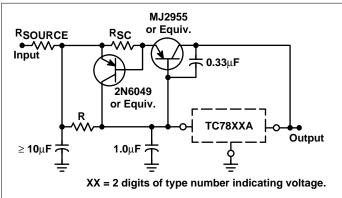
The TC7800A series can be current boosted with a PNP transistor. The MJ2955 provides current to 5.0A. Resistor R in conjunction with the V<sub>BE</sub> of the PNP determines when the pass transistor begins conducting; this circuitis not short circuit proof. Input/output differential voltage minimum is increased by V<sub>BE</sub> of the pass transistor.

Figure 3. Current Boost Regulator



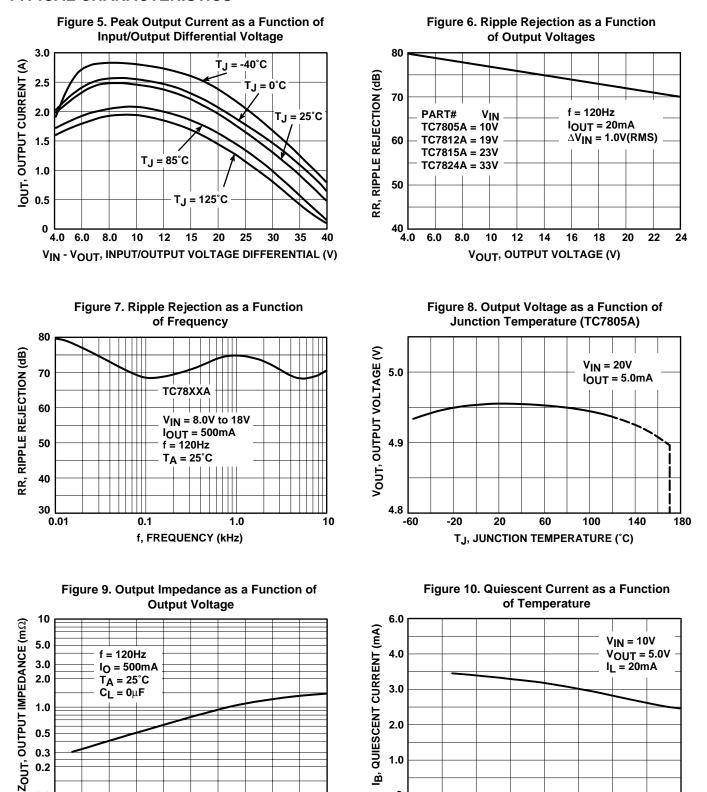
The addition of an operational amplifier allows adjustment to higher or intermediate values while retaining regulation characteristics. The minimum voltage obtainable with this arrangement is 2.0V greater than the regulator voltage.

Figure 2. Adjustable Output Regulator



The circuit of Figure 3 can be modified to provide supply protection against short circuits by adding a short circuit sense resistor,  $R_{SC}$ , and an additional PNP transistor.The current sensing PNP must be able to handle the short circuit current of the three-terminal regulator. Therefore, a four-ampere plastic power transistor is specified.

Figure 4. Short Circuit Protection



### TYPICAL CHARACTERISTICS

8.0

12

VOUT, OUTPUT VOLTAGE (V)

16

20

0.2

0.1

4.0

125

100

24

ھ

0 ∟ -75

-50

-25

0

25

TJ, JUNCTION TEMPERATURE (°C)

50

75

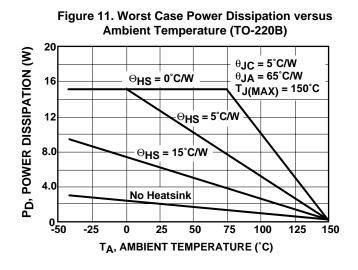
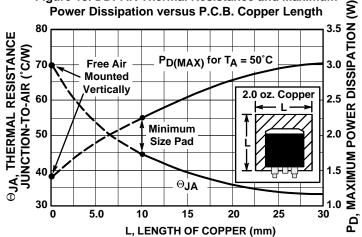
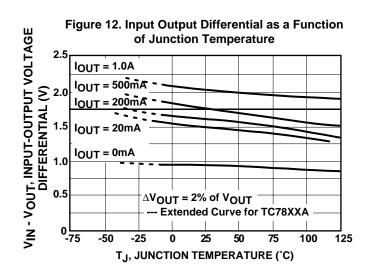


Figure 13. DDPAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length





### DEFINITIONS

Line Regulation - The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

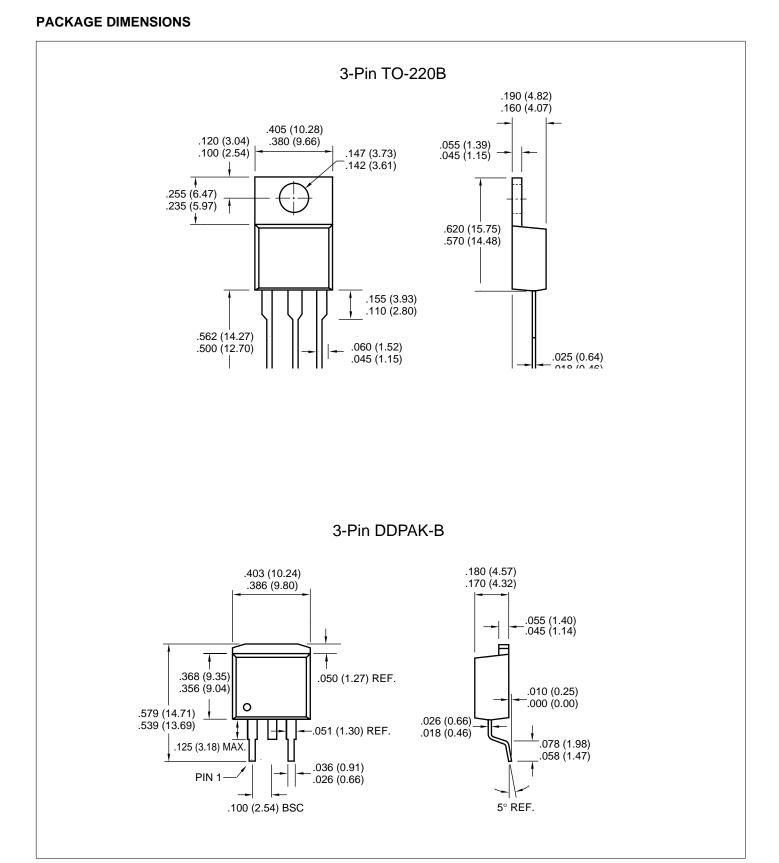
Load Regulation – The change in output voltage for a change in the load current at constant chip temperature.

Maximum Power Dissipation - The maximum total device dissipation for which the regulator will operate within specifications.

Quiescent Current – That part of the input current that is not delivered to the load.

Output Noise Voltage - The rms AC voltage at the output, with constant load and no input ripple, measured over a specified frequency range.

Long Term Stability - Output voltage stability under accelerated life test conditions with the maximum rated voltage listed in the devices' electrical characteristics and maximum power dissipation.



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