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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR 2SK3299B

SWITCHING N-CHANNEL POWER MOSFET

DESCRIPTION

The 2SK3299B is N-channel MOSFET device that features a low gate charge and excellent switching characteristics, and designed for high voltage applications such as switching power supply, AC adapter.

FEATURES

· Low gate charge

 $Q_G = 30 \text{ nC TYP.} (V_{DD} = 450 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A})$

- Gate voltage rating: ±30 V
- Low on-state resistance

 $R_{DS(on)}$ = 0.75 Ω MAX. (Vgs = 10 V, ID = 5.0 A)

· Avalanche capability ratings

ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE
2SK3299B-S19-AY Note	Pure Sn (Tin)	Tube 50 p/tube	TO-220AB (MP-25) typ. 1.9 g

Note Pb-free (This product does not contain Pb in external electrode).

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vcs = 0 V)	Voss	600	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±30	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±10	Α
Drain Current (pulse) Note1	ID(pulse)	±40	Α
Total Power Dissipation (T _A = 25°C)	P _{T1}	1.5	W
Total Power Dissipation (Tc = 25°C)	P _{T2}	75	W
Channel Temperature	Tch	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Single Avalanche Current Note2	las	10	Α
Single Avalanche Energy Note2	Eas	66.7	mJ

(TO-220AB)



Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Starting Tch = 25°C, VdD = 150 V, Rg = 25 Ω , Vgs = 20 \rightarrow 0 V

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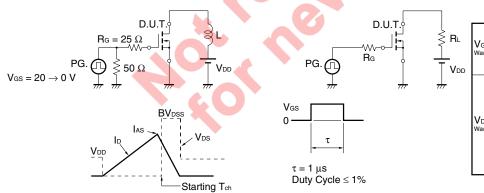
ELECTRICAL CHARACTERISTICS (TA = 25°C)

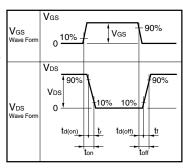
Characteristics	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 600 V, V _{GS} = 0 V			100	μΑ
Gate Leakage Current	Igss	V _{GS} = ±30 V, V _{DS} = 0 V			±100	nA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	2.5		3.5	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 5.0 A	2.0			S
Drain to Source On-state Resistance Note	RDS(on)	V _{GS} = 10 V, I _D = 5.0 A		0.59	0.75	Ω
Input Capacitance	Ciss	V _{DS} = 10 V,		1730		pF
Output Capacitance	Coss	V _{GS} = 0 V,		320		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		20		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 150 V, I _D = 5.0 A,		25		ns
Rise Time	tr	V _G S = 10 V,		10		ns
Turn-off Delay Time	td(off)	R _G = 10 Ω		45		ns
Fall Time	tr			11		ns
Total Gate Charge	Q _G	V _{DD} = 450 V,		30		nC
Gate to Source Charge	Q _{GS}	V _G S = 10 V,		11		nC
Gate to Drain Charge	Q _{GD}	I _D = 10 A		11		nC
Body Diode Forward Voltage Note	V _{F(S-D)}	I _F = 10 A, V _{GS} = 0 V		0.9		V
Reverse Recovery Time	trr	I _F = 10 A, V _{GS} = 0 V,		450		ns
Reverse Recovery Charge	Qrr	di/dt = 50 A/μs	_	2700		nC

Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

TEST CIRCUIT 2 SWITCHING TIME

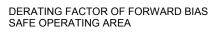


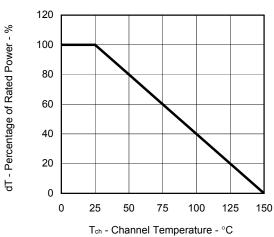


TEST CIRCUIT 3 GATE CHARGE

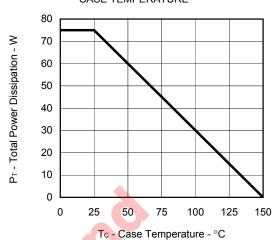
$$\begin{array}{c|c} D.U.T. \\ \hline I_G = 2 \text{ mA} \\ \hline \end{array} \begin{array}{c} D.U.T. \\ \hline \end{array} \begin{array}{c} \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \end{array} \begin{array}{c} \\ \\ \\ \\ \\ \\ \end{array}$$

TYPICAL CHARACTERISTICS (T_A = 25°C)

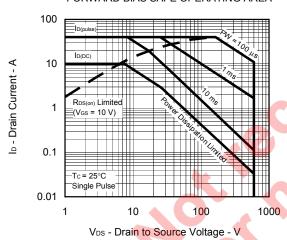




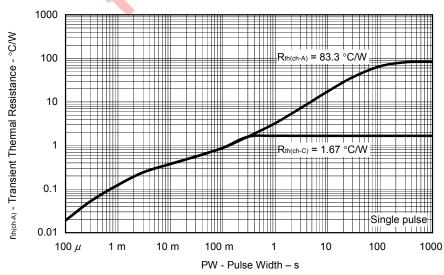
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



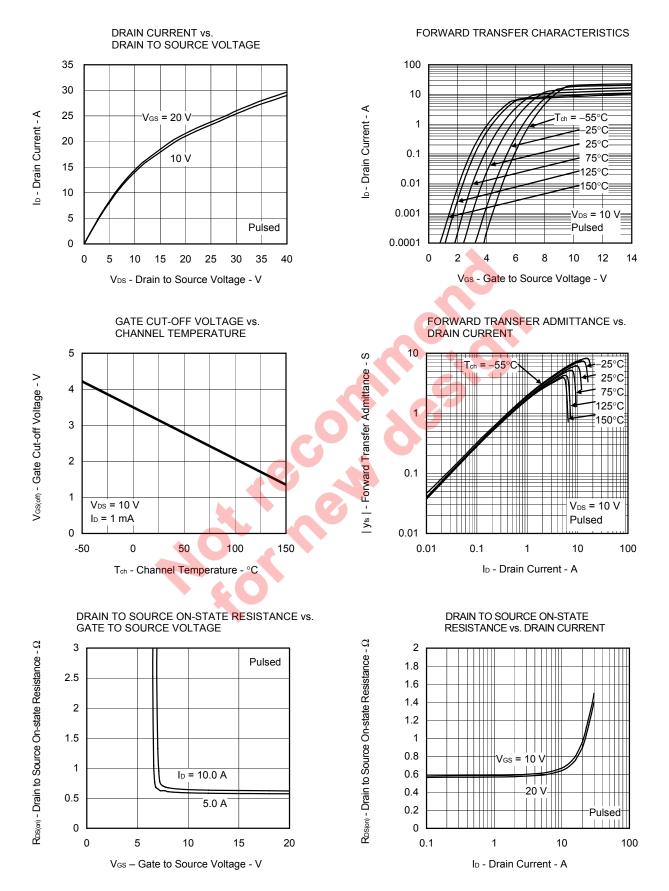
FORWARD BIAS SAFE OPERATING AREA

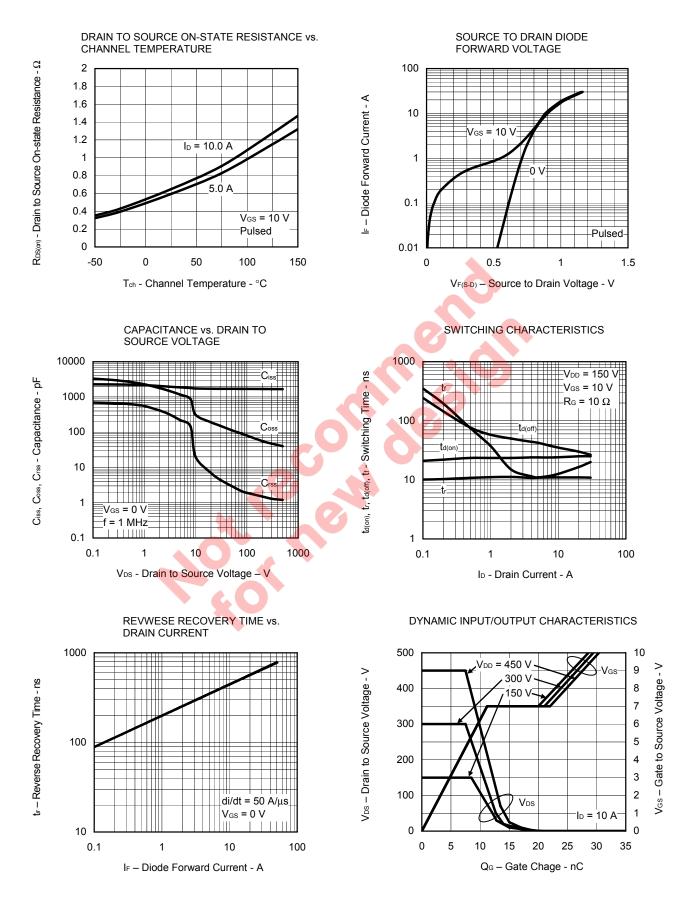


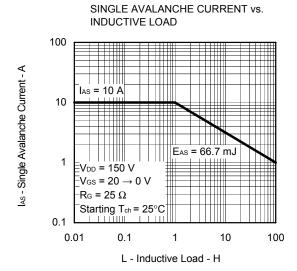


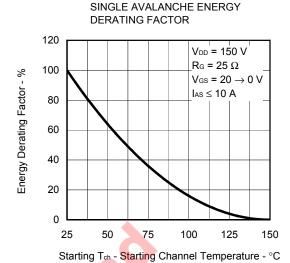


3



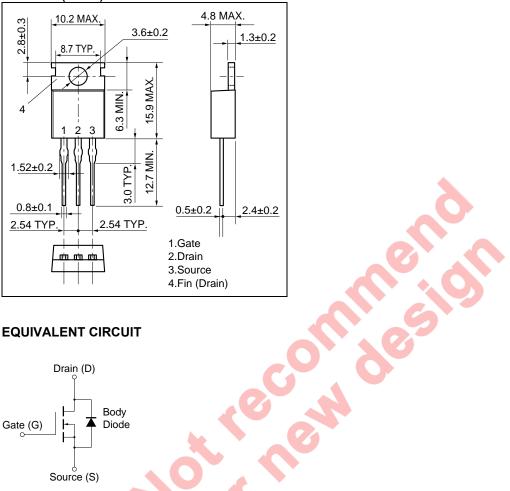




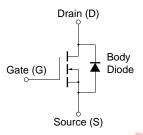


PACKAGE DRAWING (Unit: mm)

TO-220AB (MP-25)



EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

NEC 2SK3299B

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