

# **Si4463DY**

# P-Channel 2.5V Specified PowerTrench® MOSFET

# **General Description**

This P-Channel 2.5V specified MOSFET uses a rugged gate PowerTrench process. It has been optimized for power management applications with a wide range of gate drive voltage (2.5V-12V).

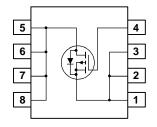
# **Applications**

- · Power management
- Load switch
- · Battery protection

# **Features**

- -11.5 A, -20 V.  $R_{DS(ON)} = 12 \text{ m}\Omega$  @  $V_{GS} = -4.5 \text{ V}$  $R_{DS(ON)} = 17.5 \text{ m}\Omega$  @  $V_{GS} = -2.5 \text{ V}$
- Fast switching speed.
- High performance trench technology for extremely low  $R_{\mbox{\scriptsize DS}(\mbox{\scriptsize ON})}$
- · High power and current handling capability





# Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		-20	V
V <sub>GSS</sub>	Gate-Source Voltage		± 12	V
I <sub>D</sub>	Drain Current - Continuous	(Note 1a)	<b>–11.5</b>	А
	- Pulsed		-50	
P <sub>D</sub>	Power Dissipation for Single Operation	(Note 1a)	2.5	W
		(Note 1b)	1.2	
		(Note 1c)	1.0	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range		-55 to +150	°C

## **Thermal Characteristics**

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	50	°C/W
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case	(Note 1)	25	°C/W

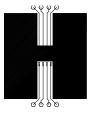
**Package Marking and Ordering Information** 

Device Marking	Device	Reel Size	Tape width	Quantity
4463	Si4463DY	13"	12mm	2500 units

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	acteristics			l	l	l
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	-20			V
ΔBV <sub>DSS</sub> ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D$ = -250 μA, Referenced to 25°C		-12		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = -16 \text{ V},  V_{GS} = 0 \text{ V}$			-1	μΑ
I <sub>GSSF</sub>	Gate-Body Leakage, Forward	$V_{GS} = 12 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage, Reverse	$V_{GS} = -12 \text{ V},  V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-0.6	-0.8	-1.5	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D$ = -250 μA, Referenced to 25°C		3		mV/°C
R <sub>DS(on)</sub>	Static Drain–Source On–Resistance	$\begin{split} &V_{GS} = -4.5 \text{ V}, & I_D = -11.5 \text{ A} \\ &V_{GS} = -2.5 \text{ V}, & I_D = -9.5 \text{ A} \\ &V_{GS} = -4.5 \text{ V}, I_D = -11.5 \text{A}, T_J = 125 ^{\circ}\text{C} \end{split}$		10 14 13	12 17.5 18	mΩ
I <sub>D(on)</sub>	On-State Drain Current	$V_{GS} = -4.5 \text{ V}, \qquad V_{DS} = -5 \text{ V}$	-50			Α
<b>g</b> <sub>FS</sub>	Forward Transconductance	$V_{DS} = -10 \text{ V}, \qquad I_{D} = -10 \text{ A}$		49		S
Dynamic	Characteristics					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = -10 \text{ V},  V_{GS} = 0 \text{ V},$		4481		pF
Coss	Output Capacitance	f = 1.0 MHz		1532		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			540		pF
Switchin	g Characteristics (Note 2)					
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = -5 \text{ V}, \qquad I_{D} = -1 \text{ A},$		15	30	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{DD} = -5 \text{ V}, \qquad I_{D} = -1 \text{ A}, \\ V_{GS} = -4.5 \text{ V}, \qquad R_{GEN} = 6 \Omega$		15	30	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			120	240	ns
t <sub>f</sub>	Turn-Off Fall Time			60	120	ns
Q <sub>g</sub>	Total Gate Charge	$V_{DS} = -10 \text{ V}, \qquad I_{D} = -11.5 \text{ A},$		41	60	nC
Q <sub>gs</sub>	Gate-Source Charge	$V_{GS} = -4.5 \text{ V}$		6.4		nC
$Q_{gd}$	Gate-Drain Charge			11.8		nC
Drain-S	ource Diode Characteristics	s and Maximum Ratings				
Is	Maximum Continuous Drain-Sourc				-2.1	Α
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V},  I_S = -1.5 \text{ A}  \text{(Note 2)}$		-0.65	-1.2	V

#### Notes

<sup>1.</sup>  $R_{\text{BJA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\text{BJC}}$  is guaranteed by design while  $R_{\text{BCA}}$  is determined by the user's board design.



a) 50°/W when mounted on a 1in² pad of 2 oz copper



b) 105°/W when mounted on a .04 in<sup>2</sup> pad of 2 oz copper



c) 125°/W when mounted on a minimum pad.

Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width <  $300\mu$ s, Duty Cycle < 2.0%

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