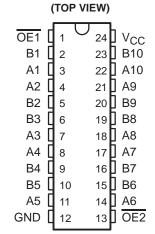
- Functionally Equivalent to QS3384 and QS3L384
- 5-Ω Switch Connection Between Two Ports
- TTL-Compatible Input and Output Levels
- Package Options Include Plastic Small-Outline (DW), Shrink Small-Outline (DB), and Thin Shrink Small-Outline (PW) Packages

description

The SN74CBT3384 provides ten bits of high-speed TTL-compatible bus switching. The low on resistance of the switch allows connections to be made without adding propagation delay.



DB, DW, OR PW PACKAGE

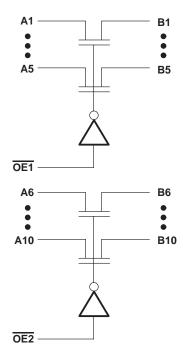
The device is organized as two 5-bit switches with separate output-enable (\overline{OE}) inputs. When \overline{OE} is low, the switch is on and port A is connected to port B. When \overline{OE} is high, the switch is open and a high-impedance state exists between the two ports.

The SN74CBT3384 is characterized for operation from 0°C to 70°C.

FUNCTION TABLE

OE1	OE2	B1 – B5	B6-B10
L	L	A1 – A5	A6-A10
L	Н	A1 – A5	Z
Н	L	Z	A6-A10
Н	Н	Z	Z

logic diagram



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V _{CC}	0.5 V to 7 V
Input voltage range, V _I (see Note 1)	−0.5 V to 6 V
Continuous channel current	128 mA
Input clamp current, I_{IK} ($V_{I/O} < 0$)	–50 mA
Maximum power dissipation at T _A = 55°C (in still air) (see Note 2): DB package	ge 0.6 W
DW packa	age 1.6 W
PW packa	ige 0.7 W
Storage temperature range	−65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		MIN	MAX	UNIT
Vcc	Supply voltage	4.75	5.25	V
V _{IH}	High-level control input voltage	2		V
V _{IL}	Low-level control input voltage		0.8	V
TA	Operating free-air temperature	0	70	°C



NOTES: 1. The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

^{2.} The maximum package power dissipation is calculated using a junction temperature of 150°C and a board trace length of 750 mils. For more information, refer to the *Package Thermal Considerations* application note.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PA	RAMETER	TEST CONDITIONS		MIN	TYP [†]	MAX	UNIT	
VIK		$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$				-1.2	V
I _I		$V_{CC} = 5.25 \text{ V},$	$V_I = 5.25 \text{ V or GND}$				±5	μΑ
los		$V_{CC} = 4.75 V$,	$V_{I(A)} = 0,$	$V_{I(B)} = 4.75 \text{ V}$		250		mA
Icc		$V_{CC} = 5.5 \text{ V},$	$I_{O} = 0$,	$V_I = V_{CC}$ or GND			50	μΑ
Δl _{CC} ‡	Control pins	$V_{CC} = 5.25 \text{ V},$	One input at 3.4 V,	Other inputs at V _{CC} or GND			2.5	mA
Ci	Control pins	V _I = 3 V or 0				3		pF
C _{io(OFF}	-)	$V_0 = 3 \text{ V or } 0,$	OE = VCC			6		pF
			$V_{I} = 0$,	I _I = 64 mA		5	7	
r _{on} §		$V_{CC} = 4.75 \text{ V}$	$V_I = 0$,	I _I = 30 mA		5	7	Ω
			V _I = 2.4 V,	I _I = 15 mA		10	15	

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

switching characteristics over recommended operating free-air temperature range, $C_L = 50 \text{ pF}$ (unless otherwise noted) (see Note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	MIN	MAX	UNIT
$t_{pd}\P$	A or B	B or A		0.25	ns
t _{en}	ŌĒ	A or B	1	8.5	ns
^t dis	ŌĒ	A or B	1	8.5	ns

This parameter is characterized but not tested. This propagation delay is based on the RC time constant of the typical on resistance of the switch and a load capacitance of 50 pF.

NOTE 3: Load circuit and voltage waveforms are shown in Section 1.

[‡] This is the increase in supply current for each input that is at the specified TTL voltage level rather than VCC or GND.

[§] Measured by the voltage drop between the A and B pin at the indicated current through the switch. On resistance is determined by the lower of the voltages of the two (A or B) pins.

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