

April 2000

FQD6P25 / FQU6P25

250V P-Channel MOSFET

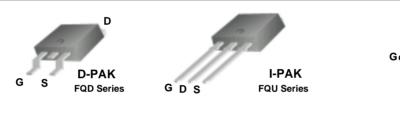
General Description

These P-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switching DC/DC converters.

Features

- -4.7A, -250V, $R_{DS(on)}$ = 1.1 Ω @V_{GS} = -10 V Low gate charge (typical 21 nC)
- Low Crss (typical 20 pF)
- Fast switching
- 100% avalanche tested
- · Improved dv/dt capability



Absolute Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter		FQD6P25 / FQU6P25	Units
V _{DSS}	Drain-Source Voltage		-250	V
I _D	Drain Current - Continuous (T _C = 25°C)		-4.7	Α
	- Continuous (T _C = 100°C	;)	-3.0	А
I _{DM}	Drain Current - Pulsed	(Note 1)	-18.8	А
V _{GSS}	Gate-Source Voltage		± 30	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	540	mJ
I _{AR}	Avalanche Current	(Note 1)	-4.7	Α
E _{AR}	Repetitive Avalanche Energy	(Note 1)	5.5	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	-5.5	V/ns
P _D	Power Dissipation (T _A = 25°C) *		2.5	W
	Power Dissipation (T _C = 25°C)		55	W
	- Derate above 25°C		0.44	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T _L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	°C

Thermal Characteristics

Symbol	Parameter	Тур	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		2.27	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient *		50	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		110	°C/W

^{*} When mounted on the minimum pad size recommended (PCB Mount)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Cha	aracteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V, } I_D = -250 \mu\text{A}$	-250			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = -250 μ A, Referenced to 25°C		-0.1		V/°C
I _{DSS}	- 0	V _{DS} = -250 V, V _{GS} = 0 V			-1	μΑ
	Zero Gate Voltage Drain Current	V _{DS} = -200 V, T _C = 125°C			-10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
On Cha	aracteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	-3.0		-5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	$V_{GS} = -10 \text{ V}, I_D = -2.35 \text{ A}$		0.82	1.1	Ω
9 _{FS}	Forward Transconductance	V _{DS} = -40 V, I _D = -2.35 A (Note 4)		3.0		S
C _{oss} C _{rss}	Output Capacitance Reverse Transfer Capacitance	f = 1.0 MHz		115 20	150 25	pF pF
	,					F-
	ing Characteristics	I		40	0.5	
t _{d(on)}	Turn-On Delay Time	$V_{DD} = -125 \text{ V}, I_{D} = -6.0 \text{ A},$		13	35	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$		75	160	ns
t _{d(off)}	Turn-Off Delay Time	(Note 4, 5)		40	90	ns
t _f	Turn-Off Fall Time	, , ,		50	110	ns
Q _g	Total Gate Charge	$V_{DS} = -200 \text{ V}, I_{D} = -6.0 \text{ A},$		21 4.7	27	nC
Q _{gs}	Gate-Source Charge Gate-Drain Charge	V _{GS} = -10 V (Note 4, 5)		10.7		nC nC
∝ga	Gale-Dialii Charge	(1555 %)		10.7		IIC
Drain-S	Source Diode Characteristics a	nd Maximum Ratings				
	Maximum Continuous Drain-Source Did	ode Forward Current			-4.7	Α
I _S	Maximum Pulsed Drain-Source Diode Forward Current				-18.8	Α
I _S	Maximum Pulsed Drain-Source Diode F					
	Maximum Pulsed Drain-Source Diode F Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{S} = -4.7 \text{ A}$			-5.0	V
I _{SM}				 170	-5.0 	V ns

- Notes:
 1. Repetitive Rating : Pulse width limited by maximum junction temperature 2. L = 39mH, I_{AS} = -4.7A, V_{DD} = -50V, R_G = 25 Ω , Starting T_J = 25°C
 3. I_{SD} \le -6.0A, di/dt \le 300A/µs, V_{DD} \le BV_{DSS}, Starting T_J = 25°C
 4. Pulse Test : Pulse width \le 300µs, Duty cycle \le 2%
 5. Essentially independent of operating temperature

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Typical Characteristics

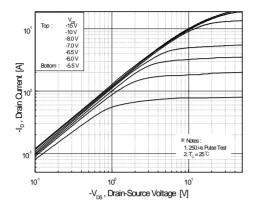


Figure 1. On-Region Characteristics

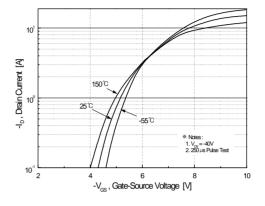


Figure 2. Transfer Characteristics

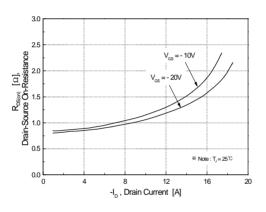


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

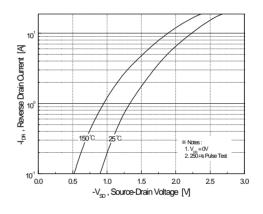


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

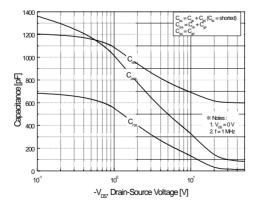


Figure 5. Capacitance Characteristics

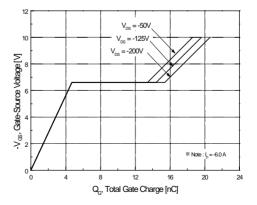
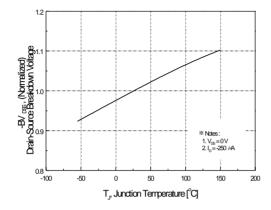


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)



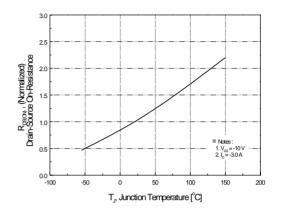
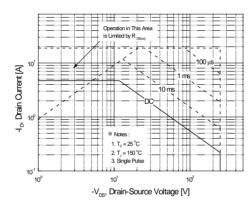


Figure 7. Breakdown Voltage Variation vs. Temperature

Figure 8. On-Resistance Variation vs. Temperature



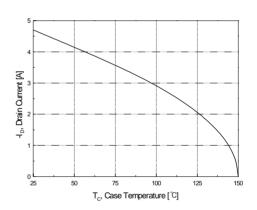


Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature

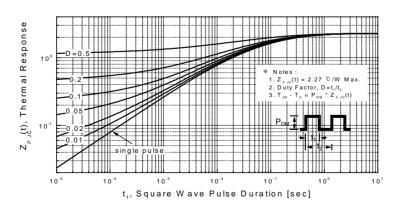
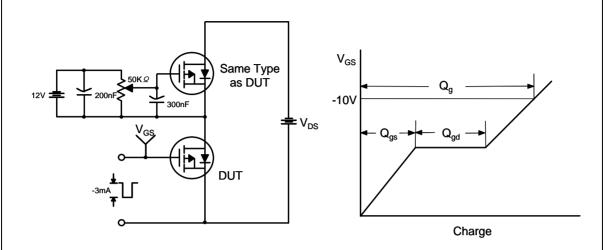


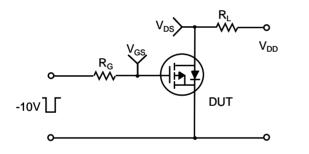
Figure 11. Transient Thermal Response Curve

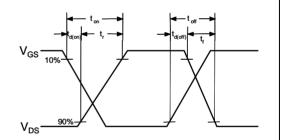
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Gate Charge Test Circuit & Waveform

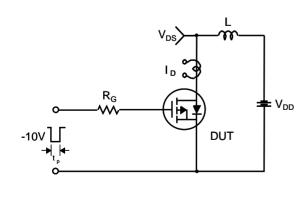


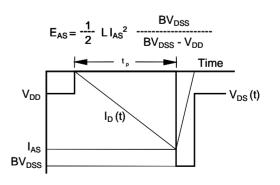
Resistive Switching Test Circuit & Waveforms



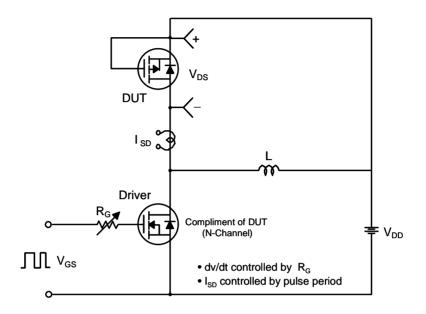


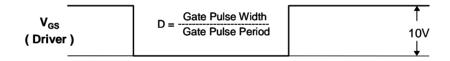
Unclamped Inductive Switching Test Circuit & Waveforms





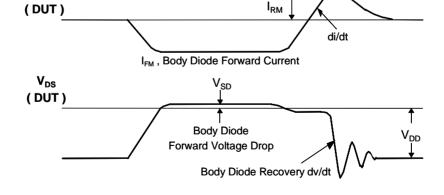
Peak Diode Recovery dv/dt Test Circuit & Waveforms



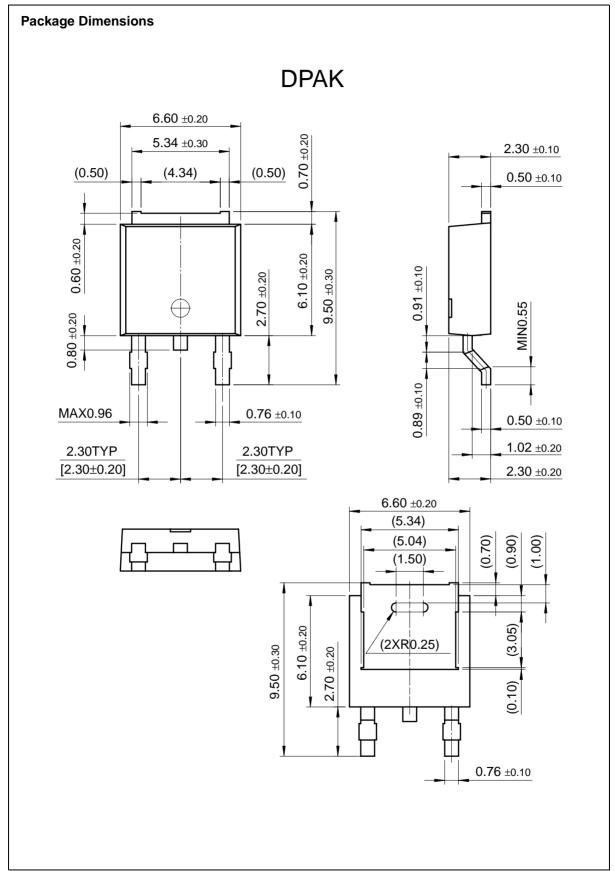


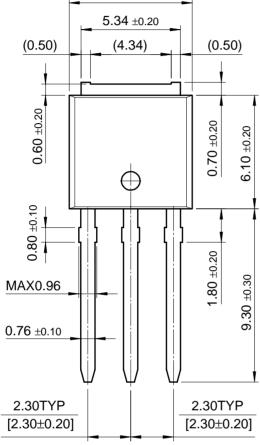
I_{SD}

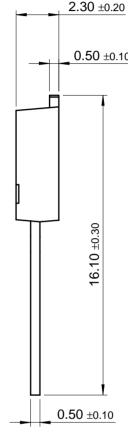
Body Diode Reverse Current

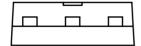


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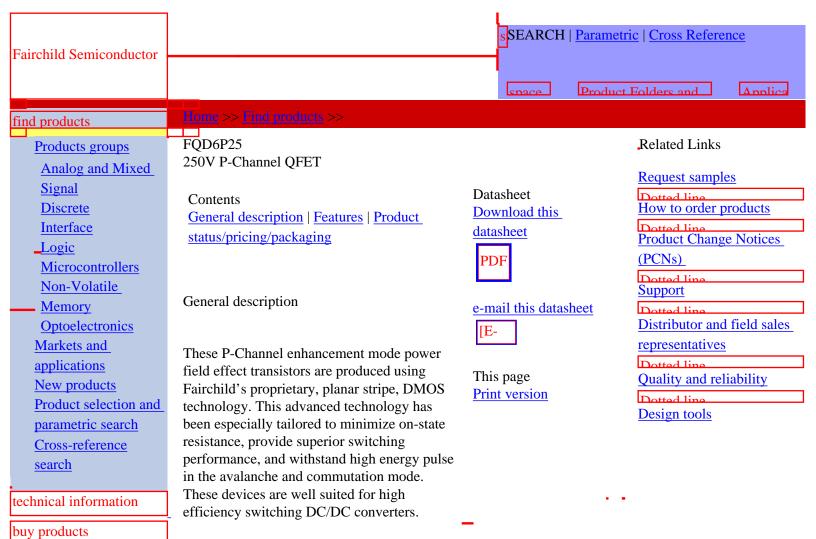
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Features

• -4.7A, -250V, $R_{DS(on)} = 1.1\Omega$ @ $V_{GS} = -$

10 V

- Low gate charge (typical 21 nC)
- Low Crss (typical 20 pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

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Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
FQD6P25TF	Full Production	\$0.65	TO-252(DPAK)	2	TAPE REEL
FQD6P25TM	Full Production	\$0.65	TO-252(DPAK)	2	TAPE REEL

^{* 1,000} piece Budgetary Pricing

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