Preferred Device

Product Preview

Power MOSFET 6 Amps, 600 Volts

N-Channel TO-220

Designed for high voltage, high speed switching applications in power supplies, converters, power motor controls and bridge circuits.

Features

- Higher Current Rating
- Lower R_{DS(on)}
- Lower Capacitances
- Lower Total Gate Charge
- Tighter V_{SD} Specifications
- Avalanche Energy Specified

Typical Applications

- Switch Mode Power Supplies
- PWM Motor Controls
- Converters
- Bridge Circuits

MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	600	Vdc
Drain–Gate Voltage ($R_{GS} = 1.0 \text{ M}\Omega$)	V_{DGR}	600	Vdc
Gate–Source Voltage – Continuous – Non–Repetitive (t _p ≤10 ms)	V _{GS} V _{GSM}	±20 ±40	Vdc
Drain - Continuous - Continuous @ 100°C - Single Pulse (t _p ≤ 10 μs)	I _D I _D	6.0 4.8 21	Adc
Total Power Dissipation Derate above 25°C	P _D	142 1.14	Watts W/°C
Operating and Storage Temperature Range	T _J , T _{stg}	-55 to 150	°C
Single Drain–to–Source Avalanche Energy – Starting $T_J = 25^{\circ}C$ ($V_{DD} = 100 \text{ V}, V_{GS} = 10 \text{ Vdc},$ $I_L = 6 \text{ A}, L = 25 \text{ mH}, R_G = 25 \Omega$)	E _{AS}	450	mJ
Thermal Resistance – Junction–to–Case – Junction–to–Ambient – Junction–to–Ambient (Note 1.)	$R_{ heta JC} \ R_{ heta JA} \ R_{ heta JA}$	0.88 62.5 50	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C

When surface mounted to an FR4 board using the minimum recommended pad size.

This document contains information on a product under development. ON Semiconductor reserves the right to change or discontinue this product without notice.

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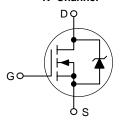


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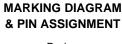
V _{DSS}	R _{DS(ON)} TYP	I _D MAX
600 V	850 mΩ	6.0 A

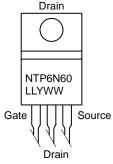
N-Channel





TO-220AB CASE 221A STYLE 5





NTP6N60 = Device Code
LL = Location Code
Y = Year
WW = Work Week

ORDERING INFORMATION

Device	Package	Shipping
NTP6N60	TO-220AB	50 Units/Rail

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS ($T_C = 25$ °C unless otherwise noted)

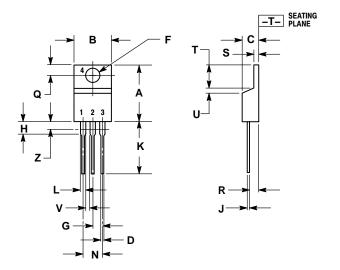
Ch	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Vo	V _{(BR)DSS}	600	-	-	Vdc	
Temperature Coefficient (Pos		_	715	_	mV/°C	
Zero Gate Voltage Collector Cui $(V_{DS} = 600 \text{ Vdc}, V_{GS} = 0 \text{ Vdc})$ $(V_{DS} = 600 \text{ Vdc}, V_{GS} = 0 \text{ Vdc})$	I _{DSS}	- -	- -	10 100	μAdc	
Gate-Body Leakage Current (V	I _{GSS(f)}	_ _	_ _	100 100	nAdc	
ON CHARACTERISTICS (Note 2	.)	•	•	•		•
Gate Threshold Voltage $I_D = 0.25 \text{ mA}, V_{DS} = V_{GS}$ Temperature Coefficient (Neg	V _{GS(th)}	2.0	2.6 6.6	4.0	Vdc mV/°C	
Static Drain-to-Source On-Res	sistance (V _{GS} = 10 Vdc, I _D = 3 Adc)	R _{DS(on)}	_	850	1200	mOhm
Drain-to-Source On-Voltage ($V_{GS} = 10 \text{ Vdc}$, $I_D = 6 \text{ Adc}$) ($V_{GS} = 10 \text{ Vdc}$, $I_D = 3 \text{ Adc}$, T_J	V _{DS(on)}	- -	- -	8.6 7.9	Vdc	
Forward Transconductance (V _D	_S = 15 Vdc, I _D = 3 Adc)	9FS	2.0	7.0	-	mhos
DYNAMIC CHARACTERISTICS						
Input Capacitance		C _{iss}	_	1190	1670	pF
Output Capacitance	$(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, f = 1.0 \text{ MHz})$	C _{oss}	_	350	490	•
Transfer Capacitance	1 – 1.0 m 12)	C _{rss}	-	20	40	
SWITCHING CHARACTERISTIC	S (Note 3.)					
Turn-On Delay Time		t _{d(on)}	_	11	20	ns
Rise Time	$(V_{DD} = 300 \text{ Vdc}, I_D = 6 \text{ Adc},$	t _r	_	10	20	
Turn-Off Delay Time	$V_{GS} = 10 \text{ Vdc},$ $R_G = 9.1 \Omega)$	t _{d(off)}	_	34	70	
Fall Time	-	t _f	-	19	40	1
Gate Charge	$(V_{DS} = 480 \text{ Vdc}, I_{D} = 6 \text{ Adc}, V_{GS} = 10 \text{ Vdc})$	Q _T	-	24	30	nC
		Q ₁	-	6.0	-	
		Q ₂	-	8.0	-	
		Q_3	_	12	-	
SOURCE-DRAIN DIODE CHAR	ACTERISTICS					
Forward On-Voltage (Note 2.)	$(I_S = 6 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ $(I_S = 6 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_J = 125^{\circ}\text{C})$	V _{SD}	_ _	0.85 0.73	1.0	Vdc
Reverse Recovery Time		t _{rr}	_	440	_	ns
·		t _a	_	130	_	
	$(I_S = 6 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, \\ di_S/dt = 100 \text{ A/}\mu\text{s})$	t _b	_	310	_	
Reverse Recovery Stored Charge	2.5 200 / 440/	Q _{RR}	_	2.8	-	μС
NTERNAL PACKAGE INDUCTA	NCE	1	1	1		
Internal Drain Inductance (Measured from contact screw (Measured from the drain lead	L _D	_ _	3.5 4.5	- -	nH	
Internal Source Inductance	L _S				1	

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperature.

PACKAGE DIMENSIONS

TO-220 THREE-LEAD TO-220AB

CASE 221A-09 **ISSUE AA**



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

- STYLE 5:
 PIN 1. GATE
 2. DRAIN
 3. SOURCE
 4. DRAIN

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