# FAIRCHILD

SEMICONDUCTOR®

# SGP13N60UF

### **Ultra-Fast IGBT**

### **General Description**

Fairchild's UF series of Insulated Gate Bipolar Transistors (IGBTs) provides low conduction and switching losses. The UF series is designed for applications such as motor control and general inverters where high speed switching is a required feature.

### Features

- High speed switching
- Low saturation voltage :  $V_{CE(sat)} = 2.1 \text{ V} @ I_C = 6.5 \text{ A}$
- High input impedance

### Applications

AC & DC motor controls, general purpose inverters, robotics, and servo controls.



### Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Description		SGP13N60UF	Units
V <sub>CES</sub>	Collector-Emitter Voltage		600	V
V <sub>GES</sub>	Gate-Emitter Voltage		± 20	V
	Collector Current	@ T <sub>C</sub> = 25°C	13	A
I <sub>C</sub>	Collector Current	@ T <sub>C</sub> = 100°C	6.5	А
I <sub>CM (1)</sub>	Pulsed Collector Current	-	52	Α
PD	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	60	W
	Maximum Power Dissipation	@ T <sub>C</sub> = 100°C	25	W
TJ	Operating Junction Temperature	-	-55 to +150	°C
T <sub>J</sub> T <sub>stg</sub>	Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temp. for Soldering Purposes, 1/8" from Case for 5 Sec		300	°C

Notes :

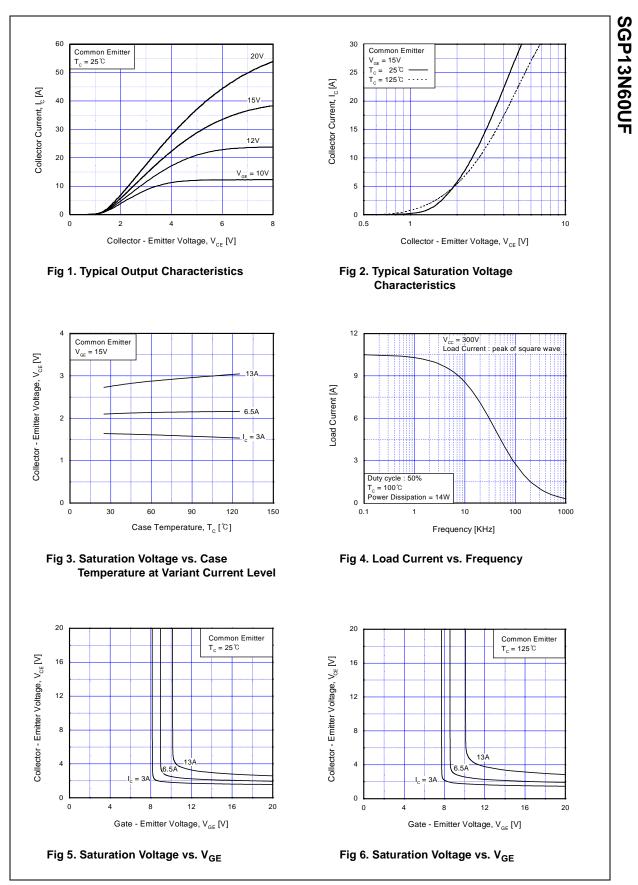
(1) Repetitive rating : Pulse width limited by max. junction temperature

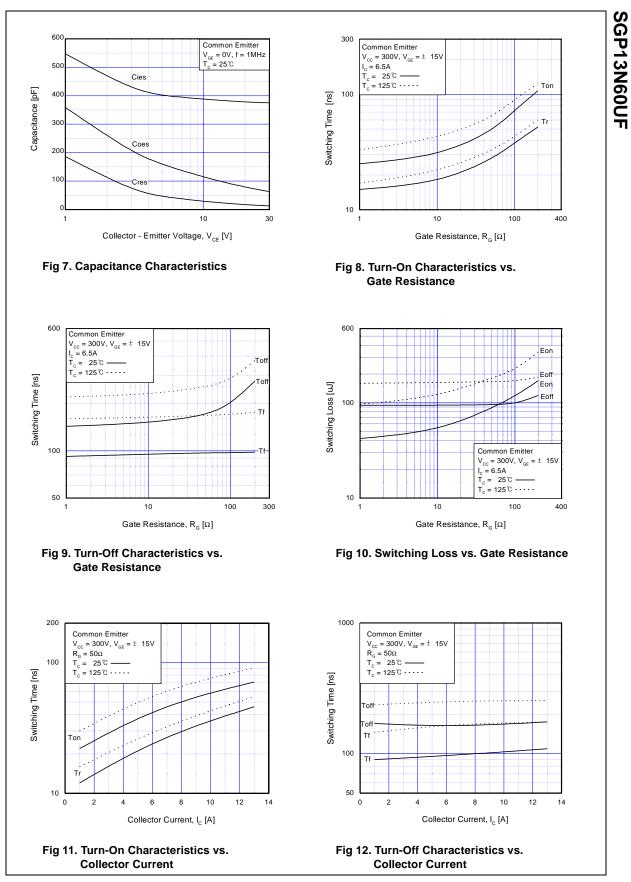
### **Thermal Characteristics**

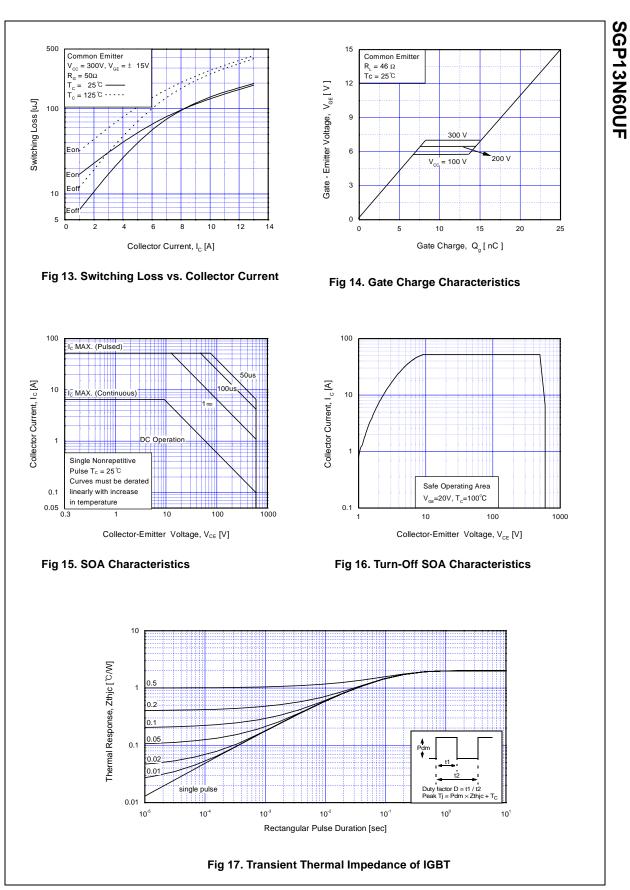
Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		2.0	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient		62.5	°C/W

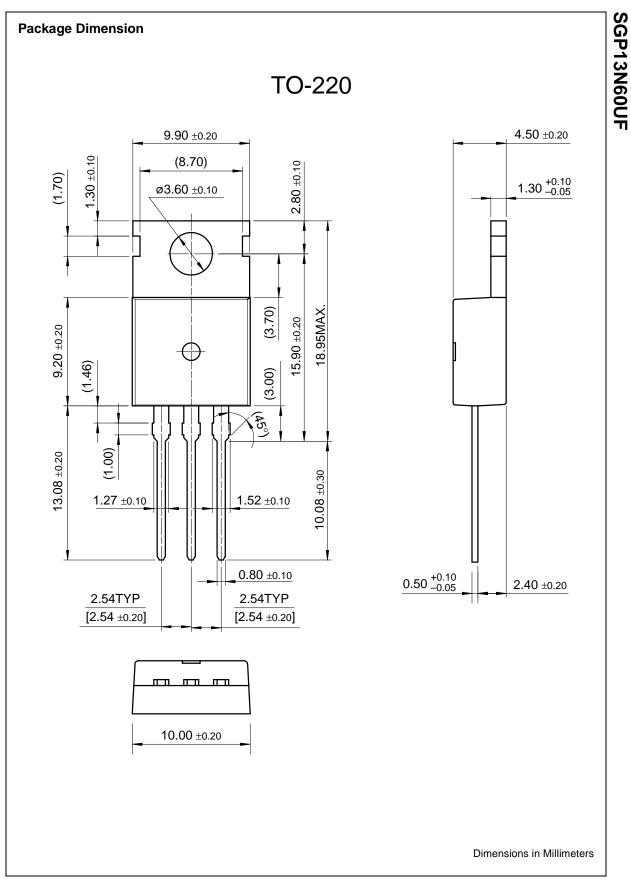
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Cha	racteristics					
BV <sub>CES</sub>	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250uA$	600			V
ΔB <sub>VCES</sub> / ΔT <sub>J</sub>	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0V, I_C = 1mA$		0.6		V/∘C
ICES	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$			250	uA
I <sub>GES</sub>	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$			± 100	nA
On Cha	racteristics					
V <sub>GE(th)</sub>	G-E Threshold Voltage	$I_{C} = 6.5 \text{mA}, V_{CE} = V_{GE}$	3.5	4.5	6.5	V
	Collector to Emitter	I <sub>C</sub> = 6.5A, V <sub>GE</sub> = 15V		2.1	2.6	V
V <sub>CE(sat)</sub>	Saturation Voltage	I <sub>C</sub> = 13A, V <sub>GE</sub> = 15V		2.6		V
<b>Dynami</b> C <sub>ies</sub>	c Characteristics			375		pF
C <sub>oes</sub>	Output Capacitance	$V_{CE} = 30V$ , $V_{GE} = 0V$ ,		63		pF
C <sub>res</sub>	Reverse Transfer Capacitance	f = 1MHz		13		pF
t <sub>d(on)</sub>	ng Characteristics Turn-On Delay Time			20		ns
t <sub>r</sub>	Rise Time	-		27		ns
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>CC</sub> = 300 V, I <sub>C</sub> = 6.5A,		70	130	ns
t <sub>f</sub>	Fall Time	$R_{G} = 50\Omega, V_{GE} = 15V,$		97	150	ns
Eon	Turn-On Switching Loss	Inductive Load, $T_C = 25^{\circ}C$		85		uJ
E <sub>off</sub>	Turn-Off Switching Loss			95		uJ
E <sub>ts</sub>	Total Switching Loss			180	270	uJ
t <sub>d(on)</sub>	Turn-On Delay Time			30		ns
t <sub>r</sub>	Rise Time			32		ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{\rm CC} = 300 \text{ V}, \text{ I}_{\rm C} = 6.5 \text{ A},$		85	200	ns
t <sub>f</sub>	Fall Time	$R_{G} = 50\Omega, V_{GE} = 15V,$		168	250	ns
E <sub>on</sub>	Turn-On Switching Loss	Inductive Load, $T_C = 125^{\circ}C$		180		uJ
	Turn-Off Switching Loss	4		165		uJ
	Total Switching Loss			345	500	uJ
E <sub>ts</sub>		1		25	35	nC
E <sub>ts</sub> Q <sub>g</sub>	Total Gate Charge	$V_{CE} = 300 \text{ V}$ . $I_C = 6.5 \text{ A}$ .				
E <sub>ts</sub> Q <sub>g</sub> Q <sub>ge</sub>	Gate-Emitter Charge	V <sub>CE</sub> = 300 V, I <sub>C</sub> = 6.5A, V <sub>GF</sub> = 15V		7	12	nC
E <sub>off</sub> E <sub>ts</sub> Q <sub>g</sub> Q <sub>ge</sub> Q <sub>gc</sub>	0			7 8 7.5	12 14 	nC nC nH

# SGP13N60UF









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### **PRODUCT STATUS DEFINITIONS**

### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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Interface Logic Microcontrollers Non-Volatile	Product status/pricing/packaging	datasheet PDF	Product Change Notices (PCNs) Dotted line Support
<u>Memory</u> <u>Optoelectronics</u> <u>Markets and</u> <u>applications</u>	General description Fairchild's UF series of Insulated Gate Bipolar Transistors (IGBTs) provides low conduction	e-mail this datasheet [E- This page <u>Print version</u>	Dotted line Distributor and field sales representatives Dotted line Quality and reliability
<u>New products</u> <u>Product selection and</u> <u>parametric search</u> <u>Cross-reference</u> <u>search</u>	and switching losses. The UF series is designed for applications such as motor control and general inverters where high speed switching is a required feature.		Dotted line Design tools
technical information	back to top		
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my Fairchild company	<ul> <li>High Speed Switching</li> <li>Low Saturation Voltage : V<sub>CE(sat)</sub> = 2.1 V @ I<sub>C</sub> = 6.5A</li> <li>High Input Impedance</li> </ul>		

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Controls

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Applications

Product status/pricing/packaging

AC &DC Motor controls,General Purpose Inverters, Robotics, Servo

Product         Product status         Pricing*         Package type         Leads         Packing method	d
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SGP13N60UFTU	Full Production	\$1.15	TO-220	3	RAIL
* 1,000 piece Budget	ary Pricing				
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