

BFU550XR NPN wideband silicon RF transistor Rev. 2 — 12 April 2019

Product data sheet

1 Product profile

1.1 General description

NPN silicon RF transistor for high speed, low noise applications in a plastic, 4-pin dualemitter SOT143R package.

The BFU550XR is part of the BFU5 family of transistors, suitable for small signal to medium power applications up to 2 GHz.

1.2 Features and benefits

- · Low noise, high breakdown RF transistor
- AEC-Q101 qualified
- Minimum noise figure (NF_{min}) = 0.7 dB at 900 MHz
- Maximum stable gain 21.5 dB at 900 MHz
- 11 GHz f_T silicon technology

1.3 Applications

- · Applications requiring high supply voltages and high breakdown voltages
- · Broadband amplifiers up to 2 GHz
- · Low noise amplifiers for ISM applications
- · ISM band oscillators

1.4 Quick reference data

Table 1. Quick reference data

T_{amb} = 25 °C unless otherwise specified

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CB}	collector-base voltage	open emitter		-	-	24	V
V _{CE}	collector-emitter voltage	open base		-	-	12	V
		shorted base		-	-	24	V
V _{EB}	emitter-base voltage	open collector		-	-	2	V
I _C	collector current			-	15	50	mA
P _{tot}	total power dissipation	T _{sp} ≤ 87 °C	[1]	-	-	450	mW
h _{FE}	DC current gain	I _C = 15 mA; V _{CE} = 8 V		60	95	200	
C _c	collector capacitance	V _{CB} = 8 V; f = 1 MHz		-	0.41	-	pF
f _T	transition frequency	I _C = 25 mA; V _{CE} = 8 V; f = 900 MHz		-	11	-	GHz



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Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
G _{p(max)}	maximum power gain	I _C = 15 mA; V _{CE} = 8 V; f = 900 MHz	[2]	-	21.5	-	dB
NF _{min}	minimum noise figure	I_C = 1 mA; V_{CE} = 8 V; f = 900 MHz; Γ_S = Γ_{opt}		-	0.70	-	dB
P _{L(1dB)}	output power at 1 dB gain compression	$\rm I_C$ = 25 mA; $\rm V_{CE}$ = 8 V; $\rm Z_S$ = $\rm Z_L$ = 50 Ω; f = 900 MHz		-	13.5	-	dBm

[1] [2]

 T_{sp} is the temperature at the solder point of the collector lead. If K > 1 then $G_{p(max)}$ is the maximum power gain. If K < 1 then $G_{p(max)}$ = MSG.

2 **Pinning information**

Pin	Description	Simplified outline	Graphic symbol
1	collector		
2	emitter		
3	base		3-
4	emitter		 2, 4 aaa-010457

Ordering information 3

Table 3. Ordering information

Type number	r Package					
	Name	Description	Version			
BFU550XR	-	plastic surface-mounted package; reverse pinning; 4 leads	SOT143R			
OM7964	-	Customer evaluation kit for BFU520XR, BFU530XR and BFU550XR ^[1]				

[1] The customer evaluation kit contains the following:

- Unpopulated RF amplifier Printed-Circuit Board (PCB)
 - Unpopulated RF amplifier Printed-Circuit Board (PCB) with emitter degeneration
 - Four SMA connectors for fitting unpopulated Printed-Circuit Board (PCB)
 - BFU520XR, BFU530XR and BFU550XR samples
 - USB stick with data sheets, application notes, models, S-parameter and noise files

Marking 4

Table 4. Marking						
Type number	Marking	Description				
BFU550XR	*TL * = t : made in Malaysia					
		* = w : made in China				

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Design support 5

Table 5. Available design support

Download from the BFU550XR product information page on http://www.nxp.com.

Support item	Available	Remarks
Device models for Agilent EEsof EDA ADS	yes	Based on Mextram device model.
SPICE model	yes	Based on Gummel-Poon device model.
S-parameters	yes	
Noise parameters	yes	
Customer evaluation kit	yes	See <u>Section 3</u> and <u>Section 10</u> .
Solder pattern	yes	
Application notes	yes	See <u>Section 10.1</u> and <u>Section 10.2</u> .

Limiting values 6

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CB}	collector-base voltage	open emitter	-	30	V
V _{CE}	collector-emitter voltage	open base	-	16	V
		shorted base	-	30	V
V _{EB}	emitter-base voltage	open collector	-	3	V
I _C	collector current		-	80	mA
T _{stg}	storage temperature		-65	+150	°C
V _{ESD}	electrostatic discharge voltage	Human Body Model (HBM) According to JEDEC standard 22-A114E	-	±150	V
		Charged Device Model (CDM) According to JEDEC standard 22-C101B	-	±2	kV

Recommended operating conditions 7

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CB}	collector-base voltage	open emitter	-	-	24	V
V _{CE}	collector-emitter voltage	open base	-	-	12	V
		shorted base	-	-	24	V
V _{EB}	emitter-base voltage	open collector	-	-	2	V
I _C	collector current		-	-	50	mA
Pi	input power	Z _S = 50 Ω	-	-	10	dBm
Tj	junction temperature		-40	-	+150	°C

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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
P _{tot}	total power dissipation	T _{sp} ≤ 87 °C	[1]	-	-	450	mW

[1] T_{sp} is the temperature at the solder point of the collector lead.

8 Thermal characteristics

Table 8. Thermal characteristics

Symbol	Parameter	Conditions		Тур	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point		[1]	140	K/W

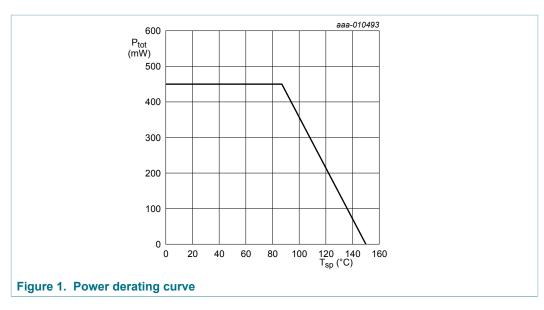
[1] T_{sp} is the temperature at the solder point of the collector lead.

 T_{sp} has the following relation to the ambient temperature T_{amb} :

 $T_{sp} = T_{amb} + P \times R_{th(sp-a)}$

With P being the power dissipation and $R_{th(sp-a)}$ being the thermal resistance between the solder point and ambient. $R_{th(sp-a)}$ is determined by the heat transfer properties in the application.

The heat transfer properties are set by the application board materials, the board layout and the environment e.g. housing.



9 Characteristics

Table 9. Characteristics

T_{amb} = 25 °C unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 nA; I _E = 0 mA	24	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 150 nA; I _B = 0 mA	12	-	-	V
I _C	collector current		-	15	50	mA
I _{CBO}	collector-base cut-off current	I _E = 0 mA; V _{CB} = 8 V	-	<1	-	nA
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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
h _{FE}	DC current gain	I _C = 15 mA; V _{CE} = 8 V		60	95	200	
C _{EBS}	emitter-base capacitance	V _{CE} = 8 V; f = 1 MHz		-	0.87	-	pF
C _{CES}	collector-emitter capacitance	V _{EB} = 0.5 V; f = 1 MHz		-	0.44	-	pF
C _{CBS}	collector-base capacitance	V _{CB} = 8 V; f = 1 MHz		-	0.41	-	pF
f _T	transition frequency	I _C = 25 mA; V _{CE} = 8 V; f = 900 MHz		-	11	-	GHz
G _{p(max)}	maximum power gain	f = 433 MHz; V _{CE} = 8 V	[1]				
		I _C = 1 mA		-	15	-	dB
		I _C = 15 mA		-	25.5	-	dB
		I _C = 25 mA		-	26.5	-	dB
		f = 900 MHz; V _{CE} = 8 V	[1]				
		I _C = 1 mA		-	12	-	dB
		I _C = 15 mA		-	21.5	-	dB
		I _C = 25 mA		-	22	-	dB
		f = 1800 MHz; V _{CE} = 8 V	[1]				
		I _C = 1 mA		-	10	-	dB
		I _C = 15 mA		-	16	-	dB
		I _C = 25 mA		-	15.5	-	dB
s ₂₁ ²	insertion power gain	f = 433 MHz; V _{CE} = 8 V					
		I _C = 1 mA		-	10	-	dB
		I _C = 15 mA		-	23.5	-	dB
		I _C = 25 mA		-	24	-	dB
		f = 900 MHz; V _{CE} = 8 V					
		I _C = 1 mA		-	8	-	dB
		I _C = 15 mA		-	17.5	-	dB
		I _C = 25 mA		-	18	-	dB
		f = 1800 MHz; V _{CE} = 8 V					
		I _C = 1 mA		-	4.5	-	dB
		I _C = 15 mA		-	12	-	dB
		I _C = 25 mA		-	12	-	dB
NF _{min}	minimum noise figure	f = 433 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}					
		I _C = 1 mA		-	0.55	-	dB
		I _C = 15 mA		-	0.9	-	dB
		I _C = 25 mA		-	1.05	-	dB
		f = 900 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}					
		I _C = 1 mA		-	0.7	-	dB
		I _C = 15 mA		-	0.95	-	dB
		I _C = 25 mA		-	1.1	-	dB

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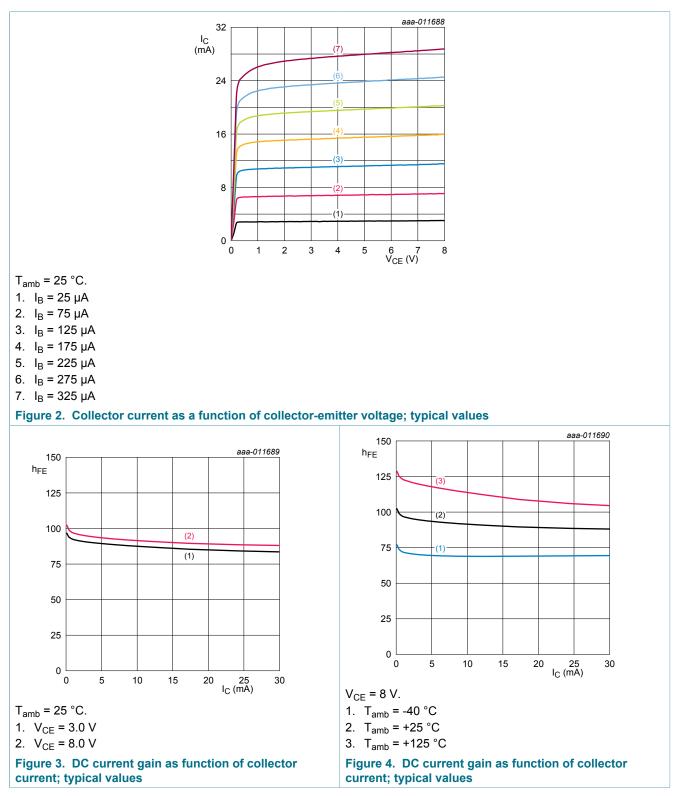
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
		f = 1800 MHz; V_{CE} = 8 V; Γ_S = Γ_{opt}				
		I _C = 1 mA	-	0.95	-	dB
		I _C = 15 mA	-	1.05	-	dB
		I _C = 25 mA	-	1.25	-	dB
G _{ass}	associated gain	f = 433 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 1 mA	-	22.5	-	dB
		I _C = 15 mA	-	25	-	dB
		I _C = 25 mA	-	25.5	-	dB
		f = 900 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 1 mA	-	15	-	dB
		I _C = 15 mA	-	19	-	dB
		I _C = 25 mA	-	19.5	-	dB
		f = 1800 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 1 mA	-	9.5	-	dB
		I _C = 15 mA	-	13.5	-	dB
		I _C = 25 mA	-	14	-	dB
P _{L(1dB)}	output power at 1 dB gain compression	f = 433 MHz; V_{CE} = 8 V; Z_S = Z_L = 50 Ω				
		I _C = 15 mA	-	9.5	-	dBm
		I _C = 25 mA	-	13	-	dBm
		f = 900 MHz; V_{CE} = 8 V; Z_S = Z_L = 50 Ω				
		I _C = 15 mA	-	10.5	-	dBm
		I _C = 25 mA	-	13.5	-	dBm
		f = 1800 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω				
		I _C = 15 mA	-	10.5	-	dBm
		I _C = 25 mA	-	13.5	-	dBm
IP3 _o	output third-order intercept point	f_1 = 433 MHz; f_2 = 434 MHz; V_{CE} = 8 V; Z_S = Z_L = 50 Ω				
		I _C = 15 mA	-	19	-	dBm
		I _C = 25 mA	-	23	-	dBm
		f_1 = 900 MHz; f_2 = 901 MHz; V_{CE} = 8 V; Z_S = Z_L = 50 Ω				
		I _C = 15 mA	-	20	-	dBm
		I _C = 25 mA	-	23	-	dBm
		f_1 = 1800 MHz; f_2 = 1801 MHz; V_{CE} = 8 V; Z_S = Z_L = 50 Ω				
		I _C = 15 mA	-	20	-	dBm
		I _C = 25 mA	-	23	-	dBm

 $[1] \quad \ \ If K > 1 \ then \ G_{p(max)} \ is \ the \ maximum \ power \ gain. \ If \ K < 1 \ then \ G_{p(max)} = MSG.$

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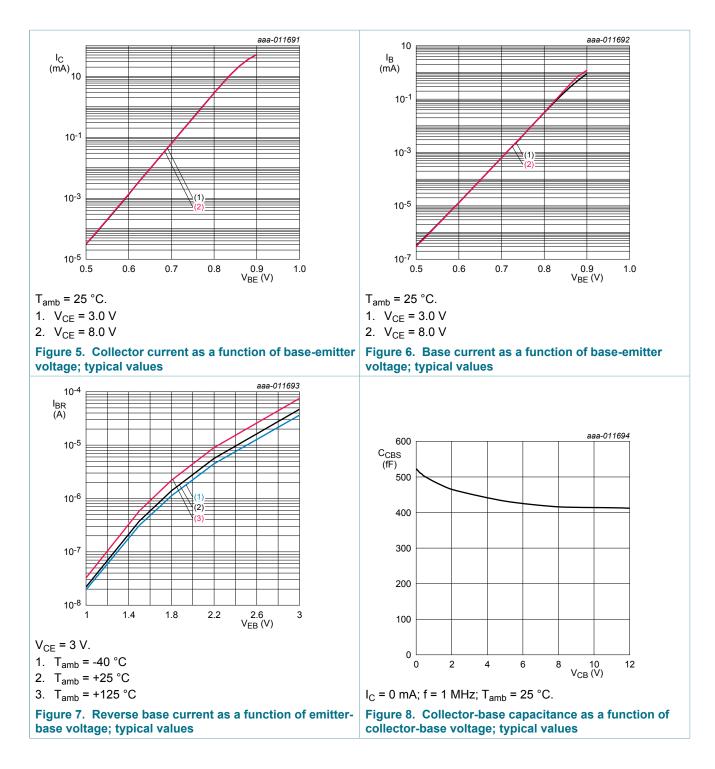
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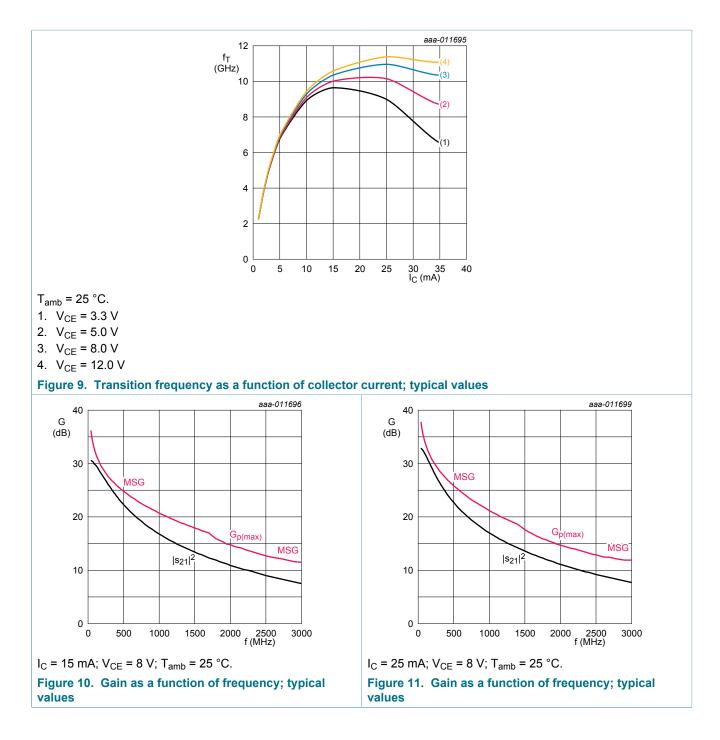
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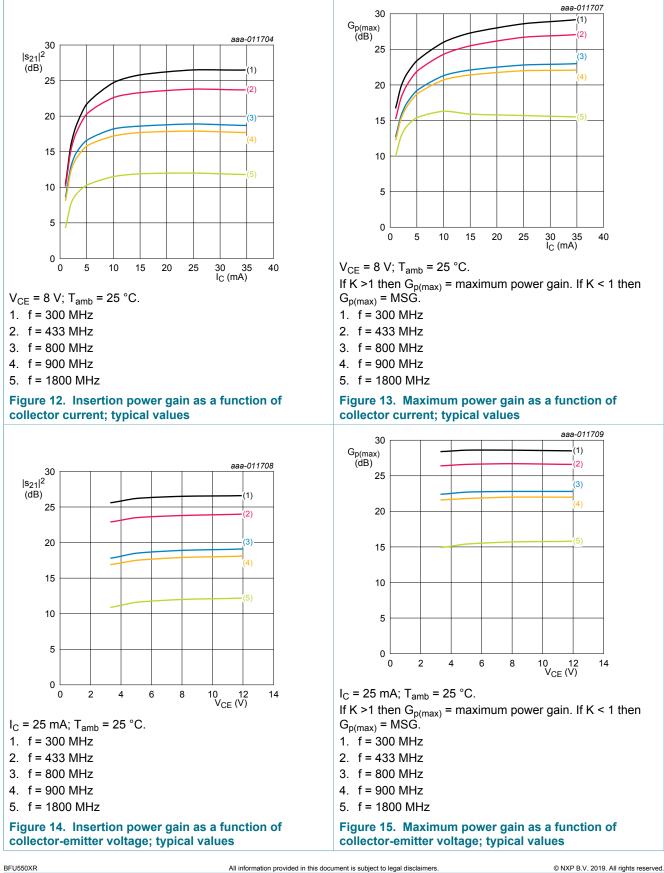
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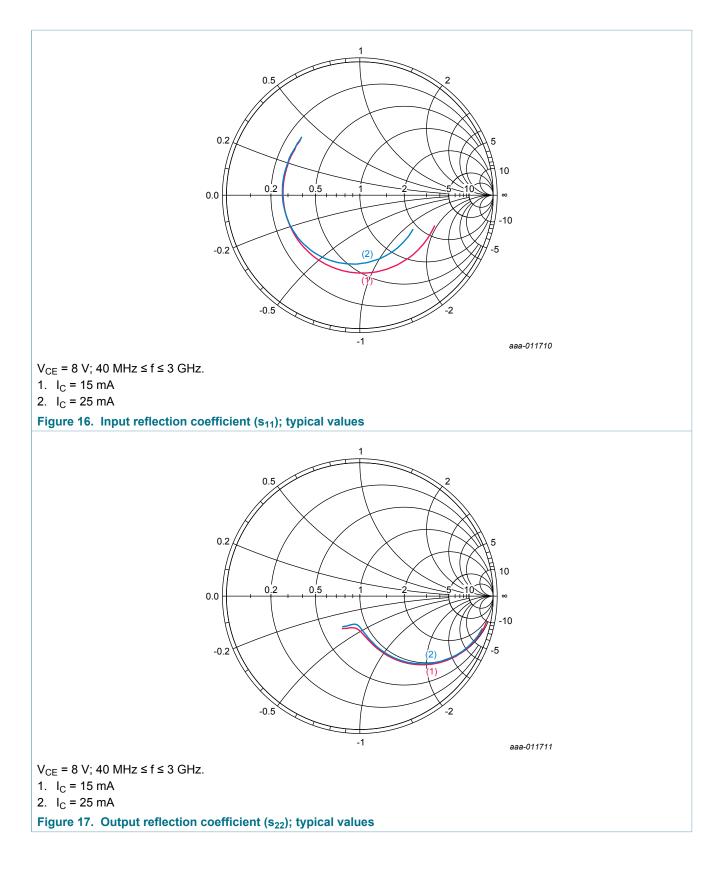
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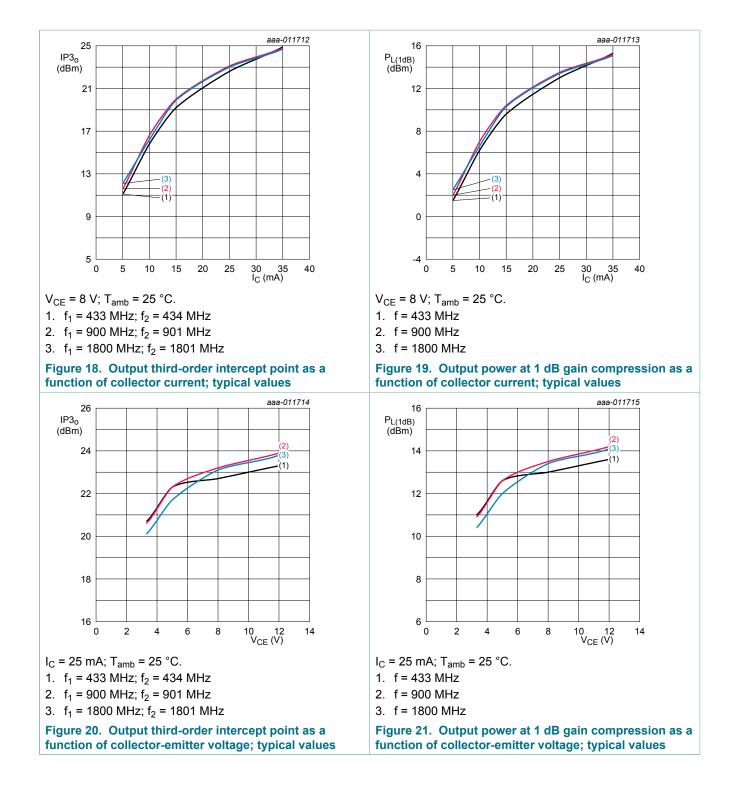
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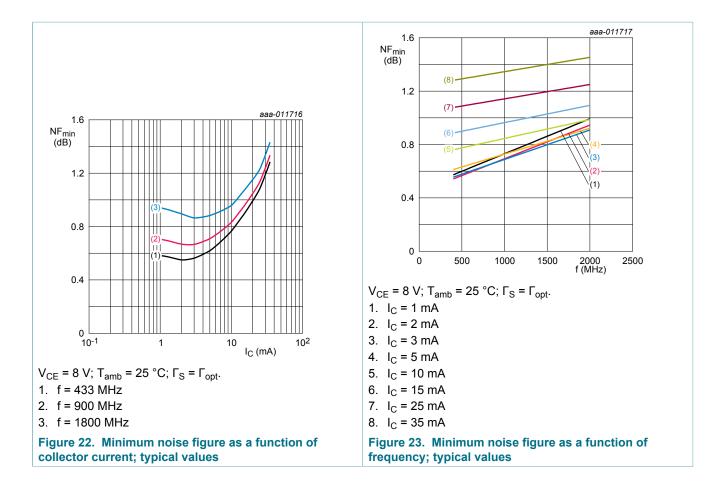
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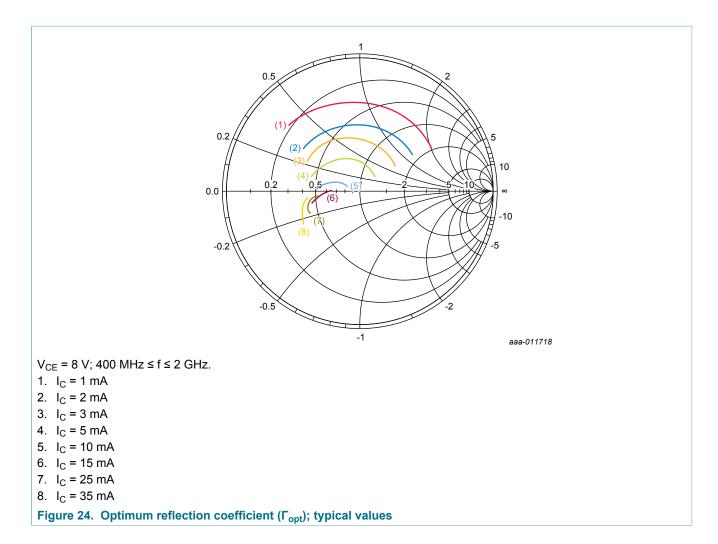
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10 Application information

More information about the following application example can be found in the application notes. See <u>Section 5 "Design support"</u>.

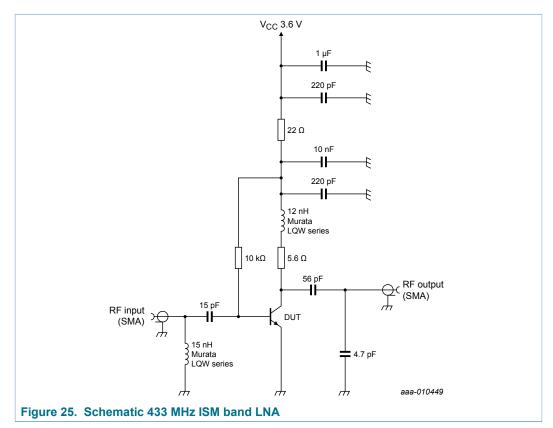
The following application example can be implemented using the evaluation kit. See <u>Section 3 "Ordering information"</u> for the order type number.

The following application example can be simulated using the simulation package. See <u>Section 5 "Design support"</u>.

10.1 Application example: 433 ISM band LNA

433 ISM band LNA, optimized for low noise.

More detailed information of the application example can be found in the application note: *AN11443*.



Remark: fine tuning of components maybe required depending on PCB parasitics.

Table 10. Application performance data at 433 MHz

 $I_{CC} = 20 \text{ mA}; V_{CC} = 3.6 \text{ V}$

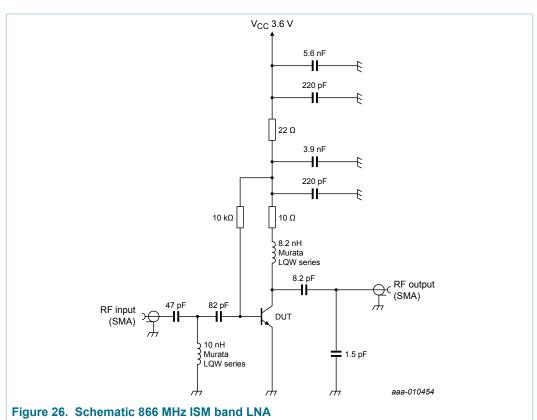
Symbol	Parameter	Conditions	N	/lin	Тур	Мах	Unit
s ₂₁ ²	insertion power gain		-		21	-	dB
NF	noise figure		-		1.3	-	dB
IP3 _o	output third-order intercept point	$f_1 = 433.1 \text{ MHz}; f_2 = 433.2 \text{ MHz};$ $P_i = -30 \text{ dBm per carrier}$	-		19	-	dBm

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10.2 Application example: 866 ISM band LNA

866 ISM band LNA, optimized for low noise.

More detailed information of the application example can be found in the application note: *AN11444*.



Remark: fine tuning of components maybe required depending on PCB parasitics.

Table 11. Application performance data at 866 MHz

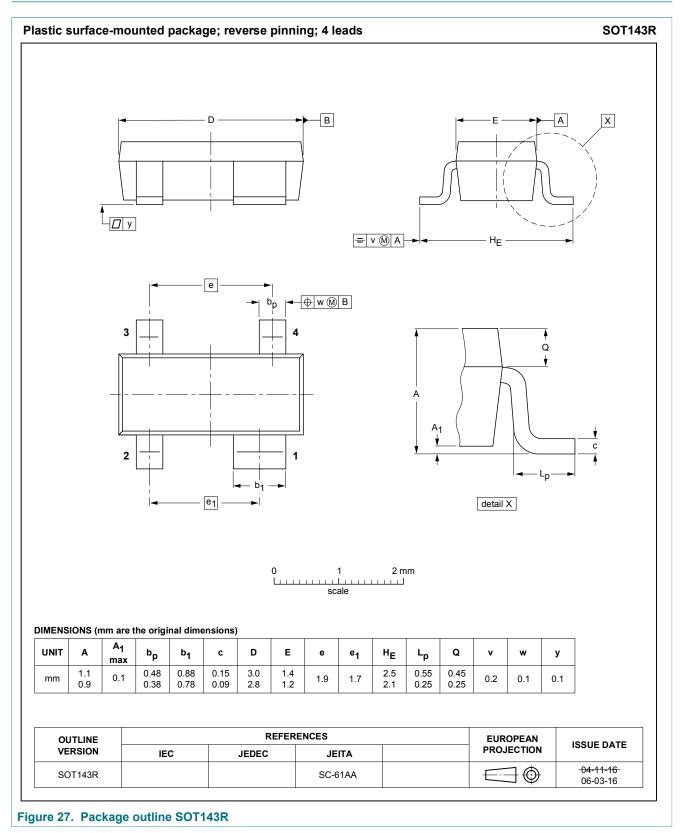
0 mA; V _{CC} = 3.6 V
0 mA; V _{CC} = 3.6 V

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
s ₂₁ ²	insertion power gain		-	15	-	dB
NF	noise figure		-	1.4	-	dB
IP3 _o	output third-order intercept point	f ₁ = 866.1 MHz; f ₂ = 866.2 MHz; P _i = -30 dBm per carrier	-	19	-	dBm

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11 Package outline



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12 Handling information

CAUTION

This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices. Such precautions are described in the *ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A* or equivalent standards.

13 Abbreviations

Acronym	Description
AEC	automotive electronics council
ISM	industrial, scientific, and medical
LNA	low-noise amplifier
MSG	maximum stable gain
NPN	negative-positive-negative
SMA	SubMiniature version A

14 Revision history

Table 13. Revision history

	3				
Document ID	Release date	Data sheet status	Change notice	Supersedes	
BFU550XR v.2	20190412	Product data sheet	-	BFU550XR v.1	
modification	 Adapted Schematic 866 MHz ISM band LNA. Biasing on the schematic is adapted according the EVB to do the RF/DC. Connection of 10 K resistor moved to the other side of the 82 pF capacitor 				
BFU550XR v.1	20140314	Product data sheet	-	-	

15 Legal information

15.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

[2] [3] The term 'short data sheet' is explained in section "Definitions".

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