

HRU0103C

Silicon Schottky Barrier Diode for Rectifying

REJ03G0071-0300
Rev.3.00
Jan 09, 2009

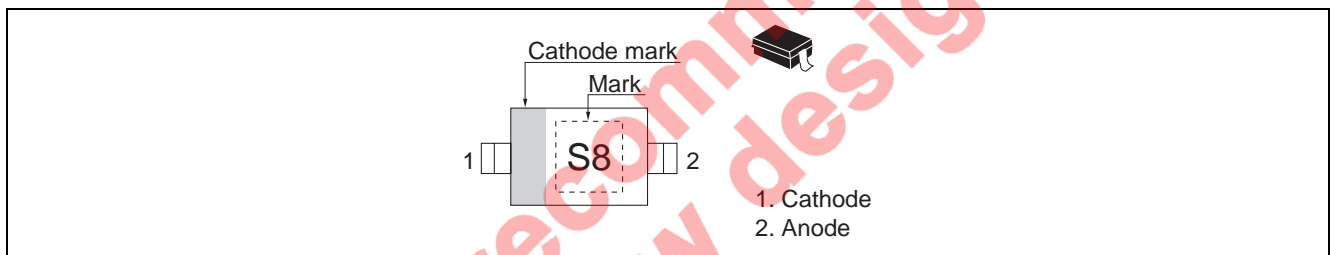
Features

- Low reverse voltage drop and suitable for high efficiency reverse current.
- Ultra small Resin Package (URP) is suitable for compact and high-density surface mount design.

Ordering Information

Part No.	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)
HRU0103CTRF	S8	URP	PTSP0002ZA-A	TRF (3,000 pcs / reel)

Pin Arrangement



Not recommended for new design

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}^{*1}	30	V
Reverse voltage	V_R	30	V
Average rectified current	I_O^{*1}	100	mA
Peak forward surge current	I_{FM}	300	mA
Non-Repetitive peak forward surge current	I_{FSM}^{*2}	1	A
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

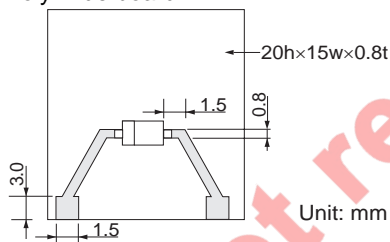
- Notes: 1. See from Fig.3 to Fig.5.
2. 10 ms sine wave 1 pulse.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	—	—	0.40	V	$I_F = 10 \text{ mA}$
	V_{F2}	—	—	0.60	V	$I_F = 100 \text{ mA}$
Reverse current	I_{R1}	—	—	0.1	μA	$V_R = 5 \text{ V}$
	I_{R2}	—	—	0.2		$V_R = 10 \text{ V}$
Capacitance	C	—	—	8.0	pF	$V_R = 0.5 \text{ V}, f = 1 \text{ MHz}$
Thermal resistance	$R_{th(j-a)}$	—	600	—	°C/W	Polyimide board ^{*1}

Note: 1. Polyimide board



Main Characteristics

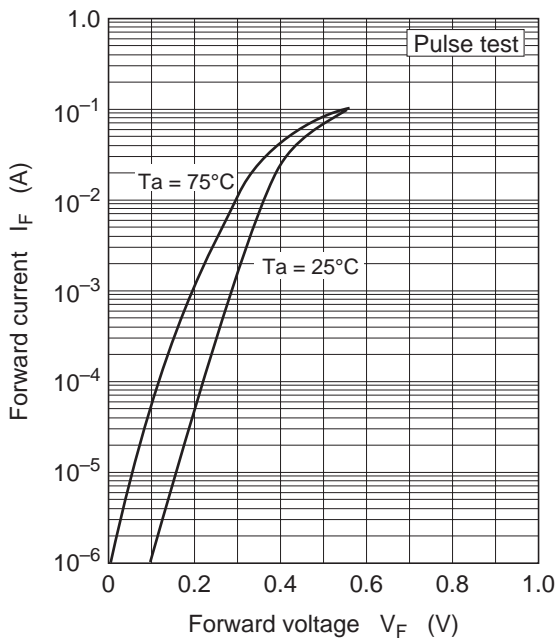


Fig.1 Forward current vs. Forward voltage

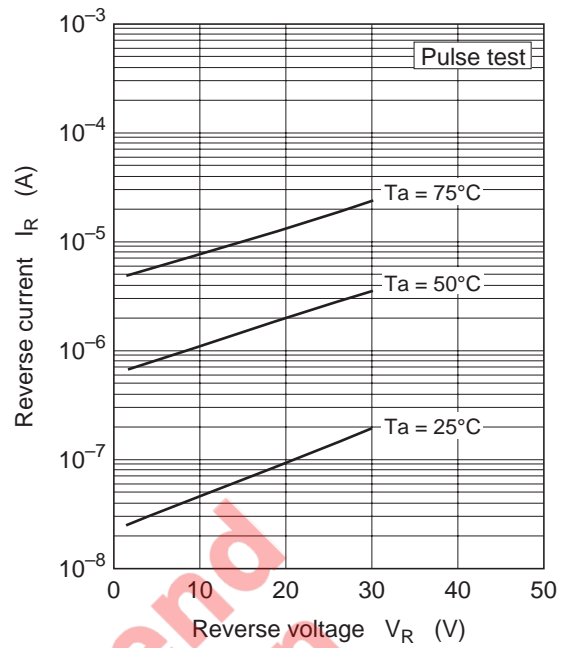


Fig.2 Reverse current vs. Reverse voltage

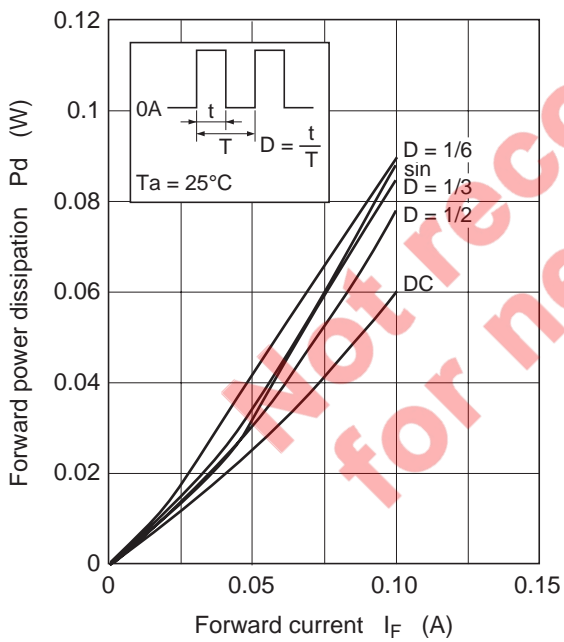


Fig3. Forward power dissipation vs. Forward current

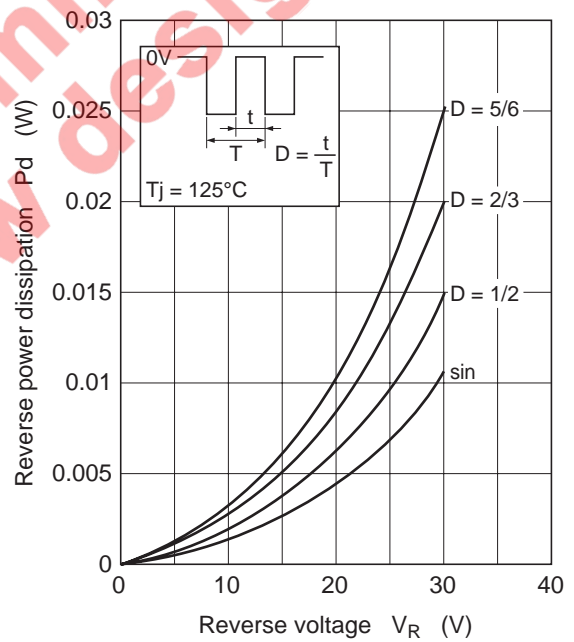


Fig4. Reverse power dissipation vs. Reverse voltage

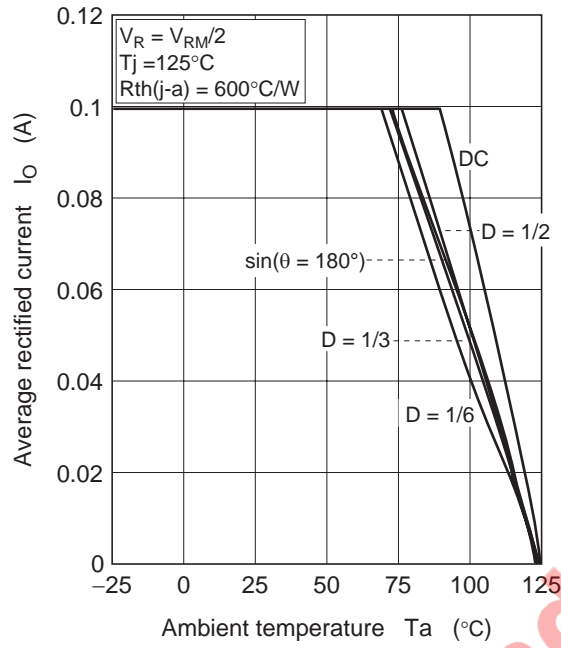
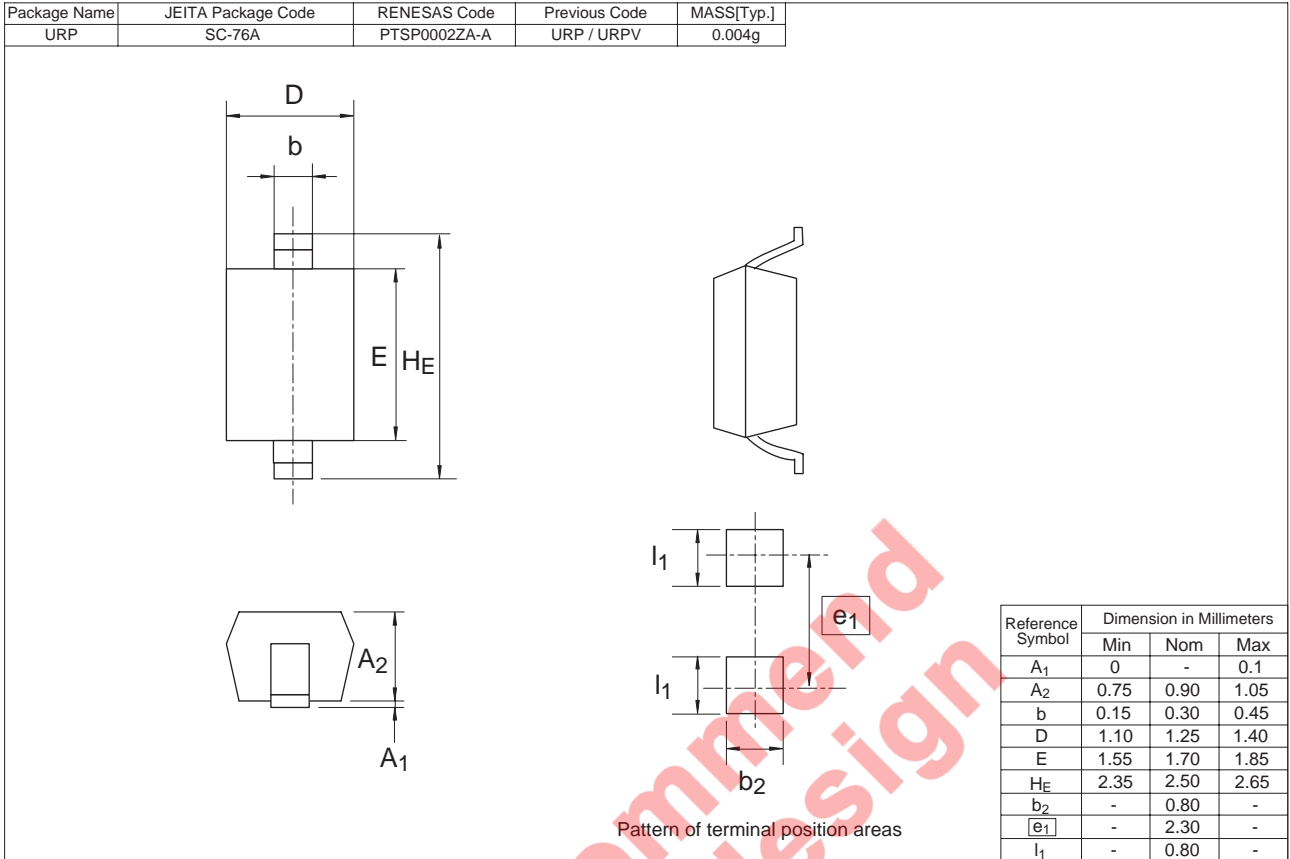


Fig.5 Average rectified current vs. Ambient temperature

Not recommended for new design

Package Dimensions



Not recommended for new design

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