NSR0130M2

Schottky Barrier Diode

These Schottky barrier diodes are designed for high-speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand-held and portable applications where space is limited.

Features

- Extremely Fast Switching Speed
- Extremely Low Forward Voltage 0.385 V (max) @ $I_F = 10 \text{ mA}$
- Low Reverse Current
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	30	Vdc
Forward Current DC	I _F	100	mA
Forward Current Surge Peak (60 Hz, 1 cycle)	I _{FSM}	1.0	Α
ESD Rating: Class 3B per Human Body Mode Class B per Machine Model	ĺ		

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) T _A = 25°C	P _D	200	mW
Derate above 25°C		2.0	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	600	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +125	°C

^{1.} FR-5 Minimum Pad.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

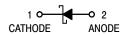
Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Leakage (V _R = 10 V) (V _R = 30 V)	I _R	1 1	-	0.35 3.0	μΑ
Forward Voltage (I _F = 10 mA) (I _F = 100 mA)	V _F	-	<u>-</u>	0.385 0.525	Vdc

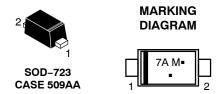


ON Semiconductor®

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30 V SCHOTTKY BARRIER DIODE





7A = Specific Device Code

M = Month Code

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

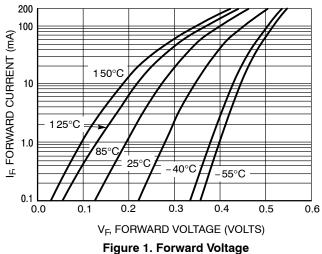
Device	Package	Shipping†
NSR0130M2T5G	SOD-723	2 mm Pitch 8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

NSR0130M2

1000

T_A = 150°C



IR, REVERSE CURRENT (µA) 100 T_A = 125°C 10 T_A = 85°C 1.0 T_A = 25°C 0.1 0.01 0.001 10 15 30 V_R, REVERSE VOLTAGE (VOLTS)

Figure 2. Leakage Current

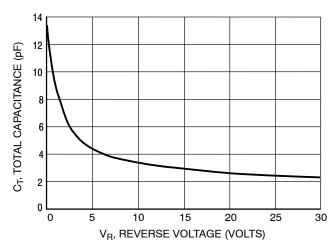
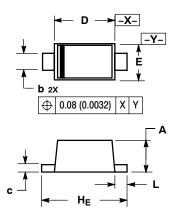


Figure 3. Total Capacitance

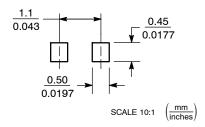


SOD-723 CASE 509AA-01 **ISSUE O**

DATE 02 MAR 2005



SOLDERING FOOTPRINT*



SOD-723

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- Y 14.5M, 1982.

 2. CONTROLLING DIMENSION: MILLIMETER.

 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	MILLIMETERS		INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.49	0.52	0.55	0.019	0.020	0.022
b	0.25	0.28	0.32	0.0098	0.011	0.013
С	0.08	0.12	0.15	0.0032	0.0047	0.0059
D	0.95	1.00	1.05	0.037	0.039	0.041
E	0.55	0.60	0.65	0.022	0.024	0.026
HE	1.35	1.40	1.45	0.053	0.055	0.057
L	0.15	0.20	0.25	0.006	0.0079	0.010

GENERIC MARKING DIAGRAM*



XX = Specific Device Code M = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

DOCUMENT NUMBER:	98AON20359D
STATUS:	ON SEMICONDUCTOR STANDARD
NEW STANDARD:	
DESCRIPTION:	SOD-723, 2-LEAD, 1.4X0.6X0.52 MM

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DOCUMENT	NUMBER:
98AON20359)D

PAGE 2 OF 2

ISSUE	REVISION	DATE
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