

RJK1560DPP-M0

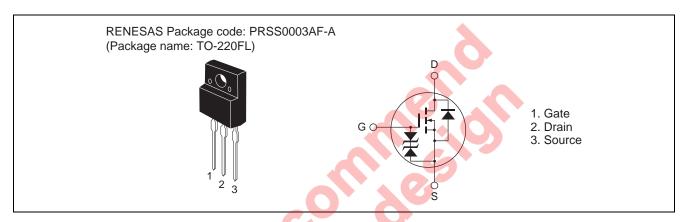
Silicon N Channel MOS FET High Speed Power Switching

R07DS0270EJ0100 Rev.1.00 Mar 07, 2011

Features

- Capable of 2.5 V gate drive
- Low on-resistance $R_{DS(on)}=0.043~\Omega~typ.~(at~I_D=10~A,~V_{GS}=4~V,~Ta=25^{\circ}C)$
- Low leakage current
- High speed switching

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	150	V
Gate to source voltage	V_{GSS}	±10	V
Drain current	I _D	20	А
Drain peak current	I _{D (pulse)} Note1	80	А
Body-drain diode reverse drain current	I _{DR}	20	А
Body-drain diode reverse drain peak current	I _{DR} (pulse)	80	А
Avalanche current	I _{AP} Note3	16	А
Avalanche energy	E _{AR} Note3	19.2	mJ
Channel dissipation	Pch Note2	28.5	W
Channel to case thermal impedance	θch-c	4.38	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at $Tc = 25^{\circ}C$
- 3. STch = 25° C, Tch $\leq 150^{\circ}$ C

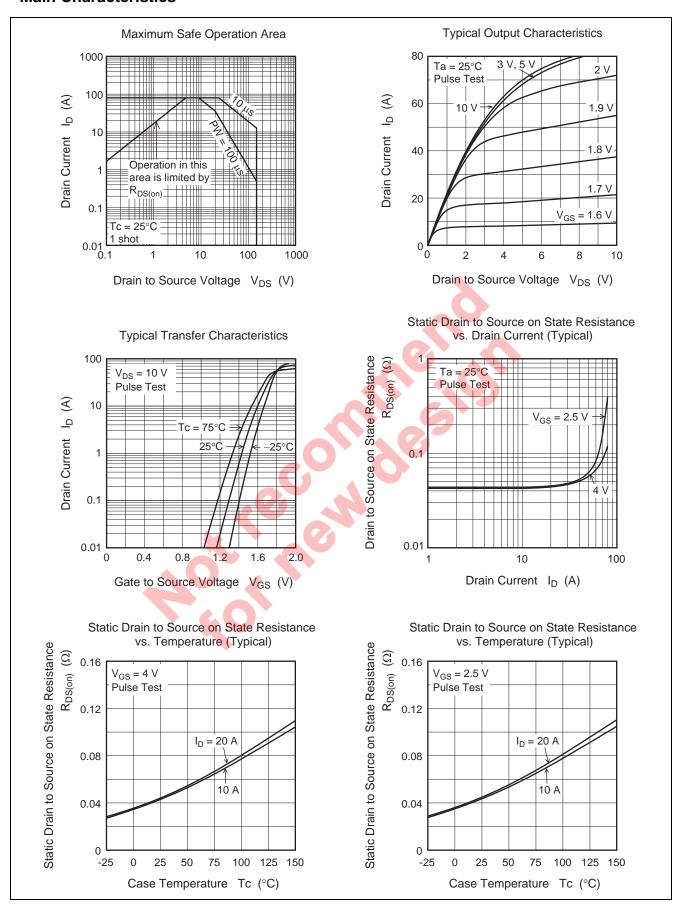
Electrical Characteristics

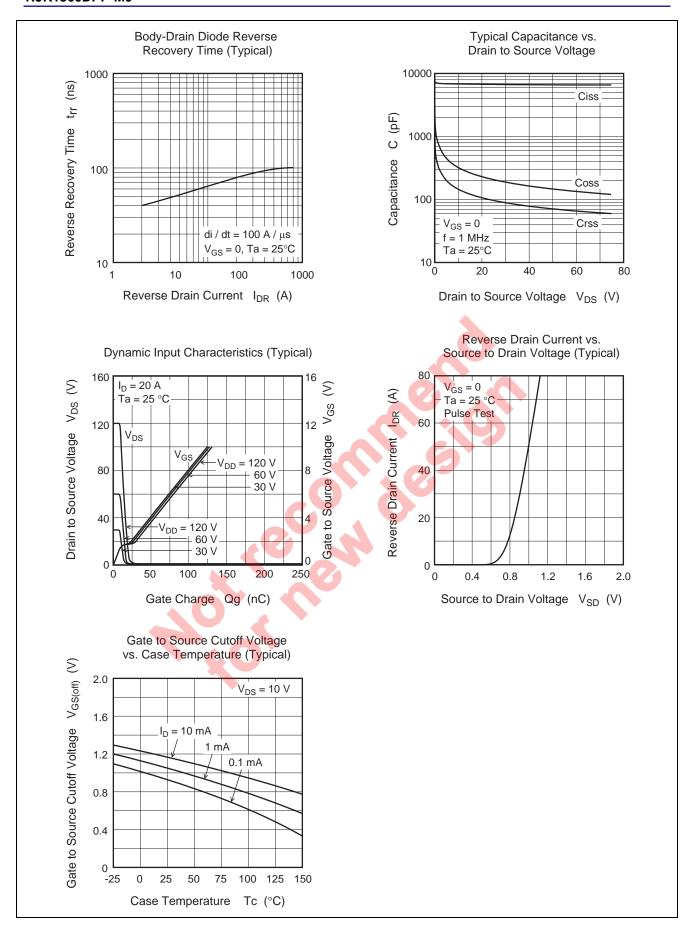
 $(Ta = 25^{\circ}C)$

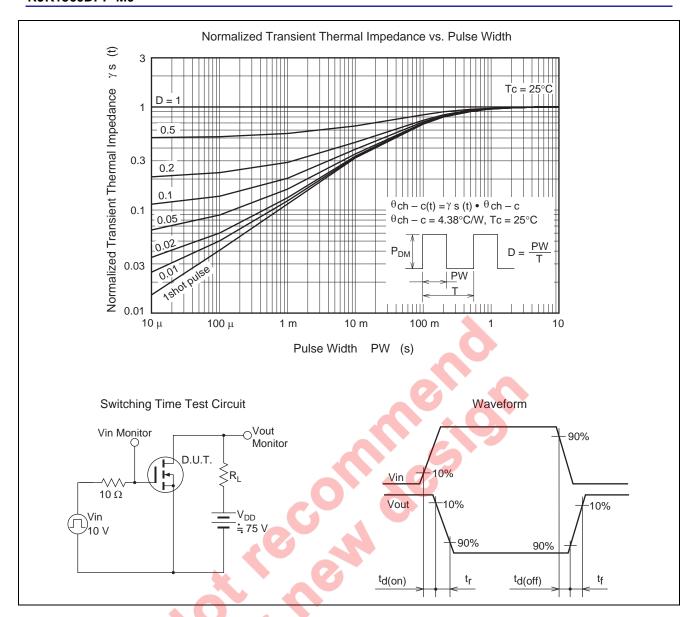
Item	Symbol	Min	Тур	Max	Unit	Test conditions	
Drain to source breakdown voltage	V _{(BR)DSS}	150	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Gate to source breakdown voltage	V _{(BR)GSS}	±10	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$	
Zero gate voltage drain current	I _{DSS}		_	1	μΑ	V _{DS} = 150 V, V _{GS} = 0	
Gate to source leak current	I _{GSS}		_	±10	μΑ	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0$	
Gate to source cutoff voltage	$V_{GS(off)}$	0.5	_	1.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	
Static drain to source on state	R _{DS(on)}	_	0.043	0.060	Ω	$I_D = 10 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note4}}$	
resistance	R _{DS(on)}	_	0.044	0.070	Ω	$I_D = 10 \text{ A}, V_{GS} = 2.5 \text{ V}^{\text{Note4}}$	
Input capacitance	Ciss	_	6720	_	pF	V _{DS} = 25 V	
Output capacitance	Coss	_	205	_	pF	$V_{GS} = 0$	
Reverse transfer capacitance	Crss	_	102	_	pF	f = 1 MHz	
Turn-on delay time	t _{d(on)}		43	_	ns	I _D = 10 A	
Rise time	t _r		79	_	ns	$V_{GS} = 4 V$	
Turn-off delay time	t _{d(off)}		250	_	ns	$R_L = 7.5 \Omega$	
Fall time	t _f		117	_	ns	$Rg = 10 \Omega$	
Total gate charge	Qg		52		nC	V _{DD} = 120 V	
Gate to source charge	Qgs		13	40	nC	$V_{GS} = 4 V$	
Gate to drain charge	Qgd		14		nÇ	$I_{D} = 20 \text{ A}$	
Body-drain diode forward voltage	V_{DF}		0.83	1.30	V	I _F = 20 A, V _{GS} = 0 Note4	
Body-drain diode reverse recovery time	t _{rr}		75	→ _	ns	$I_F = 20 \text{ A}, V_{GS} = 0$	
						di _F /dt = 100 A/μs	
Notes: 4. Pulse test							

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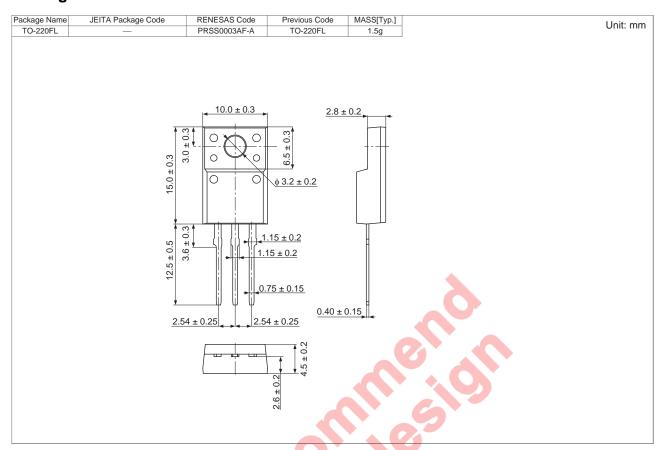
Main Characteristics







Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK1560DPP-M0-T2	600 pcs	Box (Tube)

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Renesas Electronics America Inc. 2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-2353-1155, Fax: +86-10-8235-7679

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 161F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2868-9318, Fax: +852-2886-9022/9044

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei, Taiv Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 1 harbourFront Avenue, #06-10, keppel Bay Tower, Singapore 098632 Tel: +65-6213-0200, Fax: +65-6278-8001

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd. 11F., Samik Lavied' or Bidg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea Tel: 482-2-558-3737, Fax: 482-2-558-5141