74HC365; 74HCT365 Hex buffer/line driver; 3-state Rev. 4 — 27 January 2016

Product data sheet

General description

The 74HC365; 74HCT365 is a hex buffer/line driver with 3-state outputs controlled by the output enable inputs (OEn). A HIGH on OEn causes the outputs to assume a high impedance OFF-state. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC}.

Features and benefits 2.

- Inverting outputs
- Input levels:
 - ◆ For 74HC365: CMOS level
 - For 74HC365: TTL level
- Complies with JEDEC standard no. 7A
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - ♦ MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Multiple package options

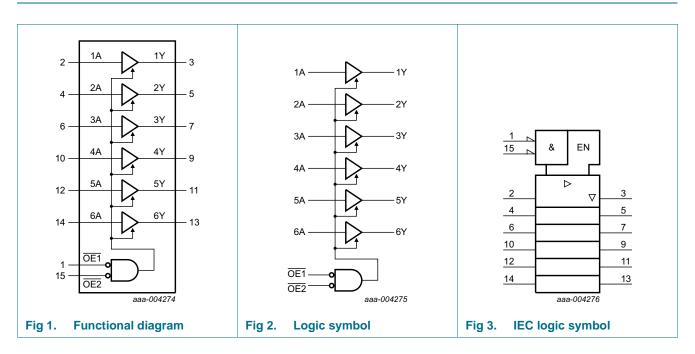
Ordering information

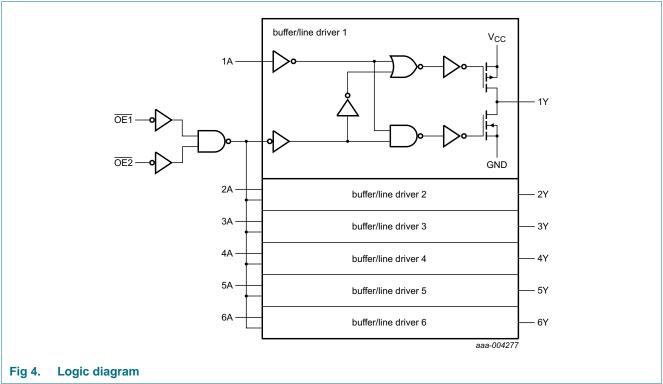
Table 1. **Ordering information**

Type number	Package			
	Temperature range	Name	Description	Version
74HC365D	–40 °C to +125 °C	SO16	plastic small outline package; 16 leads; body width 3.9 mm	SOT109-1
74HCT365D	-			
74HC365DB	–40 °C to +125 °C	SSOP16	plastic shrink small outline package; 16 leads; body width	SOT338-1
74HCT365DB	-		5.3 mm	
74HC365PW	-40 °C to +125 °C	TSSOP16	plastic thin shrink small outline package; 16 leads; body	SOT403-1
74HCT365PW	-		width 4.4 mm	



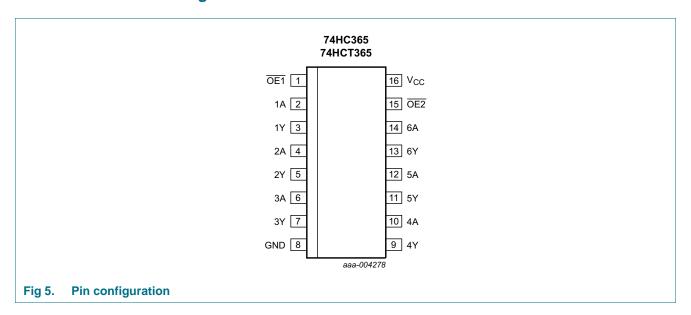
4. Functional diagram





5. Pinning information

5.1 Pinning



5.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
OE1	1	output enable input 1 (active LOW)
1A	2	data input 1
1Y	3	data output 1
2A	4	data input 2
2Y	5	data output 2
3A	6	data input 3
3Y	7	data output 3
GND	8	ground (0 V)
4Y	9	data output 4
4A	10	data input 4
5Y	11	data output 5
5A	12	data input 5
6Y	13	data output 6
6A	14	data input 6
OE2	15	output enable input 2 (active LOW)
V _{CC}	16	supply voltage

6. Functional description

Table 3. Function table [1]

Control	Control		Output
OE1	OE2	nA	nY
L	L	L	L
L	L	Н	Н
X	Н	X	Z
Н	X	X	Z

^[1] H = HIGH voltage level;

L = LOW voltage level;

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+7	V
I _{IK}	input clamping current	$V_I < -0.5 \text{ V or } V_I > V_{CC} + 0.5 \text{ V}$	-	±20	mA
I _{OK}	output clamping current	$V_O < -0.5 \text{ V or } V_O > V_{CC} + 0.5 \text{ V}$	-	±20	mA
Io	output current	$V_{O} = -0.5 \text{ V to } (V_{CC} + 0.5 \text{ V})$	-	±35	mA
I _{CC}	supply current		-	70	mA
I _{GND}	ground current		-70	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	SO16 package	-	500	mW
		SSOP16 package	-	500	mW
		TSSOP16 package	-	500	mW

^[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

X = don't care;

Z = high-impedance OFF-state.

^[2] For SO16 packages: Ptot derates linearly with 8 mW/K above 70 °C.

^[3] For SSOP16 and TSSOP16 packages: Ptot derates linearly with 5.5 mW/K above 60 °C.

Recommended operating conditions

Recommended operating conditions

Voltages are referenced to GND (ground = 0 V)

Symbol	Parameter	Conditions		74HC365	5	7	4HCT36	5	Unit
			Min	Тур	Max	Min	Тур	Max	
V _{CC}	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
VI	input voltage		0	-	V _{CC}	0	-	V _{CC}	V
Vo	output voltage		0	-	V _{CC}	0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	$V_{CC} = 2.0 \text{ V}$	-	-	625	-	-	-	ns/V
		V _{CC} = 4.5 V	-	1.67	139	-	1.67	139	ns/V
		$V_{CC} = 6.0 \text{ V}$	-	-	83	-	-	-	ns/V

Static characteristics

Static characteristics 74HC365 Table 6.

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	5 °C					
V _{IH}	HIGH-level input voltage	$V_{CC} = 2.0 \text{ V}$	1.5	1.2	-	V
		V _{CC} = 4.5 V	3.15	2.4	-	V
		V _{CC} = 6.0 V	4.2	3.2	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 2.0 V	-	8.0	0.5	V
		V _{CC} = 4.5 V	-	2.1	1.35	V
		V _{CC} = 6.0 V	-	2.8	1.8	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}	-	-	-	
		$I_{O} = -20 \mu A; V_{CC} = 2.0 V$	1.9	2.0	-	V
		$I_{O} = -20 \mu A; V_{CC} = 4.5 V$	4.4	4.5	-	V
		$I_{O} = -20 \mu A; V_{CC} = 6.0 V$	5.9	6.0	-	V
		$I_{O} = -6.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.98	4.32	-	V
		$I_{O} = -7.8 \text{ mA}; V_{CC} = 6.0 \text{ V}$	5.48	5.81	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = 20 \mu A; V_{CC} = 2.0 V$	-	0	0.1	V
		$I_O = 20 \mu A; V_{CC} = 4.5 V$	-	0	0.1	V
		$I_O = 20 \mu A; V_{CC} = 6.0 V$	-	0	0.1	V
		$I_{O} = 6.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	0.15	0.26	V
		$I_{O} = 7.8 \text{ mA}; V_{CC} = 6.0 \text{ V}$	-	0.16	0.26	V
l _l	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	±0.1	μΑ
l _{oz}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	±0.5	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 6.0$ V	-	-	8.0	μΑ
Cı	input capacitance		-	3.5	-	рF

Table 6. Static characteristics 74HC365 ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -	40 °C to +85 °C					
V _{IH}	HIGH-level input voltage	$V_{CC} = 2.0 \text{ V}$	1.5	-	-	V
		V _{CC} = 4.5 V	3.15	-	-	V
		V _{CC} = 6.0 V	4.2	-	-	V
V _{IL}	LOW-level input voltage	$V_{CC} = 2.0 \text{ V}$	-	-	0.5	V
		V _{CC} = 4.5 V	-	-	1.35	V
		V _{CC} = 6.0 V	-	-	1.8	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_{O} = -20 \mu A; V_{CC} = 2.0 V$	1.9	-	-	V
		$I_{O} = -20 \mu A; V_{CC} = 4.5 V$	4.4	-	-	V
		$I_{O} = -20 \mu A; V_{CC} = 6.0 V$	5.9	-	-	V
		$I_{O} = -6.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.84	-	-	V
		$I_{O} = -7.8 \text{ mA}; V_{CC} = 6.0 \text{ V}$	5.34	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = 20 \mu A; V_{CC} = 2.0 V$	-	-	0.1	V
		$I_O = 20 \mu A; V_{CC} = 4.5 V$	-	-	0.1	V
		$I_O = 20 \mu A; V_{CC} = 6.0 V$	-	-	0.1	V
		$I_{O} = 6.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.33	V
		$I_{O} = 7.8 \text{ mA}; V_{CC} = 6.0 \text{ V}$	-	-	0.33	V
I _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$;	-	-	±1.0	μΑ
l _{OZ}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	±5.0	μΑ
I _{CC}	supply current	$V_{I} = V_{CC}$ or GND; $I_{O} = 0$ A; $V_{CC} = 6.0 \text{ V}$	-	-	80	μΑ
T _{amb} = -	40 °C to +125 °C					
V _{IH}	HIGH-level input voltage	$V_{CC} = 2.0 \text{ V}$	1.5	-	-	V
		V _{CC} = 4.5 V	3.15	-	-	V
		V _{CC} = 6.0 V	4.2	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 2.0 V	-	-	0.5	V
		V _{CC} = 4.5 V	-	-	1.35	V
		V _{CC} = 6.0 V	-	-	1.8	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_{O} = -20 \mu A; V_{CC} = 2.0 V$	1.9	-	-	V
		$I_{O} = -20 \mu A; V_{CC} = 4.5 V$	4.4	-	-	V
		$I_{O} = -20 \mu A; V_{CC} = 6.0 V$	5.9	-	-	V
		$I_{O} = -6.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.7	-	-	V
		$I_{O} = -7.8 \text{ mA}; V_{CC} = 6.0 \text{ V}$	5.2	-	-	V

Table 6. Static characteristics 74HC365 ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		$I_O = 20 \mu A; V_{CC} = 2.0 \text{ V}$	-	-	0.1	V
		$I_O = 20 \mu A; V_{CC} = 4.5 V$	-	-	0.1	V
		$I_O = 20 \mu A; V_{CC} = 6.0 \text{ V}$	-	-	0.1	V
		$I_O = 6.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.4	V
		$I_O = 7.8 \text{ mA}; V_{CC} = 6.0 \text{ V}$	-	-	0.4	V
I _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	±1.0	μΑ
I _{OZ}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 6.0 \text{ V}$	-	-	±10.0	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 6.0$ V	-	-	160	μΑ

Table 7. Static characteristics 74HCT365

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	5 ℃					
V _{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0	1.6	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	1.2	0.8	V
V _{OH}	HIGH-level output	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$				
	voltage	$I_{O} = -20 \mu A$	4.4	4.5	-	V
		$I_{O} = -6.0 \text{ mA}$	3.98	4.32	-	V
V _{OL}	LOW-level output	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$				
	voltage	I _O = 20 μA	-	0	0.1	V
		$I_{O} = 6.0 \text{ mA}$	-	0.16	0.26	V
I _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	±0.1	μΑ
l _{OZ}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	±0.5	μΑ
Icc	supply current	$V_{I} = V_{CC}$ or GND; $I_{O} = 0$ A; $V_{CC} = 5.5$ V	-	-	8.0	μΑ
Δl _{CC}	additional supply current	$V_I = V_{CC} - 2.1 \text{ V}$; other inputs at V_{CC} or GND; $I_O = 0 \text{ A}$				
		pins nA	-	100	360	μΑ
		pin OE1	-	100	360	μΑ
		pin OE2	-	90	324	μΑ
Cı	input capacitance		-	3.5	-	pF
T _{amb} = -	40 °C to +85 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	-	0.8	V
V _{OH}	HIGH-level output	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$				
	voltage	$I_{O} = -20 \mu A$	4.4	-	-	V
		$I_{O} = -6.0 \text{ mA}$	3.84	-	-	V
V _{OL}	LOW-level output	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$				
	voltage	$I_{O} = 20 \mu A$	-	-	0.1	V
		$I_{O} = 6.0 \text{ mA}$	-	-	0.33	V
l _l	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	±1.0	μΑ
l _{OZ}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$			±5.0	μΑ

74HC_HCT365

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Table 7. Static characteristics 74HCT365 ... continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	80	μΑ
ΔI_{CC}	additional supply current	$V_I = V_{CC} - 2.1 \text{ V}$; other inputs at V_{CC} or GND; $I_O = 0 \text{ A}$				
		pins nA	-	-	450	μΑ
		pin OE1	-	-	450	μΑ
		pin OE2	-	-	405	μΑ
T _{amb} = -	40 °C to +125 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0	-	-	V
V_{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	-	0.8	V
V _{OH}	HIGH-level output	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$				
	voltage	$I_{O} = -20 \mu A$	4.4	-	-	V
		$I_{O} = -6.0 \text{ mA}$	3.7	-	-	V
V _{OL}	LOW-level output	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$				
	voltage	$I_{O} = 20 \mu A$	-	-	0.1	V
		$I_{O} = 6.0 \text{ mA}$	-	-	0.4	V
I _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	±1.0	μΑ
l _{OZ}	OFF-state output current	$V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	±10.0	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	160	μΑ
ΔI_{CC}	additional supply current	$V_I = V_{CC} - 2.1 \text{ V}$; other inputs at V_{CC} or GND; $I_O = 0 \text{ A}$				
		pins nA	-	-	490	μΑ
		pin OE1	-	-	490	μΑ
		pin OE2	-	-	441	μΑ

10. Dynamic characteristics

Dynamic characteristics 74HC365 Table 8.

Voltages are referenced to GND (ground = 0 V); $C_l = 50 \text{ pF}$ unless otherwise specified; see test circuit Figure 8.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
T _{amb} = 2	5 °C	,					
t _{pd}	propagation delay	nA to nY; see Figure 6	[1]				
		V _{CC} = 2.0 V		-	30	95	ns
		V _{CC} = 4.5 V		-	11	19	ns
		V _{CC} = 5 V; C _L = 15 pF		-	9	-	ns
		V _{CC} = 6.0 V		-	9	16	ns
t _{en}	enable time	OEn to nY; see Figure 7	[2]				
		V _{CC} = 2.0 V		-	47	150	ns
		V _{CC} = 4.5 V		-	17	30	ns
		V _{CC} = 6.0 V		-	14	26	ns
t _{dis}	disable time	OEn to nY; see Figure 7	[3]				
		V _{CC} = 2.0 V		-	61	150	ns
		V _{CC} = 4.5 V		-	22	30	ns
		V _{CC} = 6.0 V		-	18	26	ns
t _t	transition time	see Figure 6	[4]				
		V _{CC} = 2.0 V		-	14	60	ns
		V _{CC} = 4.5 V		-	5	12	ns
		V _{CC} = 6.0 V		-	4	10	ns
C _{PD}	power dissipation capacitance	per buffer; $V_I = GND$ to V_{CC}	<u>[5]</u>	-	40	-	pF
T _{amb} = -	40 °C to +85 °C						
t _{pd}	propagation delay	nA to nY; see Figure 6	<u>[1]</u>				
		V _{CC} = 2.0 V		-	-	120	ns
		V _{CC} = 4.5 V		-	-	24	ns
		V _{CC} = 6.0 V		-	-	20	ns
t _{en}	enable time	OEn to nY; see Figure 7	[2]				
		V _{CC} = 2.0 V		-	-	190	ns
		V _{CC} = 4.5 V		-	-	38	ns
		V _{CC} = 6.0 V		-	-	33	ns
t _{dis}	disable time	OEn to nY; see Figure 7	<u>[3]</u>				
		V _{CC} = 2.0 V		-	-	190	ns
		V _{CC} = 4.5 V		-	-	38	ns
		V _{CC} = 6.0 V		-	-	33	ns
t _t	transition time	see Figure 6	<u>[4]</u>				
		V _{CC} = 2.0 V		-	-	75	ns
		V _{CC} = 4.5 V		-	-	15	ns
		V _{CC} = 6.0 V		-	-	13	ns

Table 8. Dynamic characteristics 74HC365 ... continued

Voltages are referenced to GND (ground = 0 V); C_L = 50 pF unless otherwise specified; see test circuit Figure 8.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
T _{amb} = -	40 °C to +125 °C						
t _{pd}	propagation delay	nA to nY; see Figure 6	<u>[1]</u>				
		V _{CC} = 2.0 V		-	-	145	ns
		V _{CC} = 4.5 V		-	-	29	ns
		V _{CC} = 6.0 V		-	-	25	ns
t _{en} enable time	enable time	OEn to nY; see Figure 7	[2]				
		V _{CC} = 2.0 V		-	-	225	ns
		V _{CC} = 4.5 V		-	-	45	ns
		V _{CC} = 6.0 V		-	-	38	ns
t _{dis}	disable time	OEn to nY; see Figure 7	[3]				
		V _{CC} = 2.0 V		-	-	225	ns
		V _{CC} = 4.5 V		-	-	45	ns
		V _{CC} = 6.0 V		-	-	38	ns
t _t	transition time	see Figure 6	<u>[4]</u>				
		V _{CC} = 2.0 V		-	-	90	ns
		V _{CC} = 4.5 V		-	-	18	ns
		V _{CC} = 6.0 V		-	-	15	ns

- [1] t_{pd} is the same as t_{PHL} and t_{PLH} .
- [2] t_{en} is the same as t_{PZH} and t_{PZL} .
- [3] t_{dis} is the same as t_{PHZ} and t_{PLZ} .
- [4] t_t is the same as t_{THL} and t_{TLH} .
- [5] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

 f_o = output frequency in MHz;

 C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\sum (C_L \times V_{CC}^2 \times f_o) = sum of outputs.$

Table 9. Dynamic characteristics 74HCT365

Voltages are referenced to GND (ground = 0 V); C_L = 50 pF unless otherwise specified; see test circuit Figure 8.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
T _{amb} = 2	5 °C						
t _{pd}	propagation delay	nA to nY; see Figure 6	<u>[1]</u>				
pa	V _{CC} = 4.5 V		-	14	25	ns	
_		$V_{CC} = 5 \text{ V}; C_L = 15 \text{ pF}$		-	11	-	ns
t _{en}	enable time	OEn to nY; V _{CC} = 4.5 V; see Figure 7	[2]	-	18	35	ns
t _{dis}	disable time	OEn to nY; V _{CC} = 4.5 V; see Figure 7	[3]	-	23	35	ns
t _t	transition time	V _{CC} = 4.5 V; see <u>Figure 6</u>	[4]	-	5	12	ns
C _{PD}	power dissipation capacitance	per buffer; $V_I = GND$ to $(V_{CC} - 1.5 V)$	<u>[5]</u>	-	40	-	pF

74HC HCT365

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Table 9. Dynamic characteristics 74HCT365 ... continued

Voltages are referenced to GND (ground = 0 V); C_L = 50 pF unless otherwise specified; see test circuit Figure 8.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -	40 °C to +85 °C					
t _{pd}	propagation delay	nA to nY; $V_{CC} = 4.5 \text{ V}$; see Figure 6	-	-	31	ns
t _{en}	enable time	$\overline{\text{OEn}}$ to nY; $V_{CC} = 4.5 \text{ V}$; see $\underline{\text{Figure 7}}$	-	-	44	ns
t _{dis}	disable time	$\overline{\text{OEn}}$ to nY; $V_{CC} = 4.5 \text{ V}$; see $\overline{\text{Figure 7}}$	-	-	44	ns
t _t	transition time	$V_{CC} = 4.5 \text{ V}$; see Figure 6	-	-	15	ns
T _{amb} = -4	40 °C to +125 °C					
t _{pd}	propagation delay	nA to nY; $V_{CC} = 4.5 \text{ V}$; see Figure 6	-	-	38	ns
t _{en}	enable time	$\overline{\text{OEn}}$ to nY; $V_{CC} = 4.5 \text{ V}$; see $\overline{\text{Figure 7}}$	-	-	53	ns
t _{dis}	disable time	$\overline{\text{OEn}}$ to nY; $V_{CC} = 4.5 \text{ V}$; see $\overline{\text{Figure 7}}$	-	-	53	ns
t _t	transition time	$V_{CC} = 4.5 \text{ V}$; see Figure 6	-	-	18	ns

- [1] tpd is the same as tpHL and tpLH.
- [2] t_{en} is the same as t_{PZH} and t_{PZL} .
- [3] t_{dis} is the same as t_{PHZ} and t_{PLZ} .
- [4] t_t is the same as t_{THL} and t_{TLH} .
- [5] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

f_o = output frequency in MHz;

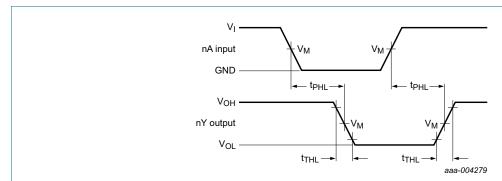
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\sum (C_L \times V_{CC}^2 \times f_o) = \text{sum of outputs.}$

11. Waveforms



Measurement points are given in Table 10.

 V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Propagation delay data input (nA) to output (nY) and output transition time Fig 6.

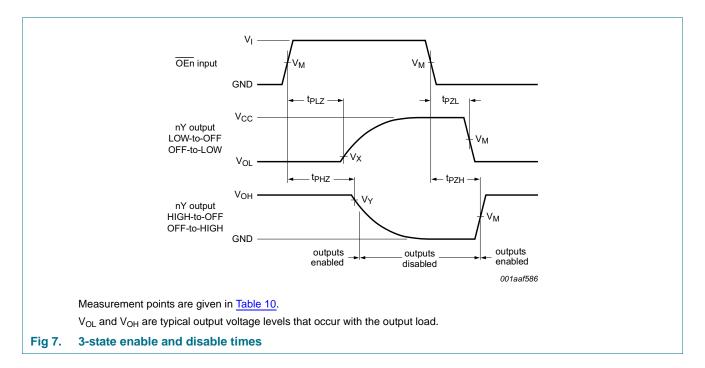
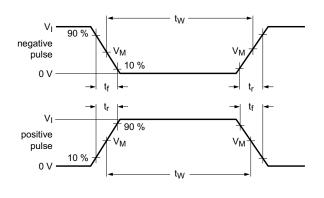
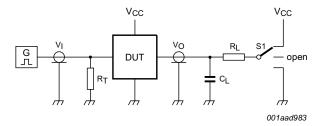


Table 10. Measurement points

Туре	Input	Output				
	V_{M}	V _M	V_X	V _Y		
74HC365	0.5V _{CC}	0.5V _{CC}	$0.1 \times V_{CC}$	$0.9 \times V_{CC}$		
74HCT365	1.3 V	1.3 V	$0.1 \times V_{CC}$	$0.9 \times V_{CC}$		





Test data is given in Table 11.

Definitions test circuit:

 R_T = Termination resistance should be equal to output impedance Z_0 of the pulse generator

C_L = Load capacitance including jig and probe capacitance

R_L = Load resistance

S1 = Test selection switch

Fig 8. Test circuit for measuring switching times

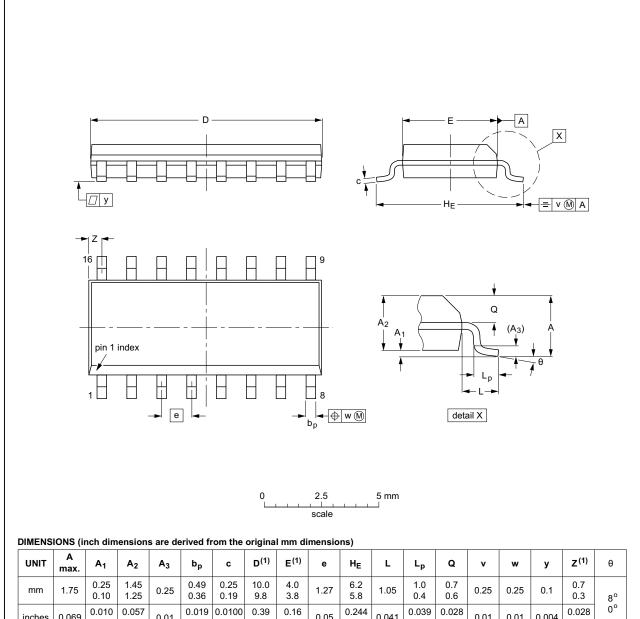
Table 11. Test data

Туре	Input		Load		S1 position			
	VI	t _r , t _f	CL	R _L	t _{PHL} , t _{PLH}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}	
74HC365	V _{CC}	6 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}	
74HCT365	3 V	6 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}	

12. Package outline

SO16: plastic small outline package; 16 leads; body width 3.9 mm

SOT109-1



UNIT	A max.	A ₁	A ₂	A ₃	bp	С	D ⁽¹⁾	E ⁽¹⁾	е	HE	L	Lp	Q	٧	w	у	Z ⁽¹⁾	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	10.0 9.8	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8°
inches	0.069	0.010 0.004	0.057 0.049	0.01		0.0100 0.0075	0.39 0.38	0.16 0.15	0.05	0.244 0.228	0.041	0.039 0.016		0.01	0.01	0.004	0.028 0.012	0°

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

OUTLINE		REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	N IEC	JEDEC	JEITA		PROJECTION	135UE DATE	
SOT109-1	076E07	MS-012				99-12-27 03-02-19	

Fig 9. Package outline SOT109-1 (SO16)

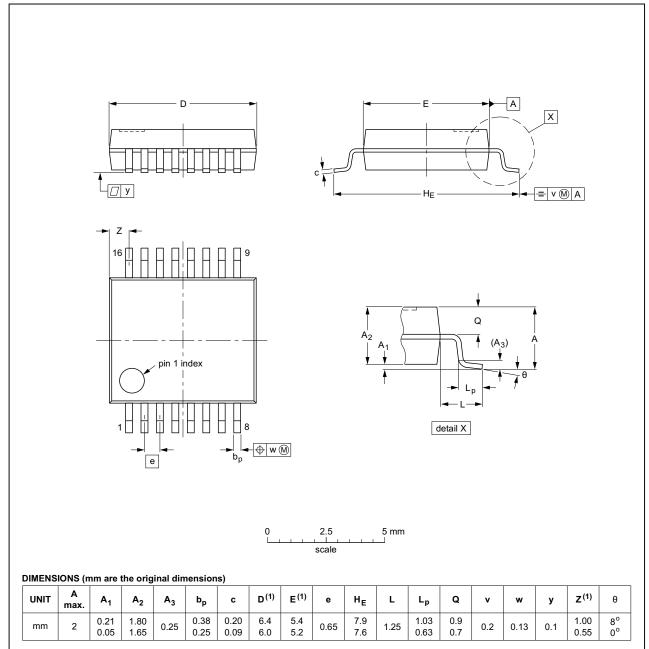
74HC_HCT365

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SSOP16: plastic shrink small outline package; 16 leads; body width 5.3 mm

SOT338-1



Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

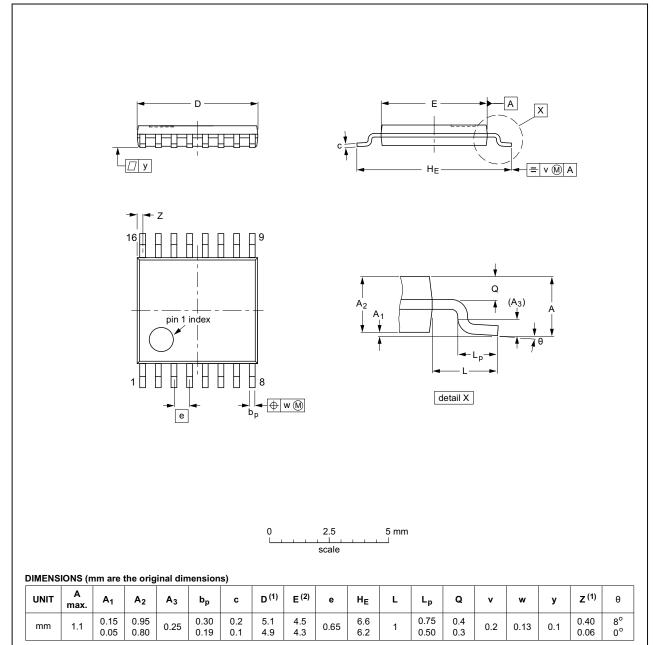
OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT338-1		MO-150			99-12-27 03-02-19

Fig 10. Package outline SOT338-1 (SSOP16)

74HC_HCT365

TSSOP16: plastic thin shrink small outline package; 16 leads; body width 4.4 mm

SOT403-1



Notes

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	RENCES	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE		
SOT403-1		MO-153			99-12-27 03-02-18		

Fig 11. Package outline SOT403-1 (TSSOP16)

74HC_HCT365

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13. Abbreviations

Table 12. Abbreviations

Acronym	Description
CMOS	Complementary Metal Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

14. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes			
74HC_HCT365 v.4	20160127	Product data sheet	-	74HC_HCT365 v.3			
Modifications:	Type numbers	Type numbers 74HC365N and 74HCT365N (SOT38-4) removed.					
74HC_HCT365 v.3	20120905	Product data sheet	-	74HC_HCT365_CNV v.2			
Modifications:		this data sheet has been rede NXP Semiconductors.	signed to comply wi	th the new identity			
	 Legal texts have been adapted to the new company name where appropriate. 						
74HC_HCT365_CNV v.2	19970829	Product specification	-	-			

15. Legal information

15.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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74HC HCT365

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74HC365; 74HCT365

Hex buffer/line driver; 3-state

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