

# PART NUMBER 54AC05

## Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All re-creations are done with the approval of the Original Component Manufacturer. (OCM)

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

#### **Quality Overview**

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
  - Class Q Military
  - Class V Space Level

Qualified Suppliers List of Distributors (QSLD)

 Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OCM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.



September 1998

#### 54AC05

### **Hex Inverter with Open Drain Outputs**

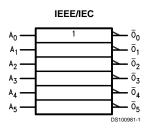
#### **General Description**

The 'AC05 contains six inverters.

#### **Features**

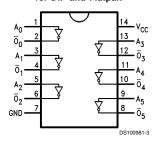
- Outputs sink 24 mA
- Open drain for wired NOR function
- Standard Microcircuit Drawing (SMD) 5962-9059001

#### **Logic Symbol**

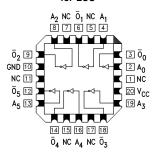


#### **Connection Diagrams**

Pin Assignment for DIP and Flatpak



Pin	Assignment
	for LCC



Pin Names	Description		
A <sub>n</sub>	Inputs		
$\overline{O}_n$	Outputs		

FACT™ is a trademark of Fairchild Semiconductor Corporation.

#### **Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage ( $V_{CC}$ ) -0.5V to +7.0V DC Input Diode Current ( $I_{IK}$ )  $V_{I} = -0.5V$  -20 mA  $V_{I} = V_{CC} + 0.5V$  +20 mA

 $V_1 - V_{CC} + 0.5V$ DC Input Voltage (V<sub>1</sub>) -0.5V to  $V_{CC} + 0.5V$ 

DC Output Diode Current ( $I_{OK}$ )

 $V_{O} = -0.5V$  -20 mA  $V_{O} = V_{CC} + 0.5V$  +20 mA

DC Output Voltage ( $V_{\rm O}$ ) = -0.5V to to  $V_{\rm CC}$  + 0.5V DC Output Source

or Sink Current ( $I_O$ ) DC  $V_{CC}$  or Ground Current

per Output Pin (I<sub>CC</sub> or I<sub>GND</sub>) ±50 mA

Storage Temperature ( $T_{STG}$ )  $-65^{\circ}C$  to +150 $^{\circ}C$ Junction Temperature ( $T_{J}$ ) CDIP 175 $^{\circ}C$ 

### Recommended Operating Conditions

Supply Voltage (V<sub>CC</sub>)

 $\begin{tabular}{lll} 'AC & 2.0V to 6.0V \\ Input Voltage (V_I) & 0V to V_{CC} \\ Output Voltage (V_O) & 0V to V_{CC} \\ \end{tabular}$ 

Operating Temperature (T<sub>A</sub>)

54AC -55°C to +125°C

Minimum Input Edge Rate ( $\Delta V/\Delta t$ )  $V_{IN}$  from 30% to 70% of  $V_{CC}$ 

 $V_{CC}$  @ 3.3V, 4.5V, 5.5V 125 mV/ns

Note 1: Absolute maximum ratings are those values beyond which damage to the device may occur. The databook specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. National does not recommend operation of FACT® circuits outside databook specifications.

#### DC Characteristics for 'AC Family Devices

			54AC			
Symbol	Parameter	V <sub>cc</sub>	T <sub>A</sub> = -55°C to +125°C	Units	Conditions	
		(V)				
			Guaranteed Limits			
V <sub>IH</sub>	Minimum High Level	3.0	2.1		V <sub>OUT</sub> = 0.1V	
	Input Voltage	4.5	3.15	V	or V <sub>CC</sub> – 0.1V	
		5.5	3.85			
V <sub>IL</sub>	Maximum Low Level	3.0	0.9		V <sub>OUT</sub> = 0.1V	
	Input Voltage	4.5	1.35	V	or V <sub>CC</sub> – 0.1V	
		5.5	1.65			
V <sub>OL</sub>	Maximum Low Level	3.0	0.1		I <sub>OUT</sub> = 50 μA	
	Output Voltage	4.5	0.1	V		
		5.5	0.1			
					(Note 2) V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub>	
		3.0	0.5		12 mA	
		4.5	0.5	V	I <sub>OL</sub> 24 mA	
		5.5	0.5		24 mA	
I <sub>IN</sub>	Maximum Input	5.5	±1.0	μA	V <sub>I</sub> = V <sub>CC</sub> , GND	
	Leakage Current					
I <sub>OHC</sub>	Output Leakage Current	5.5	-10.0	μA	V <sub>IN</sub> = V <sub>CC</sub>	
	High					
I <sub>OLD</sub>	Minimum Dynamic Output	5.5	50.0	mA	V <sub>OLD</sub> = 1.65V Max (Note 3)	
	Current					
I <sub>cc</sub>	Maximum Quiescent	5.5	80.0	μA	$V_{IN} = V_{CC}$	
	Supply Current				or GND	

±50 mA

Note 2: All outputs loaded; thresholds on input associated with output under test.

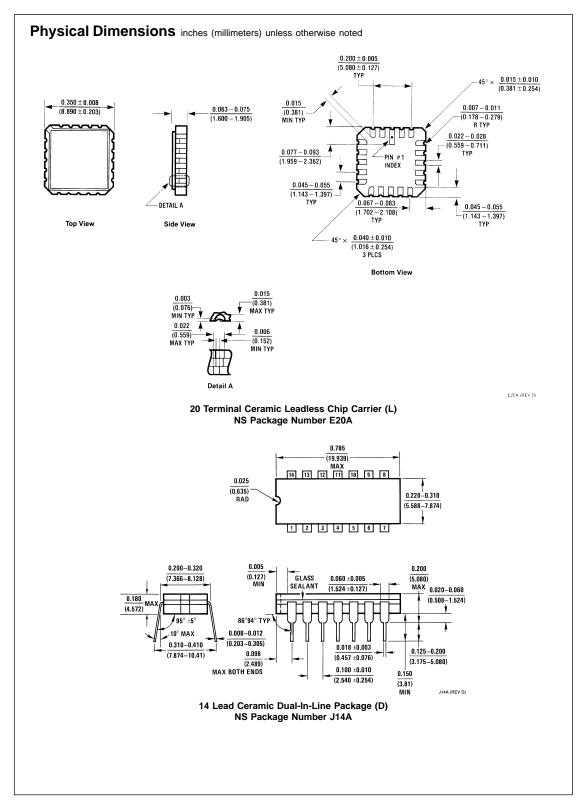
Note 3: Maximum test duration 2.0 ms, one output loaded at a time

AC Electrical Characteristics						
Symbol	Parameter	V <sub>CC</sub> (V) (Note 4)	54AC T <sub>A</sub> = -55°C to +125°C C <sub>L</sub> = 50 pF		Units	Fig. No.
			Min	Max		
t <sub>PLH</sub>	Propagation Delay	3.3	1.0	15.5	ns	
		5.0	1.0	15.5		
t <sub>PHL</sub>	Propagation Delay	3.3	1.0	8.0	ns	
		5.0	1.5	6.0		

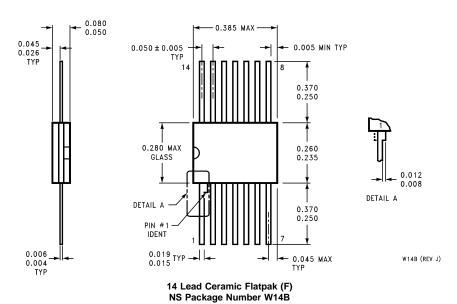
Note 4: Voltage Range 3.3 is 3.3V ±0.3V Voltage Range 5.0 is 5.0V ±0.5V

### Capacitance

Symbol	Parameter	Max	Units	Conditions
C <sub>IN</sub>	Input Capacitance	10.0	pF	V <sub>CC</sub> = Open
C <sub>PD</sub>	Power Dissipation	50.0	pF	V <sub>CC</sub> = 5.0V
	Capacitance			



#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



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