# August 2001

IGBT

# SGR20N40L / SGU20N40L

## **General Description**

**FAIRCHILD** 

Insulated Gate Bipolar Transistors (IGBTs) with a trench gate structure provide superior conduction and switching performance in comparison with transistors having a planar gate structure. They also have wide noise immunity. These devices are very suitable for strobe applications

#### Features

- High input impedance
- High peak current capability (150A)
- Easy gate drive
- Surface Mount : SGR20N40L
- Straight Lead : SGU20N40L

## Application

Strobe flash.



# Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Description	SGR / SGU20N40L	Units
V <sub>CES</sub>	Collector - Emitter Voltage	400	V
V <sub>GES</sub>	Gate - Emitter Voltage	± 6	V
I <sub>CM (1)</sub>	Pulsed Collector Current	150	A
P <sub>C</sub>	Maximum Power Dissipation @ $T_C = 25^{\circ}C$	45	W
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>stg</sub>	Storage Temperature Range	-40 to +150	°C
TL	Maximum Lead Temp. for soldering purposes, 1/8" from case for 5 seconds	300	°C

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

# **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		3.0	°C/W
R <sub>θJA</sub> (D-PAK)	Thermal Resistance, Junction-to-Ambient (PCB Mount) (2)		50	°C/W
R <sub>θJA</sub> (I-PAK)	Thermal Resistance, Junction-to-Ambient		110	°C/W

Notes :

(2) Mounted on 1" square PCB (FR4 or G-10 Material)

	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Char	acteristics					
BVCES	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 1mA$	450			V
	Collector Cut-Off Current	$V_{CE} = V_{CES}$ , $V_{CE} = 0V$			10	μA
I <sub>GES</sub>	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$			± 0.1	μΑ
On Char	acteristics		·			
	G-E Threshold Voltage	$I_{C} = 1 \text{mA}, V_{CE} = V_{CE}$	0.5	1.0	1.4	V
$V_{CE(sat)}$	C-E Saturation Current	$I_{\rm C} = 150$ A, $V_{\rm CF} = 4.5$ V	2.0	4.5	8.0	V
Jynamic C <sub>ies</sub>	Input Capacitance	N 0V/V 20V		3800		pF
Sies	Input Capacitance	$V_{05} = 0V_{05} = 30V_{05}$		3800		pF
Coes	Output Capacitance	- f = 1MHz		50		pF
S <sub>res</sub>	Reverse Transfer Capacitance			35		p⊦
Switchin	ng Characteristics	1		0.2		
d(on)	Rise Time	V <sub>CC</sub> = 300V, I <sub>C</sub> = 150A,		1.7		μ3
r 	Turn-Off Delay Time	$-V_{GE} = 4.5V, R_{G} = 15\Omega^{*}$		0.3	0.5	μ3 11S
	Fall Time	Resistive Load		1.5	2.0	μο μs
/						p

SGR20N40L / SGU20N40L



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SGR20N40L / SGU20N40L Rev. A1



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#### **PRODUCT STATUS DEFINITIONS**

#### Definition of Terms

Datasheet Identification	Product Status	Definition
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Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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- High Input Impedance
  High Peak Current Capability (150A)
  Easy Gate Drive
  Surface Mount: SGR20N40L

- Straight Lead: SGU20N40L

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Applications

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Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
SGR20N40LTF	Full Production	\$1.53	TO-252(DPAK)	2	TAPE REEL
SGR20N40LTM	Full Production	\$1.53	TO-252(DPAK)	2	TAPE REEL

\* 1,000 piece Budgetary Pricing

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Package & leads	Condition	Software version	Revision date
PSPICE			
TO-252(DPAK)-2	Electrical	9.2	May 23, 2001

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Application notes

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- High Input ImpedanceHigh Peak Current Capability (150A)
- Easy Gate Drive
- Surface Mount: SGR20N40L
- Straight Lead: SGU20N40L

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Applications

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Product status/pricing/packaging

Product	Product status	Pricing*	Package type	Leads	Packing method
SGU20N40LTU	Full Production	\$1.53	TO-251(IPAK)	3	RAIL

\* 1,000 piece Budgetary Pricing

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Models

Package & leads	Condition	Software version	Revision date
PSPICE			
TO-251(IPAK)-3	Electrical	9.2	May 23, 2001

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