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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 **Renesas Electronics Corporation**

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK1165, 2SK1166 Silicon N Channel MOS FET

REJ03G0914-0200 (Previous: ADE-208-1252) Rev.2.00 Sep 07, 2005

1. Gate

2. Drain (Flange) 3. Source

S

Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current •
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

-01-2

Outline

oduci RENESAS Package code: PRSS0004ZE-A (Package name: TO-3P) D G

Rev.2.00 Sep 07, 2005 page 1 of 6



Absolute Maximum Ratings

				$(Ta = 25^{\circ}C)$	
Item		Symbol	Ratings	Unit	
Drain to source voltage	2SK1165	V _{DSS}	450	V	
	2SK1166		500		
Gate to source voltage		V _{GSS}	±30	V	
Drain current		ID	12	А	
Drain peak current		I _{D(pulse)} * ¹	48	А	
Body to drain diode reverse drain current		I _{DR}	12	А	
Channel dissipation		Pch* ²	100	W	
Channel temperature		Tch	150	°C	
Storage temperature		Tstg	–55 to +150	°C	

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at $T_C = 25^{\circ}C$

Electrical Characteristics



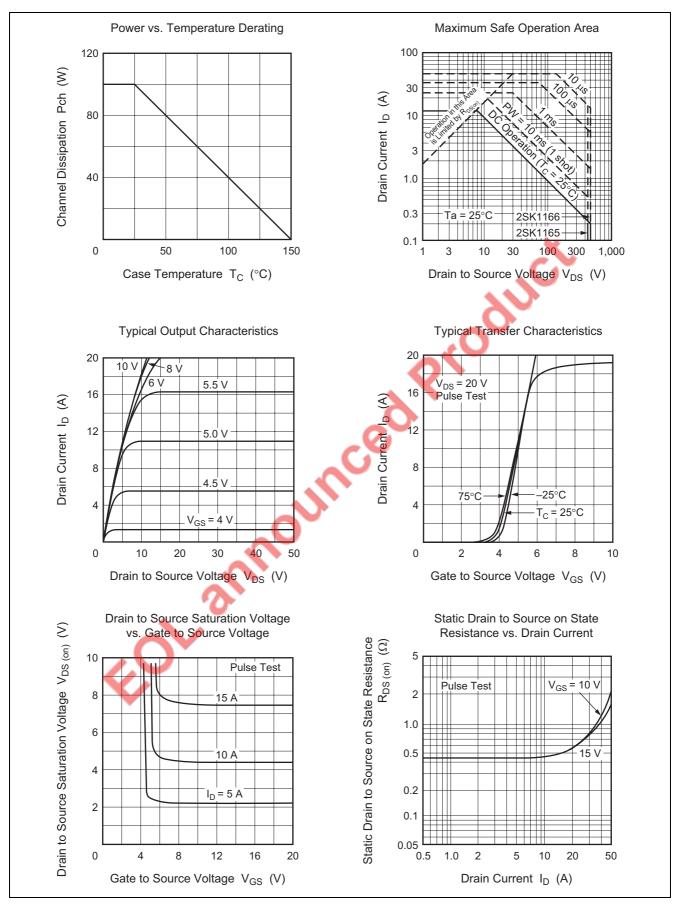
 $(Ta = 25^{\circ}C)$

Item		Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source	2SK1165	V _{(BR)DSS}	450	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
breakdown voltage	2SK1166	. ,	500				
Gate to source breakdown voltage		V _{(BR)GSS}	±30	_		V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current		I _{GSS}	_	—	±10	μΑ	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain	2SK1165	I _{DSS}	_	-	250	μΑ	$V_{DS} = 360 \text{ V}, V_{GS} = 0$
current	2SK1166				5		$V_{DS} = 400 V, V_{GS} = 0$
Gate to source cutoff volta	age	V _{GS(off)}	2.0	20	3.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on a	2SK1165	R _{DS(on)}		0.40	0.55	Ω	$I_D = 6 \text{ A}, V_{GS} = 10 \text{ V}^{*3}$
state resistance	2SK1166			0.45	0.60		
Forward transfer admittance		y _{fs}	6.0	10	—	S	$I_D = 6 \text{ A}, V_{DS} = 10 \text{ V}^{*3}$
Input capacitance		Ciss		1450	—	pF	$V_{DS} = 10 V, V_{GS} = 0,$
Output capacitance		Coss	C	410	—	pF	f = 1 MHz
Reverse transfer capacitance		Crss		55	—	pF	
Turn-on delay time		t _{d(on)}		20	—	ns	$I_D = 6 A, V_{GS} = 10 V,$
Rise time 🥖		tr		70	—	ns	$R_L = 5 \Omega$
Turn-off delay time		t _{d(off)}		120	—	ns	
Fall time		t _f		60		ns	
Body to drain diode forward voltage		V_{DF}		1.0	_	V	$I_F = 12 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery t time		t _{rr}		450	_	ns	I _F = 12 A, V _{GS} = 0, di _F /dt = 100 A/μs

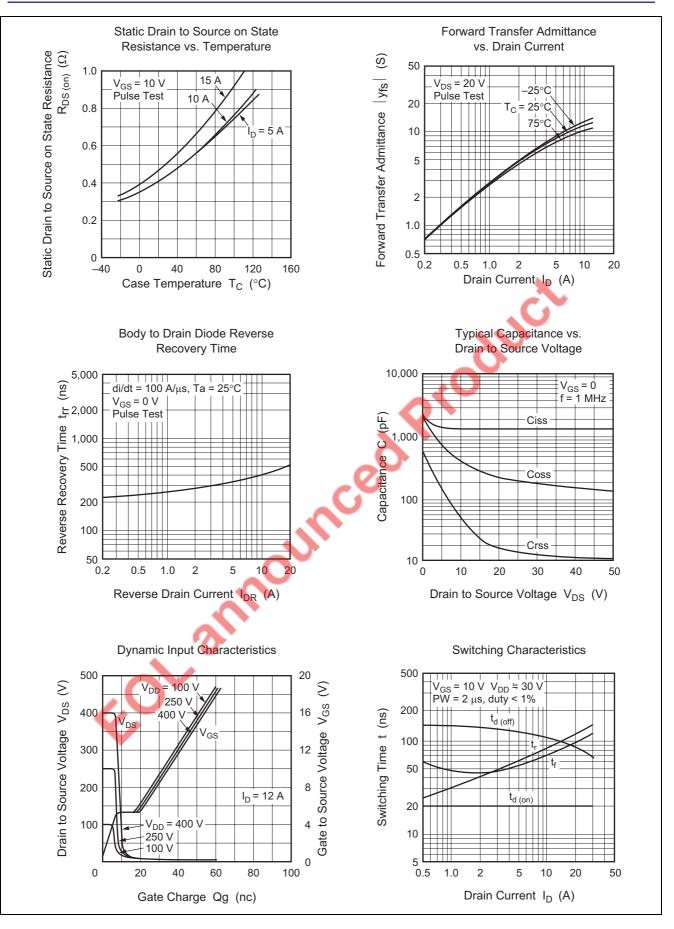
Note: 3. Pulse test



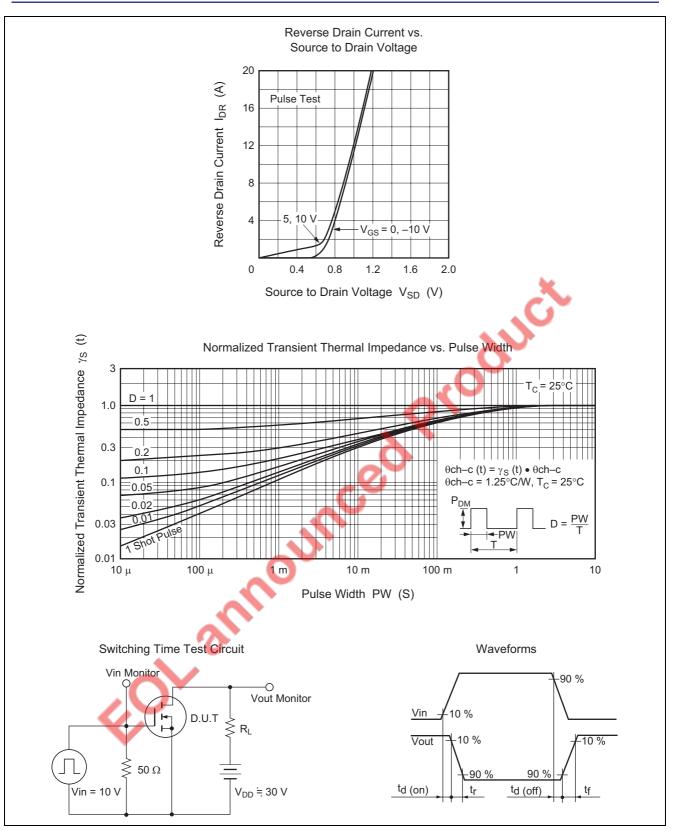
Main Characteristics



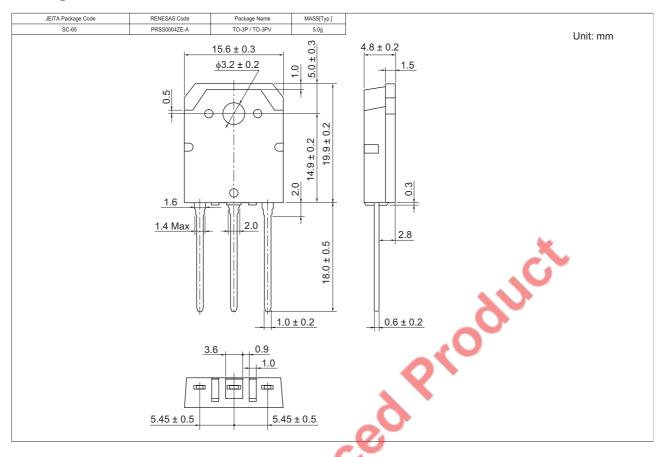








Package Dimensions



Ordering Information

Part Name	Quantity	5	Shipping Container
2SK1165-E	360 pcs 🧹		Box (Tube)
2SK1166-E	360 pcs 🛛 💦 🥂		Box (Tube)

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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