Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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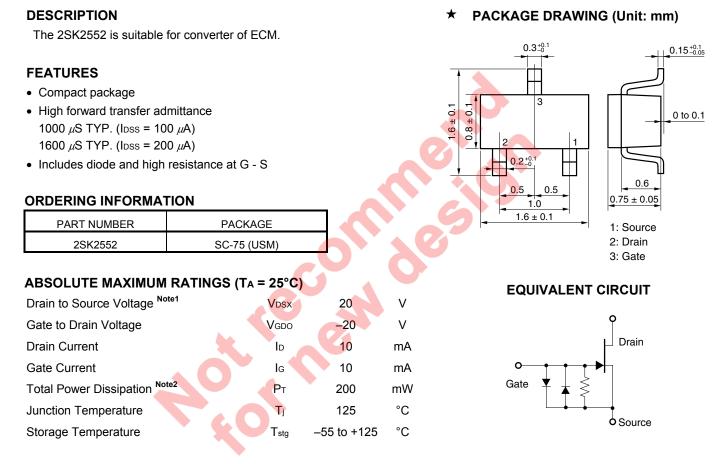
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JUNCTION FIELD EFFECT TRANSISTOR **2SK2552**

N-CHANNEL SILICON JUNCTION FIELD EFFECT TRANSISTOR FOR IMPEDANCE CONVERTER OF ECM



Notes 1. Vgs = -1.0 V

2. Mounted on ceramic substrate of 3.0 cm² x 0.64 mm

Remark Please take care of ESD (Electro Static Discharge) when you handle the device in this document.

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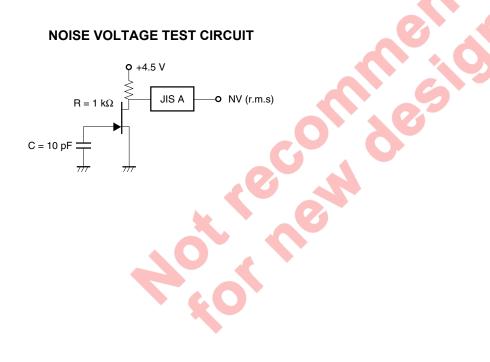
ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Cut-off Current	IDSS	$V_{DS} = 5.0 V, V_{GS} = 0 V$	40		600	μA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 5.0 V, I _D = 1.0 μA	-0.1		-1.0	V
Forward Transfer Admittance	y fs1	V _{DS} = 5.0 V, I _D = 30 μA, f = 1.0 kHz	350			μS
Forward Transfer Admittance	y _{fs2}	V_{DS} = 5.0 V, V_{GS} = 0 V, f = 1.0 kHz	350			μS
Input Capacitance	Ciss	V _{DS} = 5.0 V, V _{GS} = 0 V, f = 1.0 MHz		7.0	8.0	pF
Noise Voltage	NV	See Test Circuit		1.8	3.0	μN

IDSS RANK

MARKING	J2	J3	J4	J5	J6	J7
loss (μA)	40 to 70	60 to 110	90 to 180	150 to 300	200 to 450	300 to 600

NOISE VOLTAGE TEST CIRCUIT



0.01 10 20

200

50

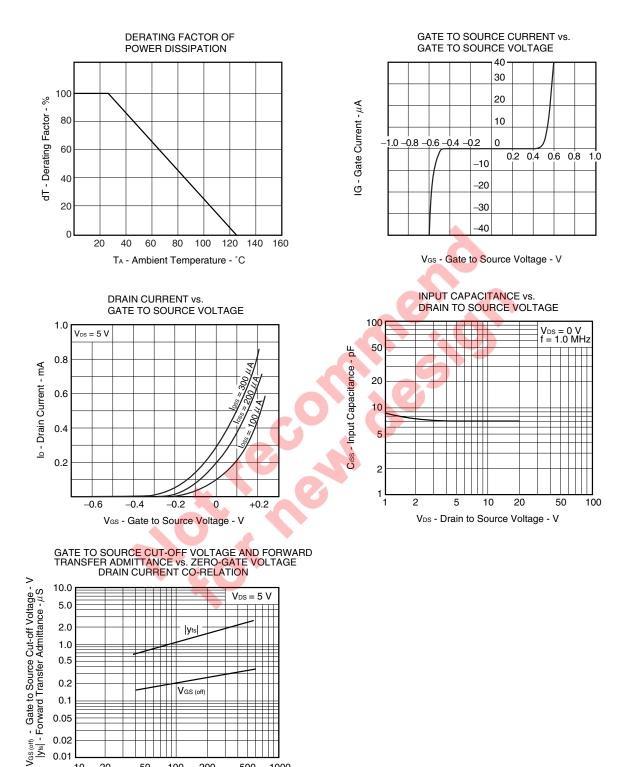
100

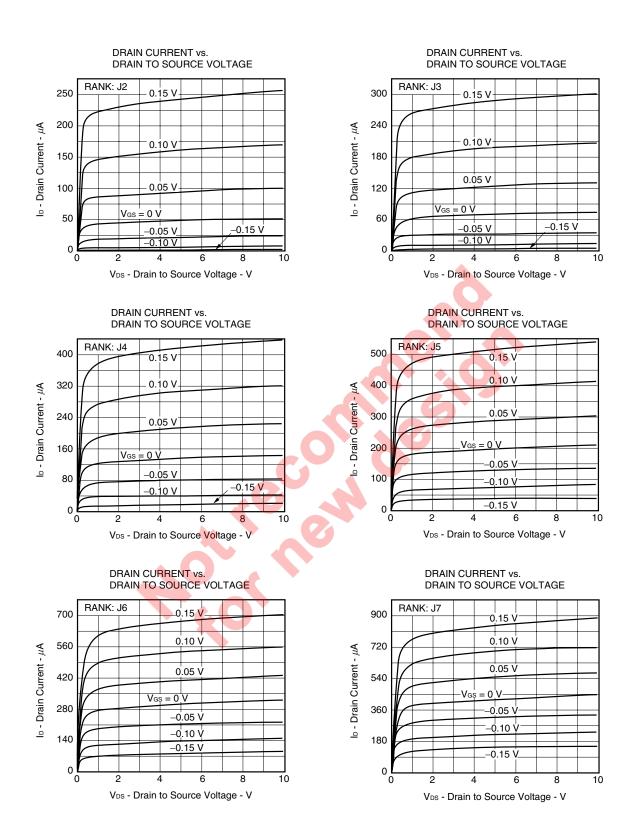
Zero-Gate Voltage Drain Current - µA

1000

500

TYPICAL CHARACTERISTICS (TA = 25°C)





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