Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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MOS FIELD EFFECT TRANSISTOR

2SK2158

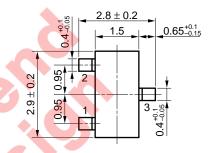
N-CHANNEL MOS FET FOR HIGH-SPEED SWITCHING

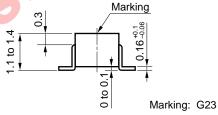
The 2SK2158 is an N-channel vertical type MOS FET featuring an operating voltage as low as 1.5 V. Because it can be driven on a low voltage and it is not necessary to consider driving current, the 2SK2158 is suitable for use in low-voltage portable systems such as headphone stereo sets and camcorders.

FEATURES

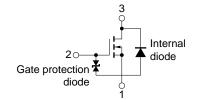
- · Capable of drive gate with 1.5 V
- Because of high input impedance, there is no need to consider driving current.
- Bias resistance can be omitted, enabling reduction in total number of parts.

PACKAGE DIMENSIONS (in millimeters)





EQUIVALENT CIRCUIT



PIN CONNECTION

- 1. Source (S)
- 2. Gate (G)
- 3. Drain (D)

ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

PARAMETER	SYMBOL	TEST CONDITIONS	RATINGS	UNIT
Drain to Source Voltage	VDSS	V _G S = 0	50	V
Gate to Source Voltage	Vgss	V _{DS} = 0	±7.0	V
Drain Current (DC)	I _{D(DC)}		±0.1	А
Drain Current (pulse)	ID(pulse)	PW ≤ 10 ms, Duty Cycle ≤ 50 %	±0.2	А
Total Power Dissipation	P⊤		200	mW
Channel Temperature	Tch		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

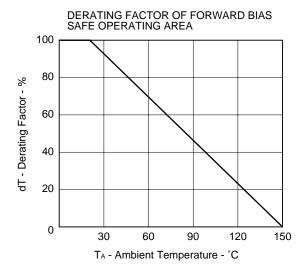


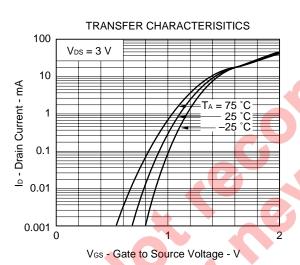
ELECTRICAL CHARACTERISTICS (TA = 25 °C)

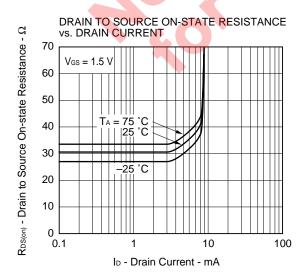
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT			
Drain Cut-off Current	IDSS	V _{DS} = 50 V, V _{GS} = 0			1.0	μΑ			
Gate Leakage Current	Igss	$V_{GS} = \pm 7.0 \text{ V}, V_{DS} = 0$			±3.0	μΑ			
Gate Cut-off Voltage	VGS(off)	$V_{DS} = 3 \text{ V}, \text{ ID} = 1.0 \mu \text{A}$	0.5	0.7	1.1	V			
Forward Transfer Admittance	y _{fs}	V _{DS} = 3 V, I _D = 10 mA	20			mS			
Drain to Source On-state Resistance	RDS(on)1	Vgs = 1.5 V, ID = 1.0 mA		32	50	Ω			
Drain to Source On-state Resistance	RDS(on)2	V _G S = 2.5 V, I _D = 10 mA		16	20	Ω			
Drain to Source On-state Resistance	RDS(on)3	Vgs = 4.0 V, ID = 10 mA		12	15	Ω			
Input Capacitance	Ciss	V _{DS} = 3 V, V _{GS} = 0		6		pF			
Output Capacitance	Coss	f = 1.0 MHz		8		pF			
Reverse Transfer Capacitance	Crss			1		pF			
Turn-On Delay Time	t _{d(on)}	V _{DD} = 3 V, I _D = 20 mA		9		ns			
Rise Time	tr	$V_{GS(on)} = 3 \text{ V}, \text{ Rg} = 10 \Omega$ $R_L = 150 \Omega$		48		ns			
Turn-Off Delay Time	t _{d(off)}			21		ns			
Fall Time	t _f		6. C	31		ns			
	40								

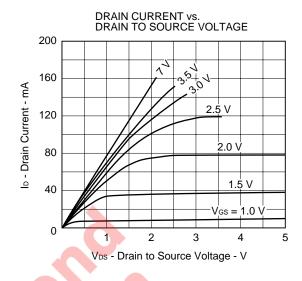


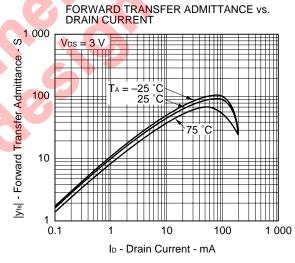
TYPICAL CHARACTERISTICS (TA = 25 °C)

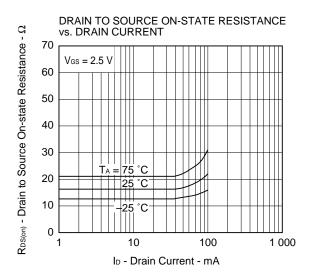




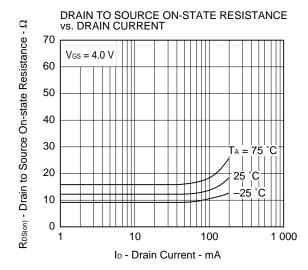


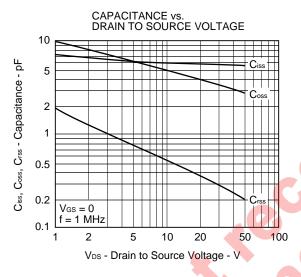


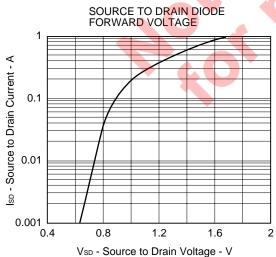


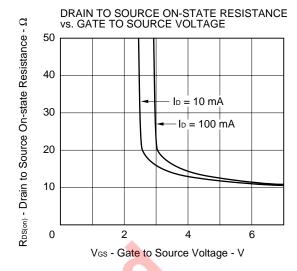


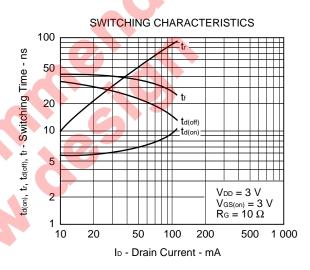














REFERENCE

Document Name	Document No.		
NEC semiconductor device reliability/quality control system	TEI-1202		
Quality grade on NEC semiconductor devices	IEI-1209		
Semiconductor device mounting technology manual	C10535E		
Guide to quality assurance for semiconductor devices	MEI-1202		
Semiconductor selection guide	X10679E		



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Anti-radioactive design is not implemented in this product.

M4 94.11