

June 2013

FJP5555

NPN Silicon Transistor

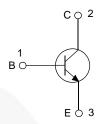
Features

- · Fast Speed Switching
- Wide Safe Operating Area
- High Voltage Capability

Application

- Electronic Ballast
- Switch Mode Power Supplies





Ordering Information

Part Number Marking		Package	Packing Method	
FJP5555TU	J5555	TO-220	Rail	

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Units	
BV _{CBO}	Collector-Base Voltage	1050	V	
BV _{CEO}	Collector-Emitter Voltage	400	V	
BV _{EBO}	Emitter-Base Voltage	14	V	
I _C	Collector Current (DC) 5			
I _{CP}	Collector Current (Pulse)	10	Α	
I _B	Base Current (DC) 2			
I _{BP}	Base Current (Pulse) 4			
TJ	Junction Temperature 150			
T _{STG}	Storage Temperature - 55 to +150			

Thermal Characteristics

Values are at $T_{\Lambda} = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter		Value	Units
P_{D}	Total Device Dissipation	T _A = 25°C	1.38	W
		$T_C = 25^{\circ}C$	75	W
$R_{\theta ja}^{(1)}$	Thermal Resistance, Junction to Ambient		90	°C/W
R _{θic} ⁽²⁾	Thermal Resistance, Junction to Case		1.66	°C/W

1

Notes:

- 1. $R_{\theta ia}$ test board and fixture under natural convection, JESD51-10 recommended thermal test board.
- 2. $R_{\theta ic}$ test fixture under infinite cooling condition.

Electrical Characteristics(3)

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Voltage	$I_C = 500 \mu A, I_E = 0$	1050			V
BV _{CEO}	Collector-Emitter Voltage	$I_C = 5 \text{ mA}, I_B = 0$	400			V
BV _{EBO}	Emitter-Base Voltage	$I_E = 500 \mu A, I_C = 0$	14			V
h _{FE}	DC Current Gain	$V_{CE} = 5 \text{ V}, I_{C} = 10 \text{ mA}$	10			
		$V_{CE} = 3 \text{ V}, I_{C} = 0.8 \text{ A}$	20		40	
\/ (==+\	Collector-Emitter Saturation Voltage	$I_C = 1 \text{ A}, I_B = 0.2 \text{ A}$			0.5	V
V _{CE} (sat)		$I_C = 3.5 \text{ A}, I_B = 1.0 \text{ A}$			1.5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_C = 3.5 \text{ A}, I_B = 1.0 \text{ A}$			1.2	V
C _{ob}	Output Capacitance	V _{CB} = 10 V, f = 1 MHz		45		pF
t _{ON}	Turn-On Time	$V_{CC} = 125 \text{ V}, I_{C} = 0.5 \text{ A},$			1.0	μs
t _{STG}	Storage Time	$I_{B1} = 45 \text{ mA}, I_{B2} = 0.5 \text{ A},$			1.2	μs
t _F	Fall Time	$R_L = 250 \Omega$			0.3	μs
t _{ON}	Turn-On Time	V _{CC} = 250 V, I _C = 2.5 A,			2.0	μs
t _{STG}	Storage Time $I_{B1} = 0.5 \text{ A}, I_{B2} = 1.0 \text{ A},$				2.5	μs
t _F	Fall Time	$R_L = 100 \Omega$			0.3	μs
EAS	Avalanche Energy	L = 2 mH	6	\		mJ

Note:

3. Pulse test: pulse width $\leq 300~\mu s,$ duty cycle $\leq 2\%.$

Typical Performance Characteristics

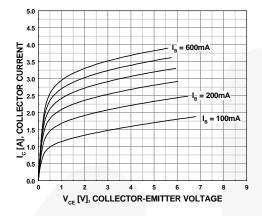


Figure 1. Static Characteristics

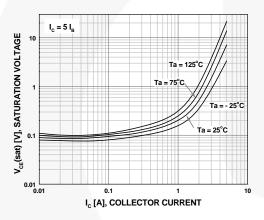


Figure 3. Saturation Voltage

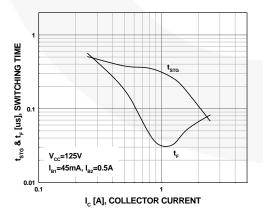


Figure 5. Resistive Load Switching

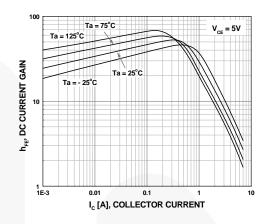


Figure 2. DC Current Gain

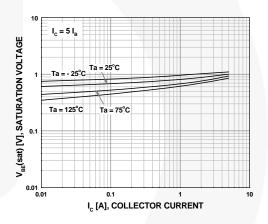


Figure 4. Saturation Voltage

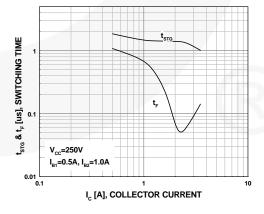


Figure 6. Resistive Load Switching

Typical Performance Characteristics (Continued)

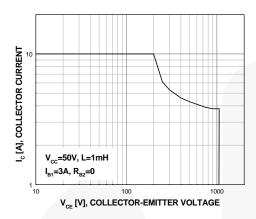


Figure 7. Reverse Biased Safe Operating Area

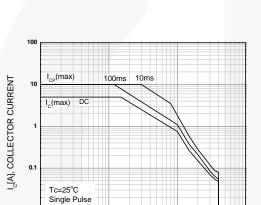


Figure 9. Forward Biased Safe Operating Area

 $V_{_{\mathrm{CF}}}[V]$, COLLECTOR-EMITTER VOLTAGE

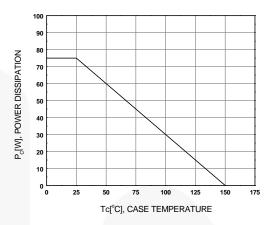
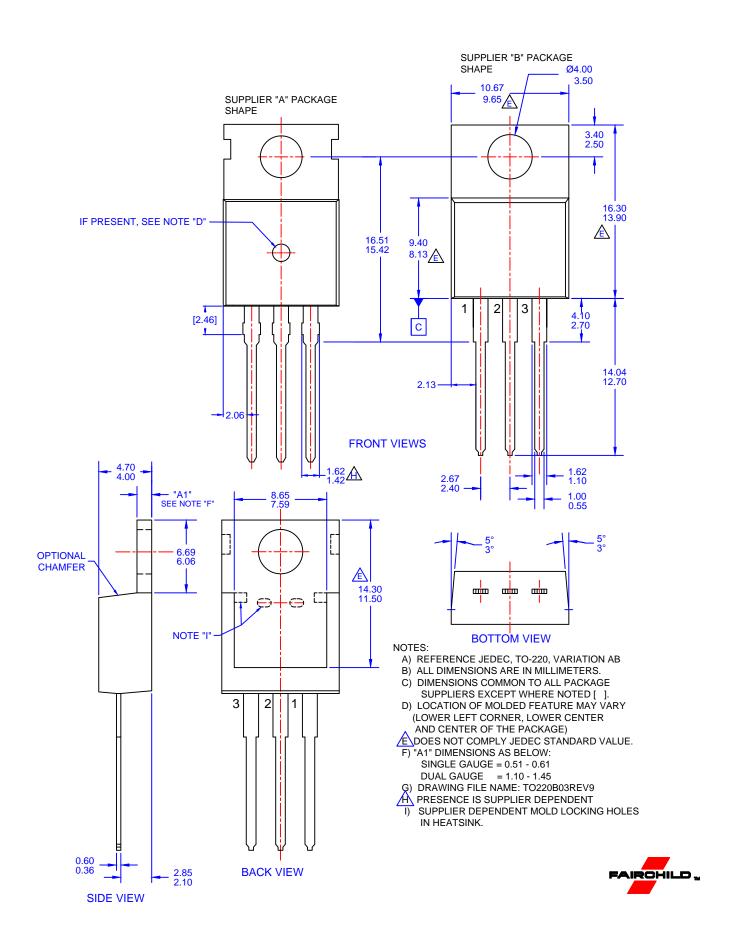


Figure 8. Power Derating







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Definition of Terms			
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