

General Description

The MIC5201 is an efficient linear voltage regulator with very low dropout voltage (typically 17mV at light loads and 200mV at 100mA), and very low ground current (1mA at 100mA output), offering better than 1% initial accuracy with a logic compatible ON/OFF switching input. Designed especially for hand-held battery powered devices, the MIC5201 is switched by a CMOS or TTL compatible logic signal. This ENABLE control may be tied directly to V_{IN} if unneeded. When disabled, power consumption drops nearly to zero. The ground current of the MIC5201 increases only slightly in dropout, further prolonging battery life. Key MIC5201 features include protection against reversed battery, current limiting, and over-temperature shutdown.

The MIC5201 is available in several fixed voltages and accuracy configurations. It features the same pinout as the LT1121 with better performance. Other options are available; contact Micrel for details.

Features

- High output voltage accuracy
- Variety of output voltages
- Guaranteed 200mA output
- Low quiescent current
- Low dropout voltage
- Extremely tight load and line regulation
- Very low temperature coefficient
- Current and thermal limiting
- Reverse-battery protection
- Zero OFF mode current
- Logic-controlled electronic enable
- Available in SO-8 and SOT-223 packages

Applications

- Cellular Telephones
- Laptop, Notebook, and Palmtop Computers
- Battery Powered Equipment
- PCMCIA V_{CC} and V_{PP} Regulation/Switching
- Bar Code Scanners
- SMPS Post-Regulator/ DC to DC Modules
- High Efficiency Linear Power Supplies

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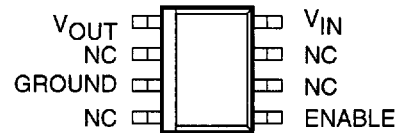
Ordering Information

Part Number	Volts	Accuracy	Temperature Range ^a	Package
MIC5201-3.0BM	3.0	1%	-40°C to +125°C	SO-8
MIC5201-3.3BM	3.3	1%	-40°C to +125°C	SO-8
MIC5201-4.8BM	4.85	1%	-40°C to +125°C	SO-8
MIC5201-5.0BM	5.0	1%	-40°C to +125°C	SO-8
MIC5201-3.0BS	3.0	1%	-40°C to +125°C	SOT-223
MIC5201-3.3BS	3.3	1%	-40°C to +125°C	SOT-223
MIC5201-4.8BS	4.85	1%	-40°C to +125°C	SOT-223
MIC5201-5.0BS	5.0	1%	-40°C to +125°C	SOT-223

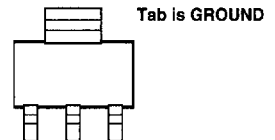
^a Junction Temperature

Other voltages are available; contact Micrel for details.

Pin Configuration

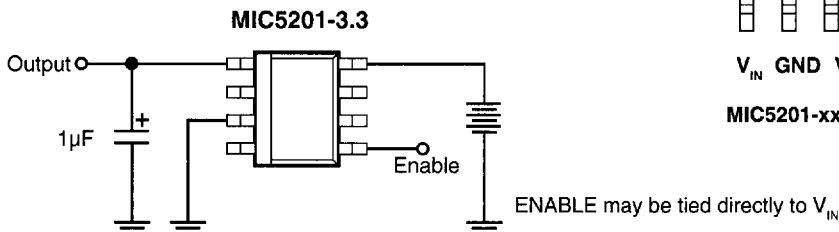


MIC5201-xxBM



MIC5201-xxBS

Typical Application



Absolute Maximum Ratings

Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Electrical specifications do not apply when operating the device beyond its specified **Operating Ratings**.

Power Dissipation	Internally Limited
Lead Temperature (Soldering, 5 seconds)	260°C
Operating Junction Temperature Range	-40°C to +125°C
Input Supply Voltage	-20V to +60V
ENABLE Input Voltage	-20V to +60V
ESD Rating	> 2000V

Recommended Operating Conditions

Input Voltage	2.5V to 26V
Operating Junction Temperature Range	-40°C to +125°C
ENABLE Input Voltage	0V to V_{IN}

Electrical Characteristics

Limits in standard typeface are for $T_J = 25^\circ\text{C}$ and limits in **boldface** apply over the junction temperature range of -40°C to $+125^\circ\text{C}$. Unless otherwise specified, $V_{IN} = V_{OUT} + 1\text{V}$, $I_L = 1\text{mA}$, $C_L = 3.3\mu\text{F}$, and $V_{ENABLE} \geq 2.0\text{V}$

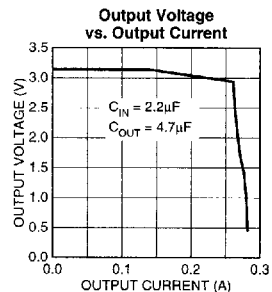
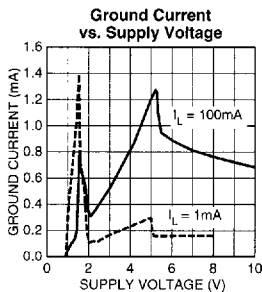
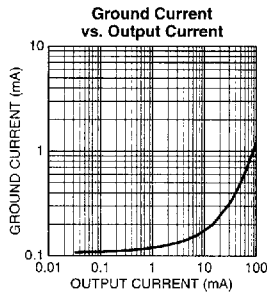
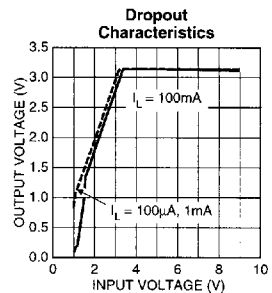
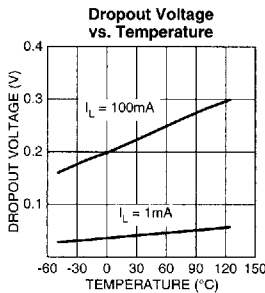
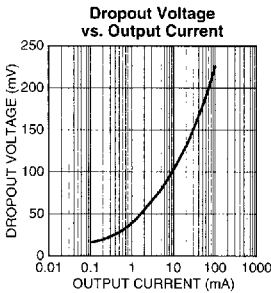
Symbol	Parameter	Conditions	Min	Typical	Max	Units
V_O	Output Voltage Accuracy	Variation from specified V_{OUT}	-1 -2		1 2	%
$\frac{\Delta V_O}{\Delta T}$	Output Voltage Temperature Coef.	(Note 2)		40	150	ppm/°C
$\frac{\Delta V_O}{V_O}$	Line Regulation	$V_{IN} = V_{OUT} + 1\text{V}$ to 26V		0.004	0.10 0.40	%
$\frac{\Delta V_O}{V_O}$	Load Regulation	$I_L = 0.1\text{mA}$ to 200mA (Note 3)		0.04	0.16 0.30	%
$V_{IN} - V_O$	Dropout Voltage (Note 4)	$I_L = 100\mu\text{A}$ $I_L = 20\text{mA}$ $I_L = 50\text{mA}$ $I_L = 100\text{mA}$ $I_L = 200\text{mA}$		17 75 130 160 180 240 225 350 270 400		mV
I_{GND}	Quiescent Current	$V_{ENABLE} \leq 0.7\text{V}$ (Shutdown)		0.01		μA
I_{GND}	Ground Pin Current	$V_{ENABLE} \geq 2.0\text{V}$, $I_L = 100\mu\text{A}$ $I_L = 20\text{mA}$ $I_L = 50\text{mA}$ $I_L = 100\text{mA}$ $I_L = 200\text{mA}$		170 270 500 1200 3.0		μA mA
PSRR	Ripple Rejection			75		dB
I_{GNDDO}	Ground Pin Current at Dropout	$V_{IN} = 0.5\text{V}$ less than specified V_{OUT} $I_L = 100\mu\text{A}$ (Note 5)		270	330	μA
I_{LIMIT}	Current Limit	$V_{OUT} = 0\text{V}$		280		mA
$\frac{\Delta V_O}{\Delta P_D}$	Thermal Regulation	(Note 6)		0.05		%/W
e_n	Output Noise			100		μV

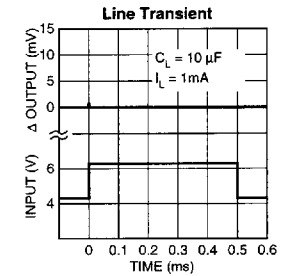
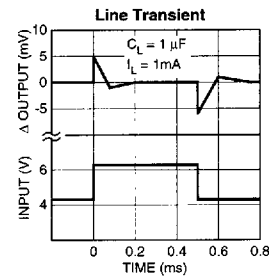
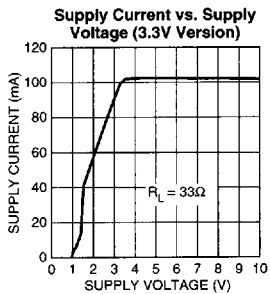
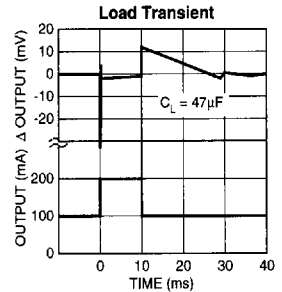
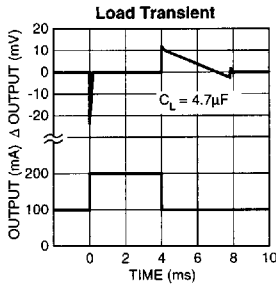
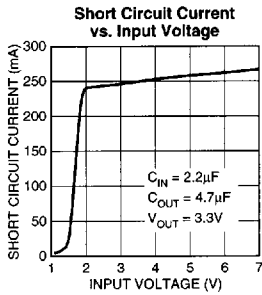
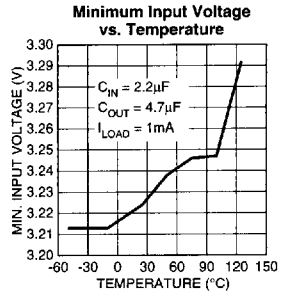
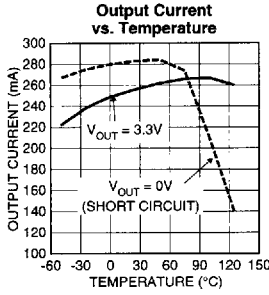
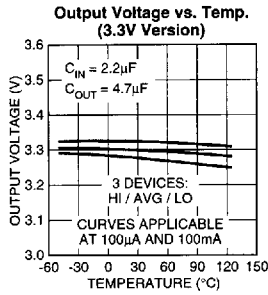
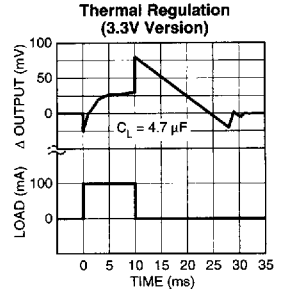
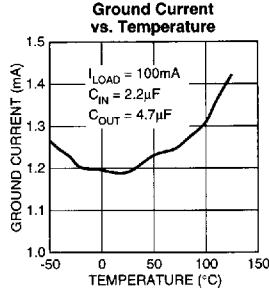
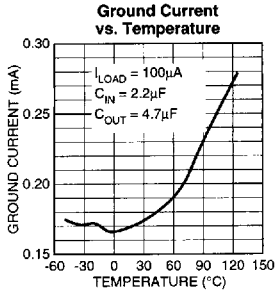
ENABLE Input

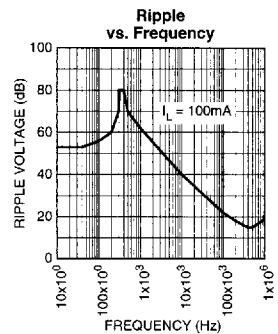
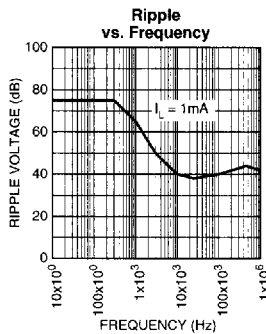
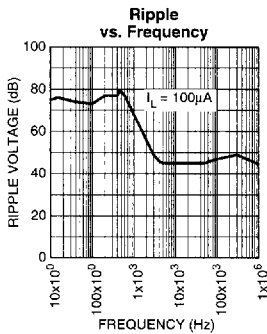
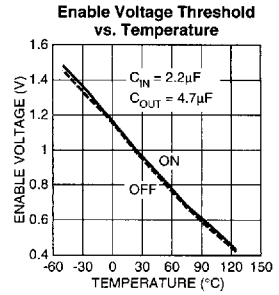
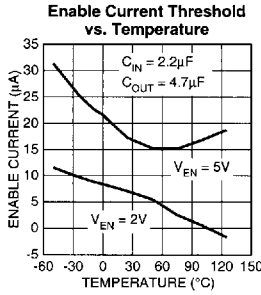
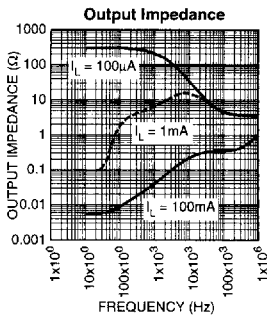
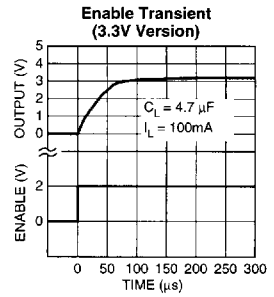
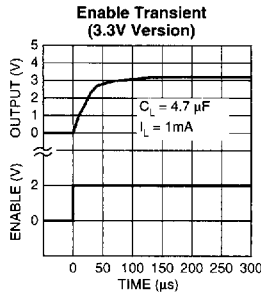
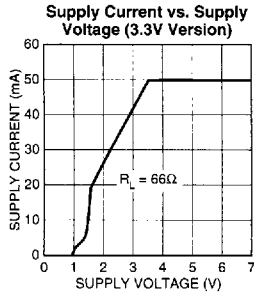
V_{IL}	Input Voltage Level Logic Low Logic High	OFF ON	2.0		0.7	V
I_{IL} I_{IH}	ENABLE Input Current	$V_{IL} \leq 0.7\text{V}$ $V_{IH} \geq 2.0\text{V}$		0.01 8	50	μA

- Note 1:** Absolute maximum ratings indicate limits beyond which damage to the component may occur. Electrical specifications do not apply when operating the device outside of its rated operating conditions. The maximum allowable power dissipation is a function of the maximum junction temperature, $T_{J(MAX)}$; the junction-to-ambient thermal resistance, θ_{JA} , and the ambient temperature, T_A . The maximum allowable power dissipation at any ambient temperature is calculated using: $P_{(MAX)} = (T_{J(MAX)} - T_A) \theta_{JA}$. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown. The θ_{JC} of the MIC5201-xxBS is 15°C/W and θ_{JA} for the MIC5201BM is 160°C/W mounted on a PC board (see "Thermal Considerations" section for further details).
- Note 2:** Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- Note 3:** Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 200mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- Note 4:** Dropout Voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at 1V differential.
- Note 5:** Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.
- Note 6:** Thermal regulation is defined as the change in output voltage at a time T after a change in power dissipation is applied, excluding load or line regulation effects. Specifications are for a 200mA load pulse at $V_{IN} = 26V$ for $T = 10ms$.

Typical Characteristics







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Applications Information

External Capacitors

A 1 μ F capacitor is recommended between the MIC5201 output and ground to prevent oscillations due to instability. Larger values serve to improve the regulator's transient response. Most types of tantalum or aluminum electrolytics will be adequate; film types will work, but are costly and therefore not recommended. Many aluminum electrolytics have electrolytes that freeze at about -30°C , so solid tantalums are recommended for operation below -25°C . The important parameters of the capacitor are an effective series resistance of about 5Ω or less and a resonant frequency above 500kHz. The value of this capacitor may be increased without limit.

At lower values of output current, less output capacitance is required for output stability. The capacitor can be reduced to 0.47 μ F for current below 10mA or 0.33 μ F for currents below 1 mA. A 1 μ F capacitor should be placed from the MIC5201 input to ground if there is more than 10 inches of wire between the input and the AC filter capacitor or if a battery is used as the input.

The MIC5201 will remain stable and in regulation with no load in addition to the internal voltage divider, unlike many other voltage regulators. This is especially important in CMOS RAM keep-alive applications.

When used in dual supply systems where the regulator load is returned to a negative supply, the output voltage must be diode clamped to ground.

Thermal Considerations

Part I. Layout

The MIC5201-xxBM (8-pin surface mount package) has the following thermal characteristics when mounted on a single layer copper-clad printed circuit board.

PC Board Dielectric	θ_{JA}
FR4	160 $^{\circ}\text{C}/\text{W}$
Ceramic	120 $^{\circ}\text{C}/\text{W}$

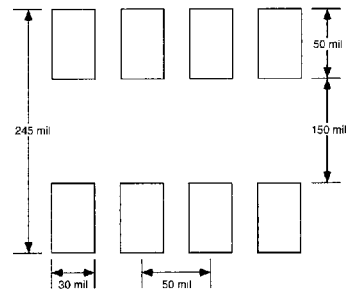
Multi-layer boards having a ground plane, wide traces near the pads, and large supply bus lines provide better thermal conductivity.

The "worst case" value of 160 $^{\circ}\text{C}/\text{W}$ assumes no ground plane, minimum trace widths, and a FR4 material board.

Part II. Nominal Power Dissipation and Die Temperature

The MIC5201-xxBM at a 25 $^{\circ}\text{C}$ ambient temperature will operate reliably at up to 625mW power dissipation when mounted in the "worst case" manner described above. At an ambient temperature of 55 $^{\circ}\text{C}$, the device may safely dissipate 440mW. These power levels are equivalent to a die temperature of 125 $^{\circ}\text{C}$, the recommended maximum temperature for non-military grade silicon integrated circuits.

For MIC5201-xxBS (SOT-223 package) heat sink characteristics, please refer to Micrel Application Hint 17, "P.C. Board Heat Sinking".



Minimum recommended board pad size, SO-8.