

KSD363

B/W TV Horizontal Deflection Output

Collector-Base Voltage : V_{CBO}=300V
 Collector Current : I_C=6A

• Collector Dissipation : P_C=40W(T_C=25°C)



1.Base 2.Collector 3.Emitter

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	300	V
V _{CEO}	Collector-Emitter Voltage	120	V
V _{EBO}	Emitter-Base Voltage	8	V
I _C	Collector Current	6	Α
P _C	Collector Dissipation (T _C =25°C)	40	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

Electrical Characteristics $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = 1 \text{mA}, I_E = 0$	300			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 20 \text{mA}, I_B = 0$	120			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 1 \text{mA}, I_C = 0$	8			V
I _{CBO}	Collector Cut-off Current	$V_{CB} = 250V, I_{E} = 0$			1	mA
h _{FE}	DC Current Gain	$V_{CE} = 5V, I_{C} = 1A$	40		240	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 1A, I_B = 0.1A$			1	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_C = 1A, I_B = 0.1A$			1.5	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 5V, I_{C} = 0.5A$		10		MHz

h_{FE} Classification

Classification	R	0	Y	
h _{FF}	40 ~ 80	70 ~ 140	120 ~ 240	

Typical Characteristics

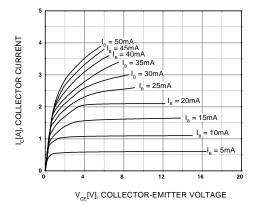


Figure 1. Static Characteristic

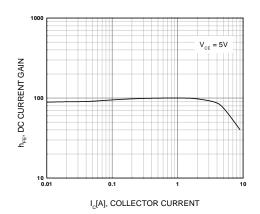


Figure 2. DC current Gain

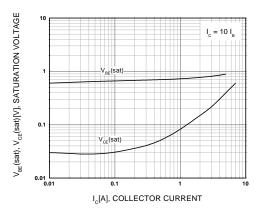


Figure 3. Base-Emitter Saturation Voltage Collect-Emitter Saturation Voltage

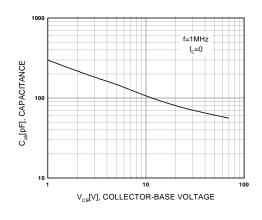


Figure 4. Collector Output Capacitance

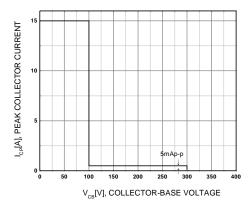


Figure 5. Safe Operating (On Horizonal Deflection Output Circuit)

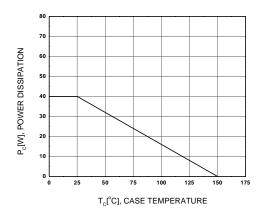
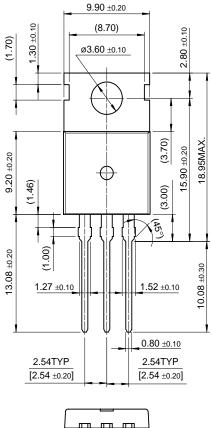


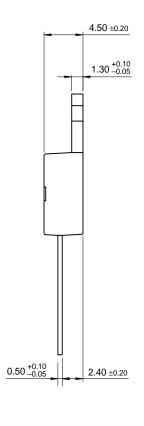
Figure 6. Power Derating

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Package Demensions

TO-220





10.00 ±0.20

Dimensions in Millimeters

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PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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Go

Home >> Find products >>

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Contents

Features

Qualification Support

- Applications
- Product status/pricing/packaging
- Order Samples

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• Collector-Base Voltage: V_{CBO}=300V

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back to top

Applications

B/W TV Horizontal Deflection Output

back to top

Product status/pricing/packaging

BUY

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This page Print version

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Request samples

How to order products

Product Change Notices (PCNs)

Support

Sales support

Quality and reliability

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Product	Product status	Pb-free Status	Pricing*	Package type	Leads	Packing method	Package Marking Convention**
KSD363O	Full Production	Full Production	\$0.434	<u>TO-220</u>	3		<u>Line 1:</u> \$Y (Fairchild logo) <u>Line 2:</u> &3 <u>Line 3:</u> D363-O
KSD363OTU	Full Production	Full Production	\$0.434	<u>TO-220</u>	3	BULK	<u>Line 1:</u> \$Y (Fairchild logo) <u>Line 2:</u> &3 <u>Line 3:</u> D363-O

KSD363R	Full Production	Full Production	\$0.434	<u>TO-220</u>	3	<u>Line 1:</u> \$Y (Fairchild logo) <u>Line 2:</u> &3 <u>Line 3:</u> D363-R
KSD363RTU	Full Production	Full Production	\$0.434	TO-220	3	<u>Line 1:</u> \$Y (Fairchild logo) <u>Line 2:</u> &3 <u>Line 3:</u> D363-R
KSD363Y	Full Production	Full Production	\$0.434	<u>TO-220</u>	3	Line 1: \$Y (Fairchild logo) Line 2: &3 Line 3: D363-Y
KSD363YTU	Full Production	Full Production	\$0.434	TO-220	3	Line 1: \$Y (Fairchild logo) Line 2: &3 Line 3: D363-Y

^{*} Fairchild 1,000 piece Budgetary Pricing

** A sample button will appear if the part is available through Fairchild's on-line samples program. If there is no sample button, please contact a Fairchild distributor to obtain samples



Indicates product with Pb-free second-level interconnect. For more information click here.

Package marking information for product KSD363 is available. Click here for more information .

back to top

Qualification Support

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Product
KSD363O
KSD363OTU
KSD363R
KSD363RTU
KSD363Y
KSD363YTU

back to top



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