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Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# BIPOLAR ANALOG INTEGRATED CIRCUIT $\mu PC311$

# PRECISION VOLTAGE COMPARATOR

# DESCRIPTION

The  $\mu$ PC311 is a voltage comparator that has input currents more than a hundred times lower than devices like conventional standard type of 710. It is also designed to operate over a wide range of supply voltages; from ±15 V op amp supplies down to the single 5 V supply used for IC logic. Its output is compatible with HNIL, DTL and TTL as well as MOS circuits.

#### FEATURES

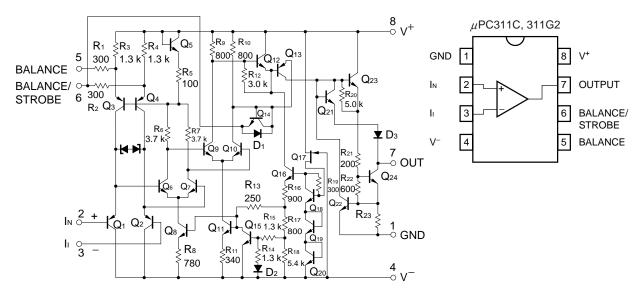
- Operate from single 5 V supply
- Maximum input current: 250 nA
- Maximum offset current: 50 nA
- Fast transient response: 200 ns TYP.

# **ORDERING INFORMATION**

| Part Number Package |                                   |
|---------------------|-----------------------------------|
| μPC311C             | 8-pin plastic DIP (7.62 mm (300)) |
| μPC311G2            | 8-pin plastic SOP (5.72 mm (225)) |

# **EQUIVALENT CIRCUIT (1/2 Circuit)**

# **PIN CONFIGURATION (Top View)**



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# ABSOLUTE MAXIMUM RATINGS (TA = $25^{\circ}$ C)

| Pa                            | rameter                         | Symbol  | Ratings                                    | Unit |
|-------------------------------|---------------------------------|---|--|------|
| Voltage between $V^{+}$ a     | and $V^{-Note 1}$               | $V^{\scriptscriptstyle +}-V^{\scriptscriptstyle -}$ | -0.3 to +36                                | V    |
| Differential Input Volt       | age                             | VID   | ±30  | V    |
| Input Voltage Note 2          |                                 | Vı  | V <sup>-</sup> -0.3 to V <sup>+</sup> +0.3 | V    |
| Output to Negative S          | upply Voltage <sup>Note 3</sup> | $V_O - V^-$   | –0.3 to +40                                | V    |
| Ground to Negative S          | Supply Voltage                  | $V_{\text{GND}} - V^{\scriptscriptstyle -}$         | -0.3 to +30                                | V    |
| Power Dissipation             | C Package Note 4                | Рт  | 350  | mW   |
|                               | G2 Package Note 5               |   | 440  | mW   |
| Output Short Circuit          | Duration Note 6                 |   | 10   | sec  |
| Operating Ambient Temperature |                                 | Та  | –20 to +80                                 | °C   |
| Storage Temperature           |                                 | Tstg  | -55 to +125                                | °C   |

Notes 1. Reverse connection of supply voltage can cause destruction.

- 2. The input voltage should be allowed to input without damage or destruction. Even during the transition period of supply voltage, power on/off etc., this specification should be kept. The normal operation will establish when the both inputs are within the Common Mode Input Voltage Range of electrical characteristics.
- **3.** This specification is the voltage which should be allowed to supply to the output and GND terminal from external without damage or destruction. Even during the transition period of supply voltage, power on/off etc., this specification should be kept.
- 4. Thermal derating factor is -5.0 mW/°C when operating ambient temperature is higher than 55°C.
- 5. Thermal derating factor is -4.4 mW/°C when operating ambient temperature is higher than 25°C.
- **6.** Pay careful attention to the total power dissipation not to exceed the absolute maximum ratings, Note 4 and Note 5.

# **RECOMMENDED OPERATING CONDITIONS**

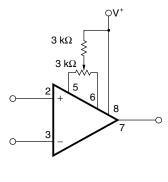
| Parameter                             | Symbol    | MIN. | TYP. | MAX. | Unit |
|---------------------------------------|-----------|------|------|------|------|
| Supply Voltage (Split)                | $V^{\pm}$ | ±4   |      | ±16  | V    |
| Supply Voltage (V <sup>-</sup> = GND) | V         | +5   |      | +32  | V    |

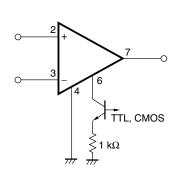
# **TYPICAL CONNECTIONS**

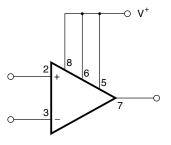
OFFSET VOLTAGE NULL CIRCUIT

STROBING CIRCUIT

FAST RESPONSE CIRCUIT (INCREASING INPUT STAGE CURRENT)



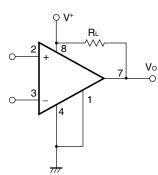


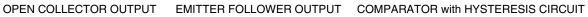


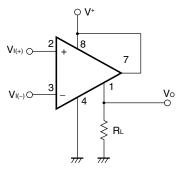
| Parameter                 | Symbol         | Conditions   | MIN.  | TYP.    | MAX. | Unit |
|---------------------------|----------------|--|-------|---------|------|------|
| Input Offset Voltage      | Vio            | $V^{\scriptscriptstyle +} - V^{\scriptscriptstyle -}$ = 5 to 30 V, $Rs \leq$ 50 $k\Omega$                          |       | ±2.0    | ±7.5 | mV   |
| Input Offset Current      | lio            | $V^{+} - V^{-} = 5 \text{ to } 30 \text{ V}$   |       | ±6.0    | ±50  | nA   |
| Input Bias Current        | Ів             | $V^{+} - V^{-} = 5$ to 30 V  |       | 100     | 250  | nA   |
| Voltage Gain              | Av             | RL = 1.0 kΩ  |       | 200,000 |      |      |
| Response Time             |                | Input step 100 mV, Overdrive 5 mV  |       | 200     |      | ns   |
| Output Saturation Voltage | Vol            | $V_{I} \leq 10 \text{ mV}, \text{ Io} = 50 \text{ mA}$   |       | 0.75    | 1.5  | V    |
| Strobe ON Current         |                |  |       | 3.0     |      | mA   |
| Output Leakage Current    | Ioleak         | $V_{\rm I} \geq 10 \ mV, \ V_{\rm O} = 35 \ V$   |       | 0.2     | 50   | nA   |
| Positive Supply Current   | l <sup>+</sup> | lo = 0 A   |       | 5.1     | 7.5  | mA   |
| Negative Supply Current   | l-             | lo = 0 A   |       | 4.1     | 5.0  | mA   |
| Input Offset Voltage      | Vio            | $V^{\scriptscriptstyle +}-V^{\scriptscriptstyle -}=5 \text{ to } 30 \text{ V}, \text{Rs} \leq 50 \text{ k}\Omega,$ |       |         | ±10  | mV   |
|                           |                | $T_A = 0$ to $70^{\circ}C$   |       |         |      |      |
| Input Offset Current      | lio            | $V^{+} - V^{-} = 5$ to 30 V, T <sub>A</sub> = 0 to 70°C  |       |         | ±70  | nA   |
| Input Bias Current        | Ів             | $V^{+} - V^{-} = 5$ to 30 V, T <sub>A</sub> = 0 to 70°C  |       |         | 300  | nA   |
| Common Mode Input Voltage | VICM           |  | ±13.0 | ±13.8   |      | V    |
| Range                     |                |  | -14.5 | -14.7   |      |      |
| Output Saturation Voltage | Vol            | $V^{\scriptscriptstyle +} \geq 4.5 \ V, \ V^{\scriptscriptstyle -} = 0 \ V, V_{\rm I} \leq -10 \ mV,$              |       | 0.23    | 0.4  | V    |
|                           |                | lo = 8 mA  |       |         |      |      |

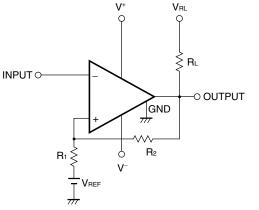
# ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = $25^{\circ}$ C, V<sup>±</sup> = $\pm 15$ V)

# **TYPICAL APPLICATION CIRCUIT**









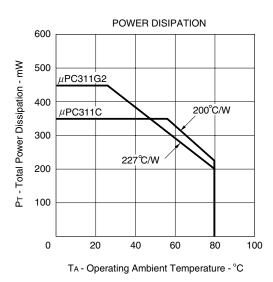
Input polarity is reversed when 1pin (GND) is used as an output  $V_N > V_I \to V_O:Low$ 

Threshold Voltage

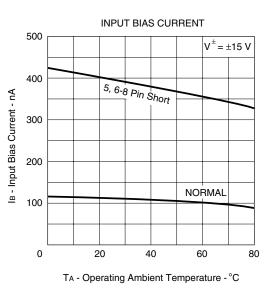
$$V_{TH (High)} = V_{REF} + \frac{R_1}{R_L + R_2 + R_1} (V_{RL} - V_{REF})$$

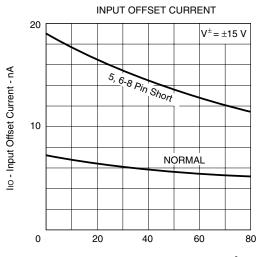
$$V_{\text{TH (Low)}} \stackrel{:}{=} V_{\text{REF}} - \frac{R_1}{R_1 + R_2} (V_{\text{REF}} - V_{\text{OL}})$$

# TYPICAL PERFORMANCE CHARACTERISTICS (TA = 25°C, TYP.)

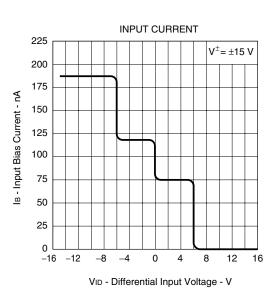


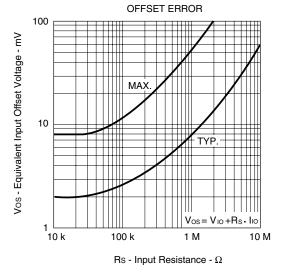
NEC

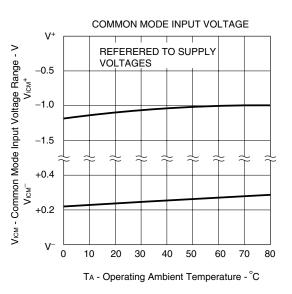




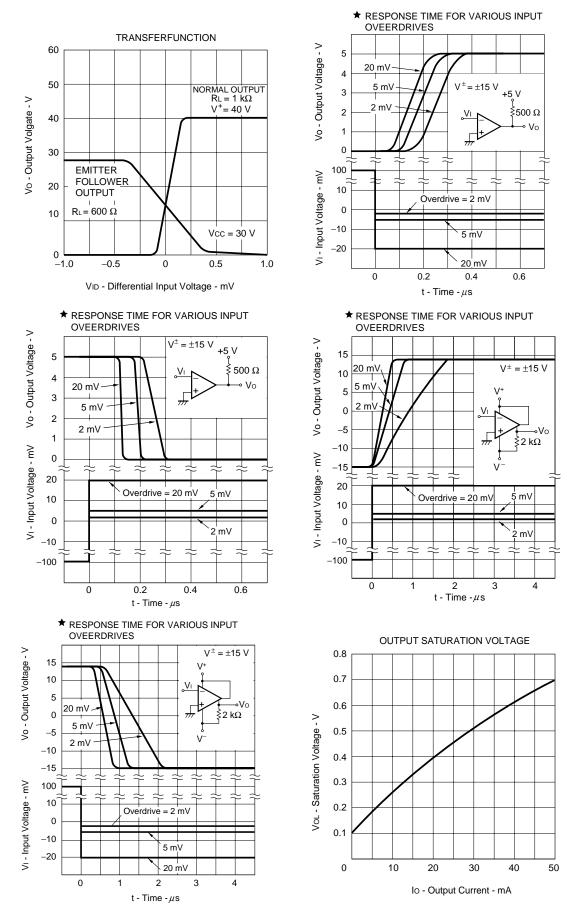




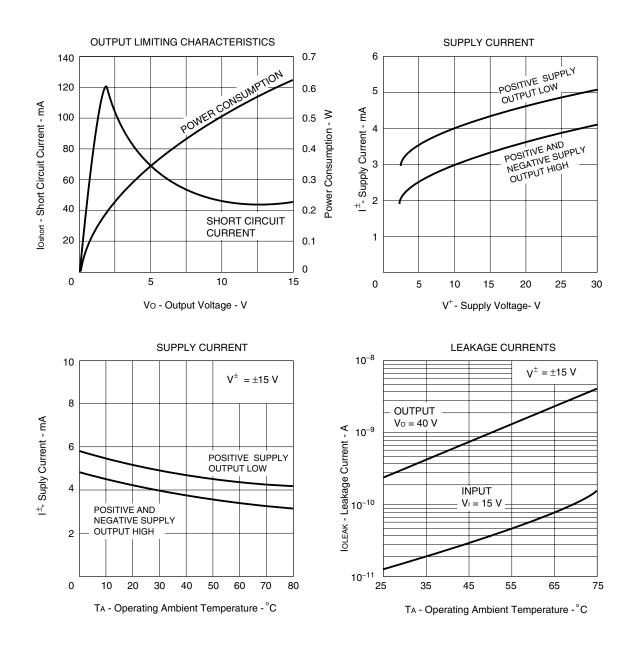




Data Sheet G15945EJ4V0DS

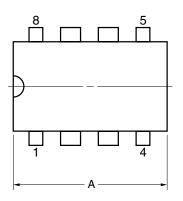


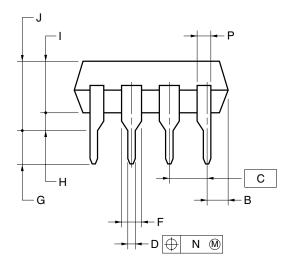
Data Sheet G15945EJ4V0DS

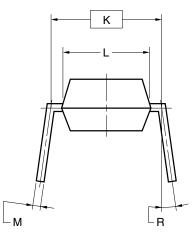


# PACKAGE DRAWINGS (Unit : mm)

# 8-PIN PLASTIC DIP (7.62mm(300))





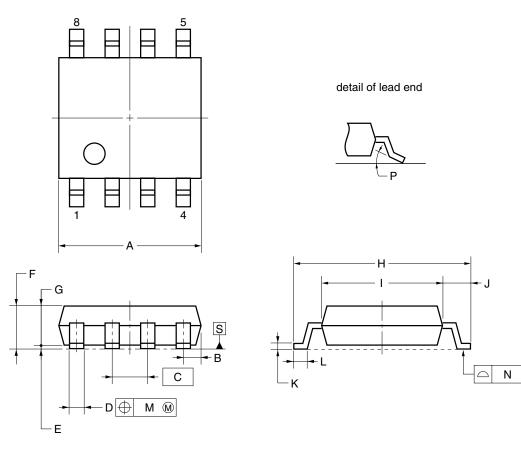


# NOTES

- 1. Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.
- 2. Item "K" to center of leads when formed parallel.

| ITEM | MILLIMETERS                     |
|------|---------------------------------|
| Α    | 10.16 MAX.                      |
| В    | 1.27 MAX.                       |
| С    | 2.54 (T.P.)                     |
| D    | 0.50±0.10                       |
| F    | 1.4 MIN.                        |
| G    | 3.2±0.3                         |
| Н    | 0.51 MIN.                       |
| I    | 4.31 MAX.                       |
| J    | 5.08 MAX.                       |
| К    | 7.62 (T.P.)                     |
| L    | 6.4                             |
| М    | $0.25\substack{+0.10 \\ -0.05}$ |
| N    | 0.25                            |
| Р    | 0.9 MIN.                        |
| R    | 0~15°                           |
|      | P8C-100-300B,C-2                |

# 8-PIN PLASTIC SOP (5.72 mm (225))



# NOTE

Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

| ITEM | MILLIMETERS                                       |
|------|---|
| А    | $5.2 \begin{array}{c} +0.17 \\ -0.20 \end{array}$ |
| В    | 0.78 MAX.   |
| С    | 1.27 (T.P.)                                       |
| D    | $0.42\substack{+0.08\\-0.07}$                     |
| E    | 0.1±0.1   |
| F    | 1.59±0.21   |
| G    | 1.49  |
| Н    | 6.5±0.3   |
| I    | 4.4±0.15  |
| J    | 1.1±0.2   |
| к    | 0.17 <sup>+0.08</sup> 0.07                        |
| L    | 0.6±0.2   |
| М    | 0.12  |
| Ν    | 0.10  |
| Р    | 3° <sup>+7°</sup> 3°                              |
|      | S8GM-50-225B-6                                    |

S

# \* RECOMMENDED SOLDERING CONDITIONS

The  $\mu$ PC311 should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, contact an NEC Electronics sales representative.

For technical information, see the following website.

## Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

#### Type of Surface Mount Device

## µPC311G2: 8-pin plastic SOP (5.72 mm (225))

| Process                | Conditions   | Symbol    |
|------------------------|--|-----------|
| Infrared Ray Reflow    | Peak temperature: 230°C or below (Package surface temperature),        | IR30-00-1 |
|                        | Reflow time: 30 seconds or less (at 210°C or higher),                  |           |
|                        | Maximum number of reflow processes: 1 time.                            |           |
| Vapor Phase Soldering  | Peak temperature: 215°C or below (Package surface temperature),        | VP15-00-1 |
|                        | Reflow time: 40 seconds or less (at 200°C or higher),                  |           |
|                        | Maximum number of reflow processes: 1 time.                            |           |
| Wave Soldering         | Solder temperature: 260°C or below, Flow time: 10 seconds or less,     | WS60-00-1 |
|                        | Maximum number of flow processes: 1 time,                              |           |
|                        | Pre-heating temperature: 120°C or below (Package surface temperature). |           |
| Partial Heating Method | Pin temperature: 300°C or below,                                       | _         |
|                        | Heat time: 3 seconds or less (Per each side of the device).            |           |

# Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

#### Type of Through-hole Device

#### µPC311C: 8-pin plastic DIP (7.62 mm (300))

| Process                | Conditions                                    |  |
|------------------------|---|--|
| Wave Soldering         | Solder temperature: 260°C or below,           |  |
| (only to leads)        | Flow time: 10 seconds or less.                |  |
| Partial Heating Method | Pin temperature: 300°C or below,              |  |
|                        | Heat time: 3 seconds or less (per each lead). |  |

Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

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