- 8-Bit Resolution
- Linearity . . . ± 1/2 LSB Maximum
- Differential Nonlinearity . . . ±1/2 LSB Maximum
- Conversion Rate . . . 60 MHz Min
- Nominal Output Signal Operating Range V<sub>CC</sub> to V<sub>CC</sub> – 1 V
- TTL Digital Input Voltage
- 5-V Single Power Supply Operation
- Low Power Consumption . . . 350 mW Typ

#### description

0°C to 70°C.

The TL5632C is a low-power ultra-high-speed video digital-to-analog converter that uses the Advanced Low-Power Schottky (ALS) process. The device has a three channel I/O; the red, the blue, and the green channel. The red, blue, and green signals are referred to collectively as the RGB signal. An internally generated reference is also provided for the standard video output voltage range. Conversion of digital signals to analog signals can be at a sampling rate of dc to 60 MHz. The high conversion rate makes the TL5632C suitable for digital television, computer digital video processing, and high-speed data conversion.

The TL5632C is characterized for operation from



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NC - No internal connection

FUNCTION TABLE							
STEP	DIGITAL INPUT	OUTPUT VOLTAGE					
0	LLLLLLL	3.980 V					
1	LLLLLLH	3.984 V					
•	•	•					
•	•	•					
•	•	•					
127	LHHHHHHH	4.488 V					
128	HLLLLLL	4.492 V					
129	HLLLLLH	4.996 V					
•	•	•					
•	•	•					
•	•	•					
254	ННННННL	4.996 V					
255	ннннннн	5.000 V					

#### **AVAILABLE OPTIONS**

Τ <sub>Α</sub>	PACKAGE	
0°C to 70°C	TL5632CFR	

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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### functional block diagram



#### schematics of outputs





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#### **Terminal Functions**

TERMINAL			DESCRIPTION				
NAME	NO.	1/0	DESCRIPTION				
B <sub>1</sub> – B <sub>8</sub>	18 – 25	Ι	B-channel digital input (B <sub>1</sub> = MSB)				
BOUT	36	0	B-channel analog output				
CCOMP	31		Phase compensation capacitance. A 1 $\mu$ F capacitor is connected from C <sub>COMP</sub> to GND.				
CLK <sub>B</sub> IN	26	I	B-channel clock input				
CLK <sub>G</sub> IN	27	I	G-channel clock input				
CLK <sub>R</sub> IN	28	Ι	R-channel clock input				
G <sub>1</sub> – G <sub>8</sub>	9 – 16	Ι	G-Channel digital input (G1= MSB)				
GND	29, 35, 37, 39, 41		Ground. All GND terminals are connected internally; however, all GND terminals should be connected externally to a ground plane or equivalent low impedance ground return.				
G <sub>OUT</sub>	38	0	G-channel analog output				
NC	17, 44		No connection internally				
R <sub>1</sub> – R <sub>8</sub>	1 – 8	Ι	R-channel digital input (R <sub>1</sub> = MSB)				
ROUT	40	0	R-channel analog output				
AVCC	32, 42		Analog power supply voltage				
DVCC	30, 43		Digital power supply voltage				
REF IN	34	I	Reference voltage input. REF IN accepts the reference voltage on REF OUT. An external reference can also be applied consistent with Note 1.				
REF OUT	33	0	Reference voltage output. An internal voltage divider generates the voltage level (see schematics of outputs, page 2).				

NOTE 1:  $V_{CC} - V_{ref} \le 1.2 \text{ V}$ 

#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

Power supply voltage range, AV <sub>CC</sub> , DV <sub>CC</sub> (see Note 2)	$\ldots \ldots \ldots \ldots -0.3$ V to 7 V
Digital input voltage range, V <sub>1</sub>	$\dots \dots \dots -0.3$ V to DV <sub>CC</sub>
Analog output voltage range, ROUT, GOUT, BOUT, CCOMP (externally applied)	$\dots -0.3 \text{ V} \text{ to } \text{AV}_{\text{CC}} + 0.3 \text{ V}$
Reference input range, REF IN	$\dots$ -0.3 V to AV <sub>CC</sub> + 0.3 V
Reference output range, REF OUT	$\dots$ -0.3 V to AV <sub>CC</sub> + 0.3 V
Operating free-air temperature range, T <sub>A</sub>	0°C to 70°C
Storage temperature range	65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.





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#### recommended operating conditions

	MIN	NOM	MAX	UNIT
Supply voltage, AV <sub>CC</sub> , DV <sub>CC</sub>	4.75	5	5.25	V
High-level input voltage, VIH	2			V
Low-level input voltage, VIL			0.8	V
Reference voltage, V <sub>ref</sub> (see Note 1)	3.8	4	4.2	V
Setup time, data before CLK <sup>↑</sup> , t <sub>SU1</sub>	10			ns
Hold time, data after CLK↑, t <sub>h1</sub>	3			ns
Pulse duration at high level, t <sub>W1</sub>	8.3			ns
Pulse duration at low level, t <sub>w2</sub>	8.3			ns
External phase compensation capacitance, C <sub>COMP</sub>	1			μF
Operating free-air temperature, T <sub>A</sub>	0		70	°C

NOTE 1:  $V_{CC} - V_{ref} \le 1.2 \text{ V}$ 

# electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

	PARAMETER	TEST	CONDITIONS	MIN	түр†	MAX	UNIT
	Resolution					8	Bit
IIН	High-level input current	$V_{CC} = 5.25 V,$	V <sub>IH</sub> = 2.7 V			20	μΑ
١ <sub>IL</sub>	Low-level input current	$V_{CC} = 5.25 V,$	V <sub>IH</sub> = 2.7 V	-400			μΑ
I <sub>ref</sub>	Reference input current	REF IN = 4 V				10	μΑ
V <sub>ref</sub>	Reference output voltage	$V_{CC} = 5 V,$	With internal reference	3.8	4	4.2	V
VFS	Full-scale analog output voltage	V <sub>IH</sub> = 2 V,	REF IN = 4 V	AV <sub>CC</sub> -15	AVCC	AV <sub>CC</sub> +15	mV
VZS	Zero-scale analog output voltage	V <sub>IL</sub> = 0.8 V,	REF IN = 4 V	3.9	3.98	4.05	V
	RGB full-scale ratio			0%	4%	8%	
z <sub>0</sub>	Output impedance			200	240	280	W
ICC	Supply current				70	90	mA

# operating characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	түр†	MAX	UNIT
EL	Linearity error	End point, REF IN = 4 V			±0.5	LSB
ED	Differential linearity error	REF IN = 4 V			±0.5	LSB
f <sub>C</sub>	Maximum conversion rate		60			MHz
<sup>t</sup> PLH	Propagation delay time, low-to-high level			10		
tPHL Propagation delay time, high-to-low level		$T_{\rm r} = 25^{\circ}C$ $C_{\rm r} < 5 \text{ pc}^{\dagger}$		10		115
tr	Rise time	$IA = 25 \text{ C}, \text{ CL} \ge 5 \text{ pr}^{+}$		5		
tf	Fall time			5		115

<sup>†</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ .

<sup>‡</sup>C<sub>L</sub> includes probe and jig capacitances.



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### **APPLICATION INFORMATION**

The following design procedures should be used for optimum operation.

- External analog and digital circuitry should be physically separated and shielded as much as possible to reduce system noise.
- RF breadboarding or RF printed-circuit-board (PCB) techniques should be used throughout the evaluation and production process.
- Wide ground leads or a ground plane should be used on the PCB layouts to minimize parasitic inductance and resistance. A ground plane is the better choice for noise reduction.
- AV<sub>CC</sub> and DV<sub>CC</sub> are also separate internally, so they must be connected externally. These external PCB leads should also be made as wide as possible. A ferrite bead or equivalent inductance should be placed in series with AV<sub>CC</sub> and the decoupling capacitor before the AV<sub>CC</sub> and DV<sub>CC</sub> leads are connected together on the board. It is critical that the supply voltage applied to AV<sub>CC</sub> be as noise free and ripple free as possible. Ripple and noise rejection should be a minimum of 60 dB below the full-scale output range of 1 V peak-to-peak.
- AV<sub>CC</sub> to GND and DV<sub>CC</sub> to GND should be decoupled with 3.3-μF and 0.1-μF capacitors, respectively, as close as possible to the appropriate device terminals. A ceramic chip capacitor is recommended for the 0.1-μF capacitor.
- The phase compensation capacitor should be connected between C<sub>COMP</sub> and GND with as short a lead-in as possible.
- The no-connection (NC) terminals on the small-outline package should be connected to GND.
- AV<sub>CC</sub>, DV<sub>CC</sub>, and R<sub>OUT</sub>, G<sub>OUT</sub>, and B<sub>OUT</sub> should be shielded from the high-frequency terminals CLK<sub>R</sub> IN, CLK<sub>G</sub> IN, and CLK<sub>B</sub> IN and the input data terminals. GND traces should be placed on both sides of the R<sub>OUT</sub>, G<sub>OUT</sub>, and B<sub>OUT</sub> traces on the PCB to the following signal processing stage. These output traces should be as short as possible.



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NOTES: A. Buffers are SN74AS244 or equivalent.

B. 0.1  $\mu\text{F}$  capacitors should be placed as close to the device terminals as possible.

C. The coupling capacitor (C\_C) value is application specific and selectable by the user.

Figure 3. Typical Bypass, Buffer, and Output Configuration



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**MECHANICAL DATA** 

FR/S-PQFP-G44

PLASTIC QUAD FLATPACK



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.



#### PACKAGING INFORMATION

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
TL5632CFR	OBSOLETE	QFP	FR	44	TBD	Call TI	Call TI

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

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<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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