

RKV607KL

Variable Capacitance Diode for VCO

REJ03G1477-0100 Rev.1.00 Dec 17, 2007

Features

- High capacitance ratio. (n = 1.75 to 2.00)
- Low series resistance. (rs = 0.85Ω max)
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

Ordering Information

Part No.	Laser Mark	Package Name	Package Code	
RKV607KL	D	EFP	PXSF0002ZA-A	

Pin Arrangement

Cathode mark Mark 1	
	1. Cathode 2. Anode

Absolute Maximum Ratings

(Ta = 25°C)

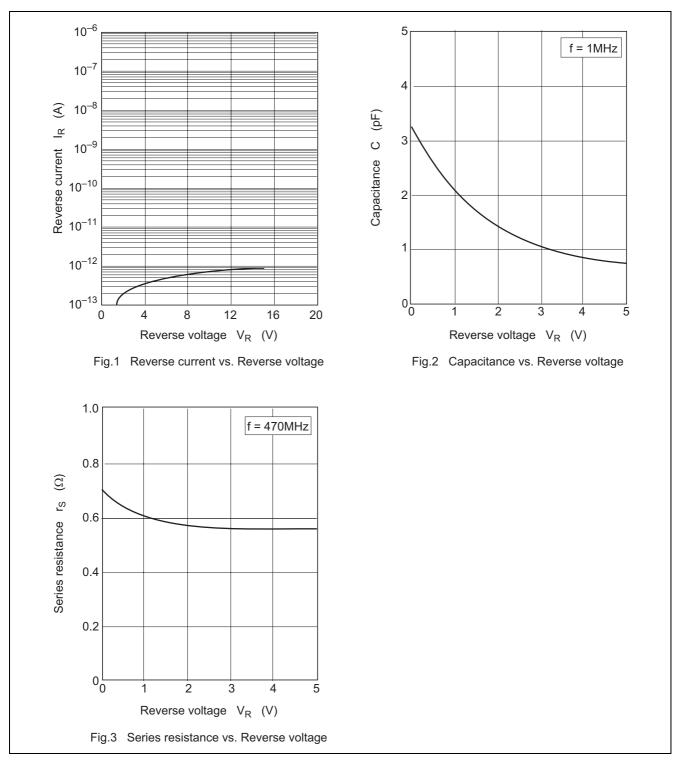
ltem	Symbol	Value	Unit
Reverse voltage	V _R	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	°C

Electrical Characteristics

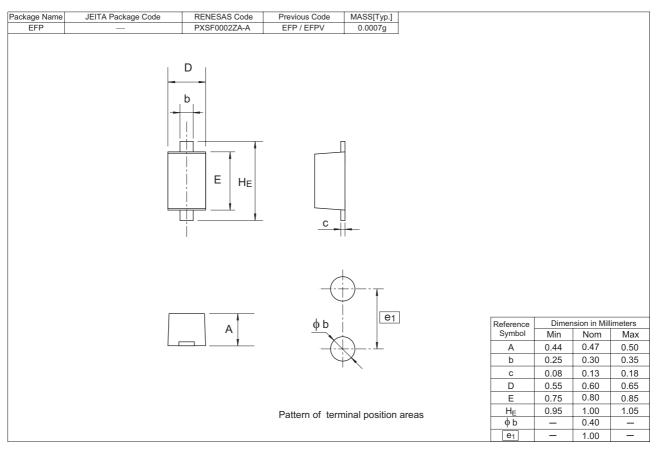
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}	—	—	10	nA	V _R = 15 V
	I _{R2}	—	—	50		V _R = 15 V, Ta = 60°C
Capacitance	C ₁	2.03	—	2.20	pF	$V_R = 1 V, f = 1 MHz$
	C ₃	1.05	—	1.20		$V_{R} = 3 V, f = 1 MHz$
Capacitance ratio	n	1.75	—	2.00	—	C ₁ /C ₃
Series resistance	r _S	_	_	0.85	Ω	V _R = 1 V, f = 470 MHz

Note: For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristic



Package Dimensions



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April 1st, 2010 Renesas Electronics Corporation

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