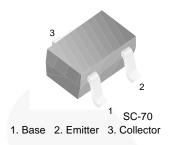
June 2013



FJX3904 NPN Epitaxial Silicon Transistor

Feature

• General-Purpose Transistor



Package Marking and Ordering Information

Device Item	Device Marking	Package	Packing Method	Qty (pcs)
FJX3904TF	S1A	SC-70	TAPE & REEL	3,000 units

Absolute Maximum Ratings⁽¹⁾

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_{\Delta} = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	60	V
V _{CES}	Collector-Emitter Voltage	40	V
V _{EBO}	Emitter-Base Voltage	6	V
Ι _C	Collector Current	200	mA
P _C	Collector Power Dissipation	350	mW
T _{STG} ⁽²⁾	Storage Temperature	-55 to 150	°C

Notes:

1. These ratings are limiting values above which the serviceability of the diode may be impaired.

2. These ratings are bansed on a maximum junction temperature of 150°C.

These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics⁽³⁾

Symbol	Parameter	Value	Unit
PD	Derate above 25°C	2.8	mW/°C
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Air	357	°C/W

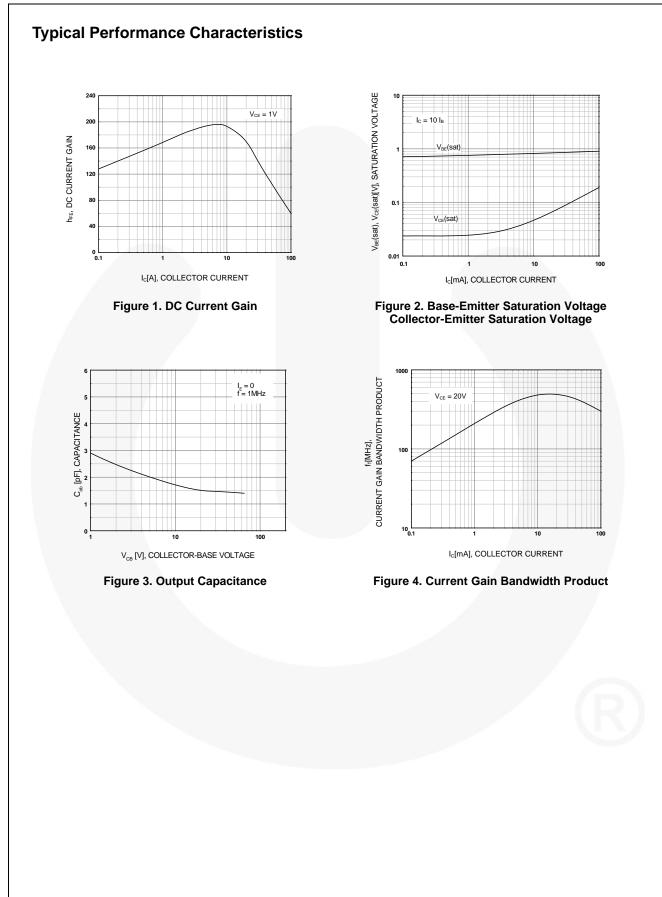
Note:

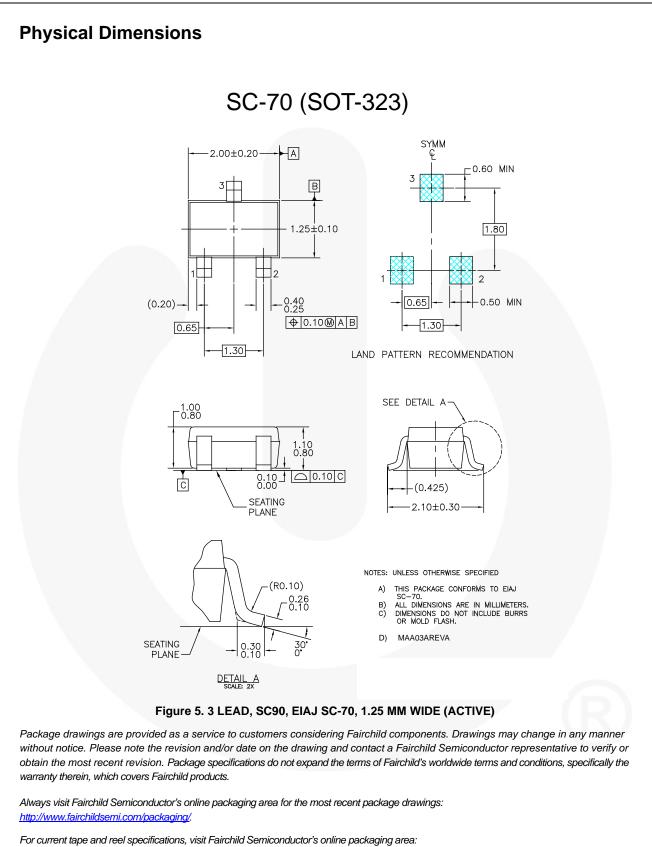
3. PCB board size: FR-4 76 x 114 x 0.6 T mm³ (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

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Symbol	Parameter	Test Conditions	Min.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{C} = 10 \ \mu A, \ I_{E} = 0$	60		V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 1 {\rm mA}, I_{\rm B} = 0$	40		V
BV_{EBO}	Emitter-Base Breakdown Voltage	$I_{E} = 10 \ \mu A, I_{C} = 0$	6		V
I _{CEX}	Collector Cut-Off Current	V _{CE} = 30 V, V _{EB} = 3 V		50	nA
		$V_{CE} = 1 V, I_{C} = 0.1 mA$	40		
h _{FE}		$V_{CE} = 1 V, I_{C} = 1 mA$	70		
	DC Current Gain	$V_{CE} = 1 V, I_{C} = 10 mA$	100	300	
		$V_{CE} = 1 \text{ V}, I_{C} = 50 \text{ mA}$	60		
		V _{CE} = 1 V, I _C = 100 mA	30		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 10 mA, I _B = 1 mA		0.2	V
		I _C = 50 mA, I _B = 5 mA		0.3	V
V (a a t)	Base-Emitter Saturation Voltage	I _C = 10 mA, I _B = 1 mA	0.65	0.85	V
V _{BE} (sat)		$I_{\rm C} = 50 \text{ mA}, I_{\rm B} = 5 \text{ mA}$		0.95	V
C _{ob}	Output Capacitance	$V_{CB} = 5 V$, $I_E = 0$, $f = 1 MHz$		4	pF
f _T	Current Gain Bandwidth Product	$V_{CE} = 20 \text{ V}, \text{ I}_{C} = 10 \text{ mA}$	300		MHz
NF	Noise Figure	$I_{C} = 100 \mu\text{A}, V_{CE} = 5 \text{V}, \\ R_{S} = 1 k\Omega, \\ f = 10 \text{Hz} \text{ to } 15.7 \text{kHz}$		5	dB
t _{ON}	Turn-On Time	$V_{CC} = 3 V, V_{BE} = 0.5 V,$ $I_{C} = 10 mA, I_{B1} = 1 mA$		70	ns
t _{OFF}	Turn-Off Time	$V_{CC} = 3 V, I_C = 10 mA,$ $I_{B1} = I_{B2} = 1 mA$		250	ns

4. Pulse test: pulse width \leq 300 µs, duty cycle \leq 2.0%.





FJX3904 — NPN Epitaxial Silicon Transistor

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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