

June 2016

FFSH15120ADN_F155 Silicon Carbide Schottky Diode 1200 V, 15 A

Features

- Max Junction Temperature 175 °C
- · Avalanche Rated 80 mJ
- · High Surge Current Capacity
- · Positive Temperature Coefficient
- · Ease of Paralleling
- · No Reverse Recovery / No Forward Recovery

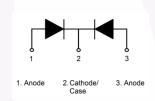
Applications

- · General Purpose
- · SMPS, Solar Inverter, UPS
- · Power Switching Circuits

Description

SiC Schottky Diode has no switching loss, provides improved system efficiency against Si diodes by utilizing new semiconductor material - Silicon Carbide, enables higher operating frequency, and helps increasing power density and reduction of system size/cost. Its high reliability ensures robust operation during surge or over-voltage conditions





Absolute Maximum Ratings T_C = 25 °C unless otherwise noted. (per leg)

Symbol	Paramete	FFSH15120ADN_F155	Unit	
V_{RRM}	Peak Repetitive Reverse Voltage	1200	V	
E _{AS}	Single Pulse Avalanche Energy	80	mJ	
l _F	Continuous Rectified Forward Current @ Tc < 148 °C		8* / 15**	Α
I _{F, Max}	Non-Repetitive Peak Forward Surge Current	T _C = 25 °C, 10 μs	560	Α
		T _C = 150 °C, 10 μs	500	Α
I _{F,SM}	Non-Repetitive Forward Surge Current	Half-Sine Pulse, t _p = 8.3 ms	80	Α
F,RM	Repetitive Forward Surge Current Half-Sine Pulse, t _p = 8.3 ms		36	Α
Ptot	Dower Dissination	T _C = 25 °C	110	W
	Power Dissipation	T _C = 150 °C	19	W
Γ _J , T _{STG}	Operating and Storage Temperature Range	-55 to +175	°C	
	TO247 Mounting Torque, M3 Screw	60	Ncm	

Thermal Characteristic

Symbol	Parameter	FFSH15120ADN_F155	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max	1.35* / 0.56**	°C/W

^{*} Per leg, ** Per Device

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity	l
FFSH15120ADN_F155	FFSH15120ADN	TO-247 Long Lead	Tube	N/A	N/A	30 units	l

Electrical Characteristics $T_C = 25$ °C unless otherwise noted. (per leg)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
		$I_F = 8 \text{ A}, T_C = 25 ^{\circ}\text{C}$	-	1.45	1.75	
V _F	Forward Voltage	I _F = 8 A, T _C = 125 °C	-	1.7	2	V
		I _F = 8 A, T _C = 175 °C	-	2	2.4	
I _R	Reverse Current	$V_R = 1200 \text{ V}, T_C = 25 ^{\circ}\text{C}$	-	-	200	μА
		V _R = 1200 V, T _C = 125 °C	-	-	300	
		$V_R = 1200 \text{ V}, T_C = 175 ^{\circ}\text{C}$	-	-	400	
Q_C	Total Capacitive Charge	V = 800 V	-	55	-	nC
С		$V_R = 1 \text{ V, f} = 100 \text{ kHz}$	-	538	-	
	Total Capacitance	$V_R = 400 \text{ V}, f = 100 \text{ kHz}$	-	50	-	pF
		$V_R = 800 \text{ V}, f = 100 \text{ kHz}$	-	40	-	

Typical Characteristics T_J = 25 °C unless otherwise noted (per leg).

Figure 1. Forward Characteristics

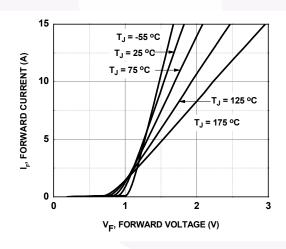


Figure 2. Reverse Characteristics

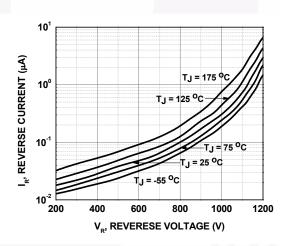


Figure 3. Reverse Characteristics

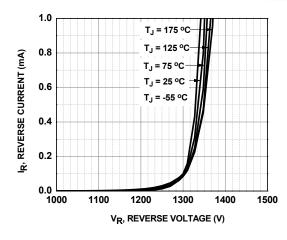
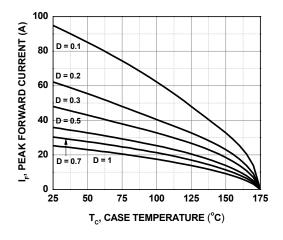


Figure 4. Current Derating



Notes: 1: EAS of 80 mJ is based on starting T_J = 25 °C, L = 0.5 mH, I_{AS} = 18 A, V = 150 V.

Typical Characteristics $T_J = 25$ °C unless otherwise noted (per leg, continue).

Figure 5. Power Derating

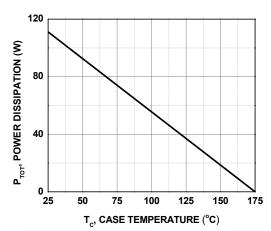


Figure 7. Capacitance vs. Reverse Voltage

Figure 6. Capacitive Charge vs. Reverse Voltage

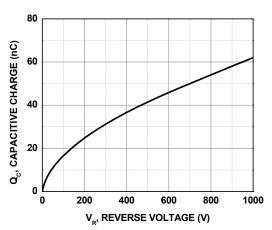
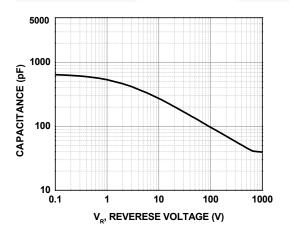


Figure 8. Capacitance Stored Energy



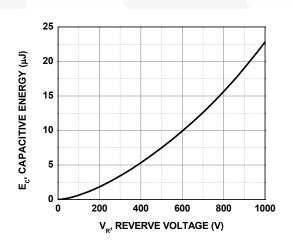
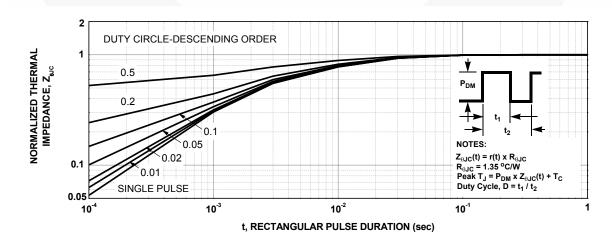
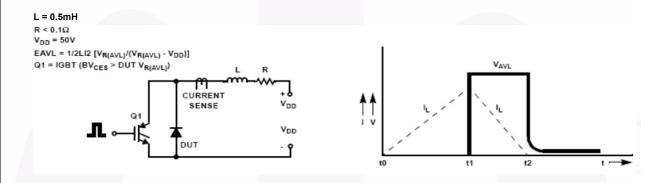


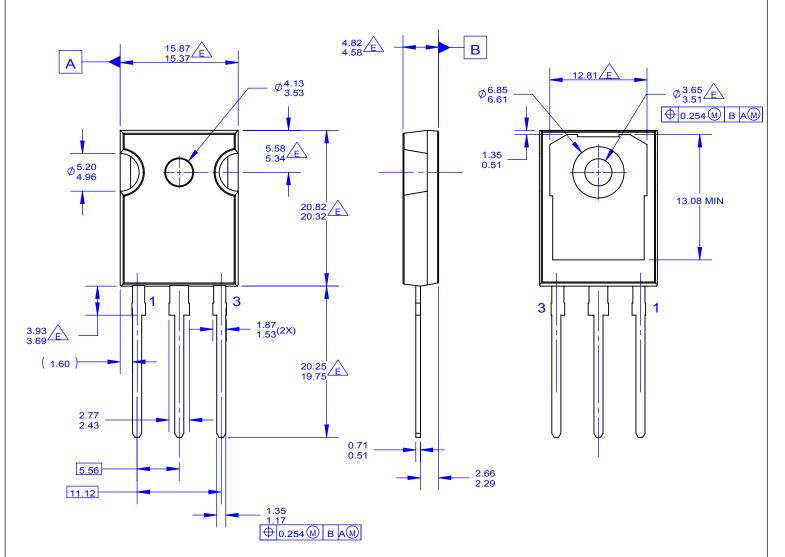
Figure 9. Junction-to-Case Transient Thermal Response Curve



Test Circuit and Waveforms

Figure 10. Unclamped Inductive Switching Test Circuit & Waveform





NOTES: UNLESS OTHERWISE SPECIFIED.

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Definition of Terms				
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