

KA741

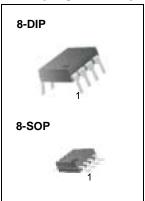
Single Operational Amplifier

Features

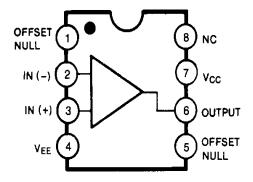
- Short circuit protection
- Excellent temperature stability
- Internal frequency compensation
- High Input voltage range
- · Null of offset

Description

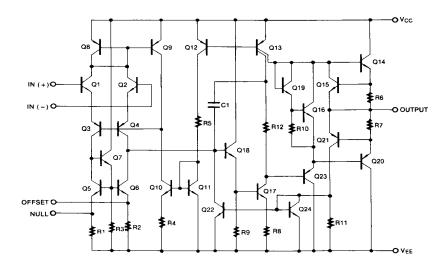
The KA741 series are general purpose operational amplifiers. It is intended for a wide range of analog applications. The high gain and wide range of operating voltage provide superior performance in intergrator, summing amplifier, and general feedback applications.



Internal Block Diagram



Schematic Diagram



Absolute Maximum Ratings (T_A = 25°C)

Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	±18	V
Differential Input Voltage	VI(DIFF)	30	V
Input Voltage	VI	±15	V
Output Short Circuit Duration	-	Indefinite	
Power Dissipation	PD	500	mW
Operating Temperature Range KA741 KA741I	TOPR	0 ~ + 70 -40 ~ +85	°C
Storage Temperature Range	TSTG	-65 ~ + 150	°C

Electrical Characteristics

(VCC = 15V, VEE = - 15V. TA = 25 $^{\circ}$ C, unless otherwise specified)

Parameter Symbol		Cumbal	Symbol Conditions		KA741/KA741I			Unit
		Conditions		Min.	Тур.	Max.	Unit	
Input Offset Voltage		Vio	R _S ≤10KΩ		-	2.0	6.0	m\/
Input Onset volta	ge	VIO	Rs≤50Ω		-	-	-	mV
Input Offset Volta Adjustment Rang		VIO(R)	VCC = ±20V		-	±15	-	mV
Input Offset Curre	ent	lio		-	-	20	200	nA
Input Bias Curren	it	IBIAS		-	-	80	500	nA
Input Resistance	(Note1)	Rı	Vcc =±20V		0.3	2.0	-	MΩ
Input Voltage Rar	nge	VI(R)		-	±12	±13	-	V
			RL≥2KΩ	VCC =±20V, VO(P-P) =±15V	-	-	-	\//m\/
Large Signal Voltage G	age Gain	Gy		VCC =±15V, VO(P-P) =±10V	20	200	-	- V/mV
Output Short Circ	uit Current	Isc		-	-	25	-	mA
			VCC = ±20V	RL≥10KΩ	-	-	-	
Output Valtage S	wing	VO(P-P)		R _L ≥2KΩ	-	-	-	V
Output voitage S	Output Voltage Swing		VCC = ±15V	RL≥10KΩ	±12	±14	-	V
				R _L ≥2KΩ	±10	±13	-	
Common Mode P	Common Mode Rejection Ratio		Rs≤10K Ω , VcM = ±12V		70	90	-	- dB
Common wode N			Rs≤50Ω, V _{CM} = ±12V		-	-	-	ub
Power Supply Rejection Ratio		PSRR	$V_{CC} = \pm 15V$ to $V_{CC} = \pm 15V$ R _S ≤50Ω		-	-	-	- dB
		PSKK	$VCC = \pm 15V$ to $VCC = \pm 15V$ R _S ≤10KΩ		77	96	-	J UB
Transient	Rise Time	TR	- Unity Gain		-	0.3	-	μs
Response	Overshoot	os			-	10	-	%
Bandwidth	•	BW	-		-	-	-	MHz
Slew Rate		SR	Unity Gain		-	0.5	-	V/μs
Supply Current		Icc	RL= ∞Ω		-	1.5	2.8	mA
Power Consumpt	ion	Do.	Vcc = ±20V		-	-	-	mW
Fower Consumpt	1011	PC	$VCC = \pm 15V$		-	50	85	IIIVV

Note:

1. Guaranteed by design.

Electrical Characteristics

(VCC = ± 15 V, unless otherwise specified) The following specification apply over the range of 0°C \leq TA \leq +70 °C for the KA741; and the -40°C \leq TA \leq +85 °C for the KA741I

Parameter	Comple	Conditions		KA741/KA741I			l lmit	
Parameter	Symbol			Min.	Тур.	Max.	Unit	
Input Offact Valtage	V _{IO} R _S ≤50Ω		-	-	-	mV		
Input Offset Voltage	VIO	Rs≤10KΩ		-	-	7.5	mv	
Input Offset Voltage Drift	ΔV10/ΔΤ		-	-	-		μV/°C	
Input Offset Current	lio		-	-	-	300	nA	
Input Offset Current Drift	ΔΙΙΟ/ΔΤ		-	-	-		nA/°C	
Input Bias Current	IBIAS		-	-	-	0.8	μΑ	
Input Resistance (Note1)	Rı	Vcc = ±20V		-	-	-	MΩ	
Input Voltage Range	VI(R)		-	±12	±13	-	V	
	VO(P-P)	VCC =±20V	Rs≥10KΩ	-	-	-	V	
Output Voltage Swing			Rs≥2KΩ	-	-	-		
Output Voltage Swing		VCC =±15V	Rs≥10KΩ	±12	±14	-		
			R _S ≥2KΩ	±10	±13	-		
Output Short Circuit Current	Isc		-	10	-	40	mA	
Common Mode Rejection Ratio	CMRR	RS≤10KΩ, V _{CM} = \pm 12V RS≤50Ω, V _{CM} = \pm 12V		70	90	-	٩D	
Common wode Rejection Ratio	CIVIKK			-	-	-	dB	
Power Supply Rejection Ratio	PSRR	VCC = ±20V to ±5V	Rs≤50Ω	-	-	-	dB	
			Rs≤10KΩ	77	96	-		
Large Signal Voltage Gain	Gv	Rs≥2KΩ	$VCC = \pm 20V,$ $VO(P-P) = \pm 15V$	-	-	-	V/mV	
			$VCC = \pm 15V,$ $VO(P.P) = \pm 10V$	15	-	-		
			$VCC = \pm 15V,$ $VO(P-P) = \pm 2V$		-			

Note:

^{1.} Guaranteed by design.

Typical Performance Characteristics

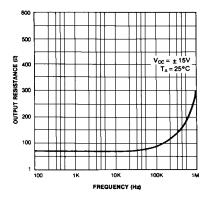


Figure 1. Output Resistance vs Frequency

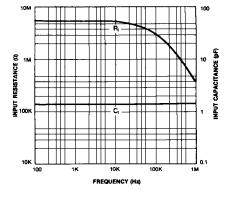


Figure 2. Input Resistance and Input Capacitance vs Frequency

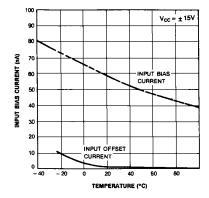


Figure 3. Input Bias Current vs Ambient Temperature

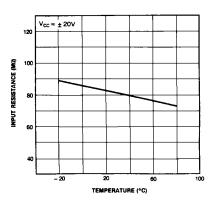


Figure 4. Power Consumption vs Ambient Temperature

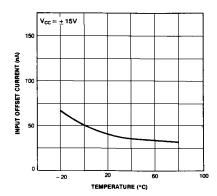


Figure 5. Input Offset Current vs Ambient Temperature

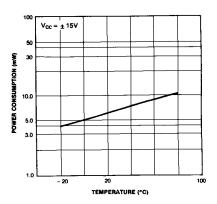


Figure 6. Input Resistance vs Ambient Temperature

Typical Performance Characteristics (continued)

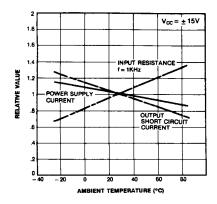


Figure 7. Normalized DC Parameters vs Ambient Temperature

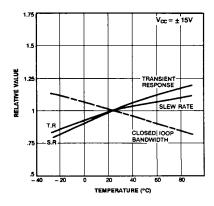


Figure 8. Frequency Characteristics vs
Ambient Temperature

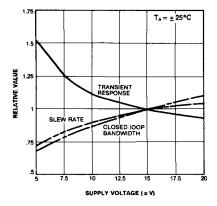


Figure 9. Frequency Characteristics vs Supply Voltage

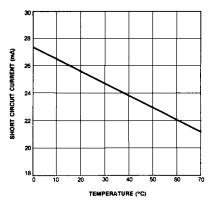


Figure 10. Output Short Circuit Current vs Ambient Temperature

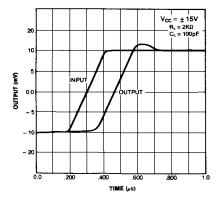


Figure 11. Transient Response

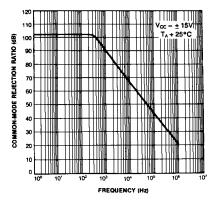


Figure 12. Common-Mode Rejection Ratio vs Frequency

Typical Performance Characteristics (continued)

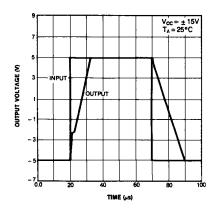


Figure 13. Voltage Follower Large Signal Pulse Response

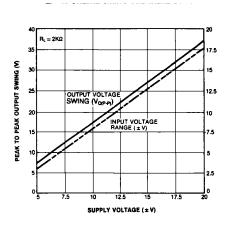
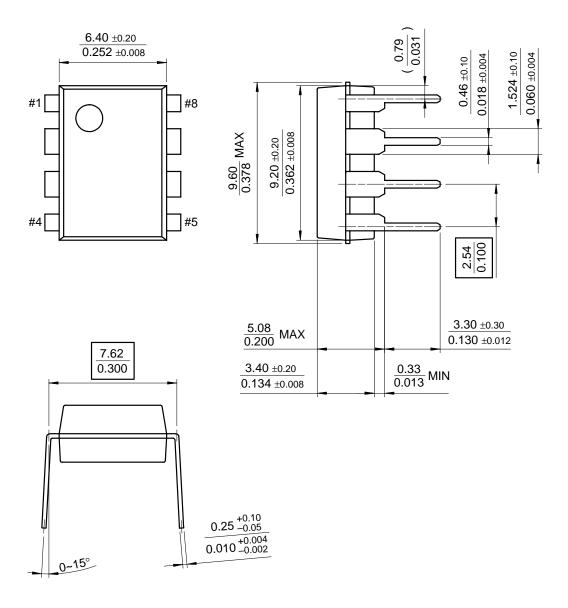


Figure 14. Output Swing and Input Range vs Supply Voltage

Mechanical Dimensions

Package

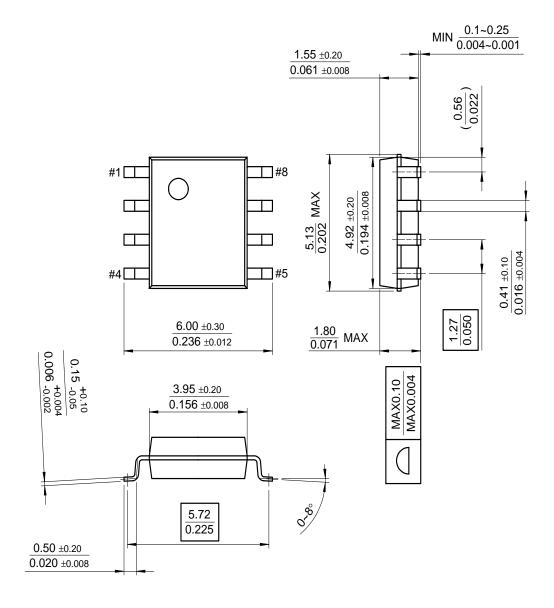
8-DIP



Mechanical Dimensions (Continued)

Package

8-SOP



Ordering Information

Product Number	Package	Operating Temperature
KA741	8-DIP	0 ~ + 70°C
KA741D	8-SOP	0~+70 C
KA741I	8-DIP	-40 ~ + 85°C

DISCLAIMER

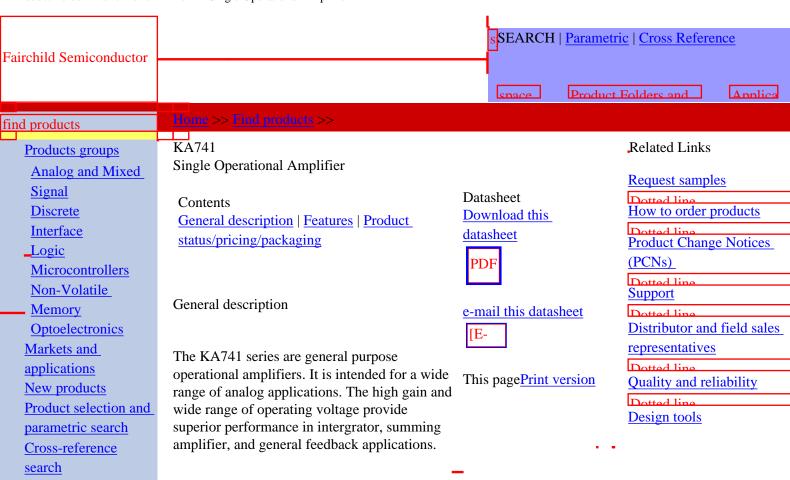
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Features

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technical information

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company

- Short circuit protection
- Excellent temperature stability
- Internal frequency compensation
- High Input voltage range
- Null of offset

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Product status/pricing/packaging

Product	Product status	Package type	Leads	Packing method
KA741DTF	Full Production	SOIC	8	TAPE REEL
KA741I	Full Production	DIP	8	RAIL
KA741	Full Production	DIP	8	RAIL
KA741D	Full Production	SOIC	8	RAIL

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