

MC74LCX00

Low-Voltage CMOS Quad 2-Input NAND Gate With 5 V-Tolerant Inputs

The MC74LCX00 is a high performance, quad 2-input NAND gate operating from a 2.3 to 3.6 V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A V_I specification of 5.5 V allows MC74LCX00 inputs to be safely driven from 5 V devices.

Current drive capability is 24 mA at the outputs.

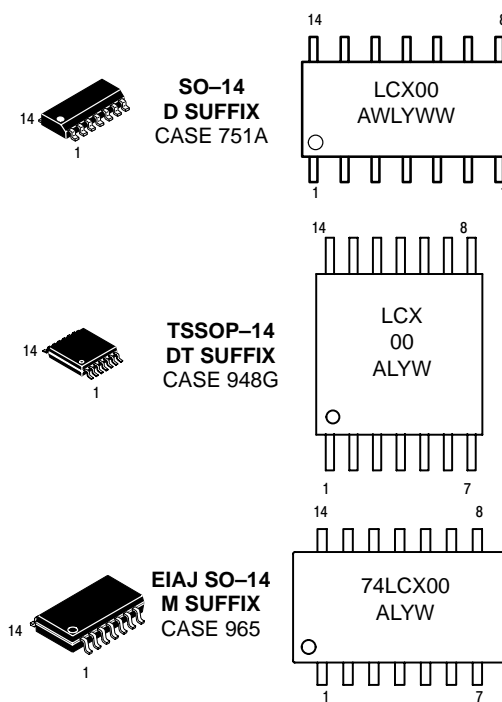
- Designed for 2.3 to 3.6 V V_{CC} Operation
- 5 V Tolerant Inputs – Interface Capability With 5 V TTL Logic
- LVTTL Compatible
- LVCMOS Compatible
- 24 mA Balanced Output Sink and Source Capability
- Near Zero Static Supply Current (10 μ A) Substantially Reduces System Power Requirements
- Latchup Performance Exceeds 500 mA
- ESD Performance: Human Body Model >2000 V; Machine Model >200 V



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MARKING DIAGRAMS



A = Assembly Location
 L, WL = Wafer Lot
 Y = Year
 W, WW = Work Week

ORDERING INFORMATION

Device	Package	Shipping
MC74LCX00D	SO-14	55 Units/Rail
MC74LCX00DR2	SO-14	2500 Units/Reel
MC74LCX00DT	TSSOP-14	96 Units/Rail
MC74LCX00DTR2	TSSOP-14	2500 Units/Reel
MC74LCX00M	EIAJ SO-14	50 Units/Rail
MC74LCX00MEL	EIAJ SO-14	2000 Units/Reel

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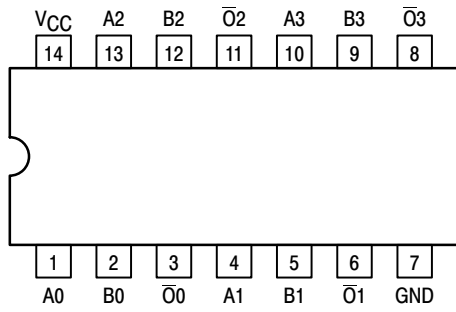


Figure 1. LOGIC DIAGRAM

PIN NAMES

Pins	Function
A _n , B _n	Data Inputs
\bar{O}_n	Outputs

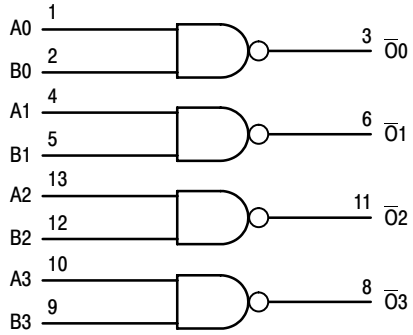


Figure 2. Pinout: 14-Lead (Top View)

TRUTH TABLE

Inputs		Outputs
A _n	B _n	\bar{O}_n
L	L	H
L	H	H
H	L	H
H	H	L

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MAXIMUM RATINGS

Symbol	Parameter	Value	Condition	Unit
V _{CC}	DC Supply Voltage	-0.5 to +7.0		V
V _I	DC Input Voltage	-0.5 ≤ V _I ≤ +7.0		V
V _O	DC Output Voltage	-0.5 ≤ V _O ≤ V _{CC} + 0.5	Note 1.	V
I _{IK}	DC Input Diode Current	-50	V _I < GND	mA
I _{OK}	DC Output Diode Current	-50	V _O < GND	mA
		+50	V _O > V _{CC}	mA
I _O	DC Output Source/Sink Current	±50		mA
I _{CC}	DC Supply Current Per Supply Pin	±100		mA
I _{GND}	DC Ground Current Per Ground Pin	±100		mA
T _{STG}	Storage Temperature Range	-65 to +150		°C

Maximum Ratings are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute maximum-rated conditions is not implied. Functional operation should be restricted to the Recommended Operating Conditions.

1. Output in HIGH or LOW State. I_O absolute maximum rating must be observed.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Typ	Max	Unit
V _{CC}	Supply Voltage	Operating	3.3	3.6	V
		Data Retention Only	1.5	3.6	
V _I	Input Voltage	0		5.5	V
V _O	Output Voltage (HIGH or LOW State)	0		V _{CC}	V
I _{OH}	HIGH Level Output Current, V _{CC} = 3.0 V – 3.6 V			-24	mA
I _{OL}	LOW Level Output Current, V _{CC} = 3.0 V – 3.6 V			24	mA
I _{OH}	HIGH Level Output Current, V _{CC} = 2.7 V – 3.0 V			-12	mA
I _{OL}	LOW Level Output Current, V _{CC} = 2.7 V – 3.0 V			12	mA
T _A	Operating Free-Air Temperature	-40		+85	°C
Δt/ΔV	Input Transition Rise or Fall Rate, V _{IN} from 0.8 V to 2.0 V, V _{CC} = 3.0 V	0		10	ns/V

DC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic	Condition	T _A = -40°C to +85°C		Unit
			Min	Max	
V _{IH}	HIGH Level Input Voltage (Note 2.)	2.7 V ≤ V _{CC} ≤ 3.6 V	2.0		V
V _{IL}	LOW Level Input Voltage (Note 2.)	2.7 V ≤ V _{CC} ≤ 3.6 V		0.8	V
V _{OH}	HIGH Level Output Voltage	2.7 V ≤ V _{CC} ≤ 3.6 V; I _{OH} = -100 μA	V _{CC} - 0.2		V
		V _{CC} = 2.7 V; I _{OH} = -12 mA	2.2		
		V _{CC} = 3.0 V; I _{OH} = -18 mA	2.4		
		V _{CC} = 3.0 V; I _{OH} = -24 mA	2.2		
V _{OL}	LOW Level Output Voltage	2.7 V ≤ V _{CC} ≤ 3.6 V; I _{OL} = 100 μA		0.2	V
		V _{CC} = 2.7 V; I _{OL} = 12 mA		0.4	
		V _{CC} = 3.0 V; I _{OL} = 16 mA		0.4	
		V _{CC} = 3.0 V; I _{OL} = 24 mA		0.55	

2. These values of V_I are used to test DC electrical characteristics only.

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DC ELECTRICAL CHARACTERISTICS (continued)

Symbol	Characteristic	Condition	T _A = -40°C to +85°C		Unit
			Min	Max	
I _I	Input Leakage Current	2.7V ≤ V _{CC} ≤ 3.6 V; 0 V ≤ V _I ≤ 5.5 V		±5.0	μA
I _{CC}	Quiescent Supply Current	2.7 ≤ V _{CC} ≤ 3.6 V; V _I = GND or V _{CC}		10	μA
		2.7 ≤ V _{CC} ≤ 3.6 V; 3.6 ≤ V _I ≤ 5.5 V		±10	μA
ΔI _{CC}	Increase in I _{CC} per Input	2.7 ≤ V _{CC} ≤ 3.6 V; V _{IH} = V _{CC} - 0.6 V		500	μA

AC CHARACTERISTICS (t_R = t_F = 2.5 ns; C_L = 50 pF; R_L = 500 Ω)

Symbol	Parameter	Waveform	Limits			Unit
			T _A = -40°C to +85°C			
			V _{CC} = 3.0 V to 3.6 V		V _{CC} = 2.7 V	
			Min	Max	Max	
t _{PLH} t _{PHL}	Propagation Delay Input to Output	1	1.5 1.5	5.2 5.2	6.0 6.0	ns
t _{OSHL} t _{OSLH}	Output-to-Output Skew (Note 3.)			1.0 1.0		ns

3. Skew is defined as the absolute value of the difference between the actual propagation delay for any two separate outputs of the same device. The specification applies to any outputs switching in the same direction, either HIGH-to-LOW (t_{OSHL}) or LOW-to-HIGH (t_{OSLH}); parameter guaranteed by design.

DYNAMIC SWITCHING CHARACTERISTICS

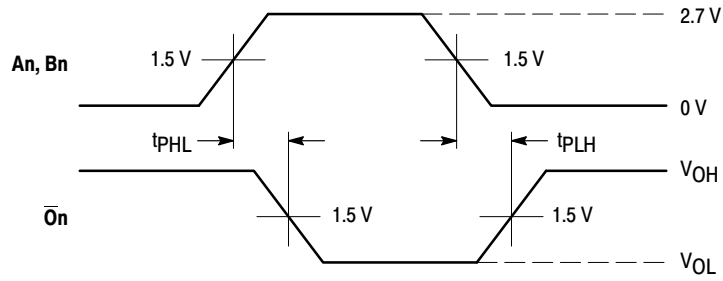
Symbol	Characteristic	Condition	T _A = +25°C			Unit
			Min	Typ	Max	
V _{OLP}	Dynamic LOW Peak Voltage (Note 4.)	V _{CC} = 3.3 V, C _L = 50 pF, V _{IH} = 3.3 V, V _{IL} = 0 V		0.8		V
V _{OLV}	Dynamic LOW Valley Voltage (Note 4.)	V _{CC} = 3.3 V, C _L = 50 pF, V _{IH} = 3.3 V, V _{IL} = 0 V		0.8		V

4. Number of outputs defined as "n". Measured with "n-1" outputs switching from HIGH-to-LOW or LOW-to-HIGH. The remaining output is measured in the LOW state.

CAPACITIVE CHARACTERISTICS

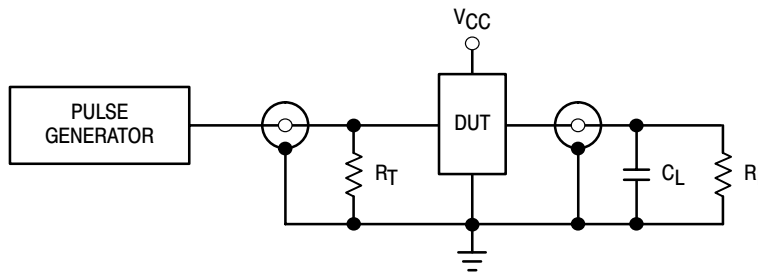
Symbol	Parameter	Condition	Typical	Unit
C _{IN}	Input Capacitance	V _{CC} = 3.3 V, V _I = 0 V or V _{CC}	7	pF
C _{OUT}	Output Capacitance	V _{CC} = 3.3 V, V _I = 0 V or V _{CC}	8	pF
C _{PD}	Power Dissipation Capacitance	10 MHz, V _{CC} = 3.3 V, V _I = 0 V or V _{CC}	25	pF

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PROPAGATION DELAYS
 $t_R = t_F = 2.5 \text{ ns}$, 10% to 90%; $f = 1 \text{ MHz}$; $t_W = 500 \text{ ns}$

Figure 3. AC Waveforms



$C_L = 50 \text{ pF}$ or equivalent (Includes jig and probe capacitance)
 $R_L = R_1 = 500 \Omega$ or equivalent
 $R_T = Z_{OUT}$ of pulse generator (typically 50Ω)

Figure 4. Test Circuit