Features

- Precision Monitoring of Multiple +1.8V, +2.5V, +3.0V, +3.3V, and +5V Power-Supply Voltages
- Precision Factory-Set Power-Supply **Reset Thresholds**
- 20µA Supply Current
- 100ms min Power-On Reset Pulse Width
- Debounced TTL/CMOS-Compatible Manual-Reset Input
- Watchdog Timer 46.4s Startup Timeout 2.9s Normal Timeout
- Fully Guaranteed Over Temperature
- ♦ Guaranteed RESET Valid to Vcc1 = 1V or $V_{CC2} = 1V$
- Power-Supply Transient Immunity
- No External Components for Dual-Voltage Systems
- Small 5- and 6-Pin SOT23 Packages
- Low Cost

Applications

Computers	Intelligent Instruments
Controllers	Multivoltage Systems
Portable/Battery-Powered Equipment	

Ordering Information

PART*	TEMP RANGE	PIN-PACKAGE
MAX6351UT-T	-40°C to +85°C	6 SOT23-6
MAX6352UK-T	-40°C to +85°C	5 SOT23-5
MAX6353UK-T	-40°C to +85°C	5 SOT23-5
MAX6354UK-T	-40°C to +85°C	5 SOT23-5
MAX6355UT-T	-40°C to +85°C	6 SOT23-6
MAX6356UT-T	-40°C to +85°C	6 SOT23-6
MAX6357UT-T	-40°C to +85°C	6 SOT23-6
MAX6358UT-T	-40°C to +85°C	6 SOT23-6
MAX6359UT-T	-40°C to +85°C	6 SOT23-6
MAX6360UT-T	-40°C to +85°C	6 SOT23-6

*The _ _ are placeholders for the threshold voltage levels of the devices. Substitute the part number suffix in the Voltage Threshold Levels table for the desired voltage level. All devices are available in tape-and-reel only. There is a 2500 piece minimum order increment for the SOT package.

Devices are available in both leaded and lead-free packaging. Specify lead-free by replacing "-T" with "+T" when ordering.

Pin Configurations appear at end of data sheet. Selector Guide appears at end of data sheet. Maxim Integrated Products 1

S

1.67 1.58 2.32 2.19 1.67 1.58 1.67 1 58

Note: Standard versions are shown in bold. Sample stock is generally held on the standard versions only. Contact factory for availability.

M/X/M

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

packages and operate over the extended (-40°C to +85°C) temperature range. Voltage Threshold Levels

General Description

The MAX6351-MAX6360 microprocessor (µP) supervi-

sors with multiple reset voltages significantly improve sys-

tem reliability and accuracy compared to separate ICs or

discrete components. If any input supply voltage drops

below its associated preset threshold, all reset outputs are asserted. In addition, the outputs are valid as long as

All devices in this series have an active-low debounced manual reset input. In addition, the MAX6358/MAX6359/

MAX6360 offer a watchdog-timer input with a 46.4s start-

up timeout period and a 2.9s timeout period. The MAX6355/MAX6356/MAX6357 offer an additional voltage

The MAX6351 features two active-low, push-pull reset outputs, one is referenced to V_{CC}1 and the other is refer-

enced to V_{CC}2. The MAX6353/MAX6356/MAX6359 offer

an active-low, push-pull reset output referenced to V_{CC}1.

The MAX6354/MAX6357/MAX6360 offer an active-low,

All these devices are offered with a wide variety of voltage

threshold levels, as shown in the Voltage Threshold

Levels table. They are available in 5- and 6-pin SOT23

monitor input to monitor a third voltage.

push-pull reset output referenced to V_{CC}2.

either input supply voltage remains greater than +1.0V

PART NO. V_{CC}1 NOMINAL Vcc2 NOMINAL SUFFIX VOLTAGE VOLTAGE THRESHOLD (V) THRESHOLD (V) (__) LT 4.63 3.08 LS 4.63 2.93 LR MT MS MR ΤZ TΥ тw 3.08 ΤV 3.08 SZ 2.93 SY 2.93 SW 2.93 SV 2.93 UW 2.78 UV 2.78 RW 2.63 1.67 RV 2.63 1.58

4.00	2.50	
4.63	2.63	
4.38	3.08	
4.38	2.93	
4.38	2.63	
3.08	2.32	
3.08	2.19	

ABSOLUTE MAXIMUM RATINGS

V _{CC} 1, V _{CC} 2 to GND0.3V to +6V	Input/Output Current, All Pins20mA
RST (MAX6352/MAX6355/MAX6358)	Continuous Power Dissipation ($T_A = +70^{\circ}C$)
RST, MR, WDI, RST1, RSTIN (MAX6351/MAX6353/	5-Pin SOT23 (derate 7.1mW/°C above +70°C)571mW
MAX6356/MAX6359)0.3V to (V _{CC} 1 + 0.3V)	6-Pin SOT23 (derate 8.7mW/°C above +70°C)695mW
RST, RST2 (MAX6351/MAX6354/	Operating Temperature Range40°C to +85°C
MAX6357/MAX6360)0.3V to (V _{CC} 2 + 0.3V)	Junction Temperature+150°C
, , , , , , , , , , , , , , , , , , , ,	Storage Temperature Range65°C to +150°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

$(V_{CC}1 = V_{CC}2 = +1.2V \text{ to } +5.5V, T_{A})$	- 10°C to 195°C ur	place otherwise poted .	Typical values are at Ty	-125° C) (Note 1)
$(VCC) = VCCZ = \pm 1.2V (0 \pm 3.3V, 1A)$	$= -40$ C to ± 05 C, ut	niess otherwise noted.	i ypical values ale al 1 <i>4</i>	(-+250.)(100001)

PARAMETER	SYMBOL	CONDITIONS		MIN	ТҮР	МАХ	UNITS	
Oursely Malta an	V _{CC} 1,	$T_A = 0^{\circ}C \text{ to } + 70^{\circ}C$	$T_{A} = 0^{\circ}C \text{ to } +70^{\circ}C$ $T_{A} = -40^{\circ}C \text{ to } +85^{\circ}C$			5.5	N	
Supply Voltage	V _{CC} 2	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$ $V_{CC1} = 5.5V, V_{CC2} = 3.6V, all I/O pins open$		1.2		5.5	V	
Supply Current	I _{CC} 1 + I _{CC} 2	V _{CC} 1 = 5.5V, V _{CC} 2	2 = 3.6V, all I/O pins open		20	50	μA	
			$T_A = +25^{\circ}C$	4.54	4.63	4.72		
		MAX63L_	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	4.5		4.75		
			T _A = +25°C	4.3	4.38	4.46]	
		MAX63M_	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	4.25		4.50		
		MAY62 T	$T_A = +25^{\circ}C$	3.03	3.08	3.14		
V _{CC} 1 Threshold (Note 3)	V _{TH} 1	MAX63T_	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	3.00		3.15	V	
VCCT THESHOLD (NOLE 3)	VIHI	MAX63S_	$T_A = +25^{\circ}C$	2.88	2.93	2.98		
			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	2.85		3.00		
		MAX63U_	$T_A = +25^{\circ}C$	2.74	2.78	2.82		
			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	2.70		2.85		
		MAX63R_	T _A = +25°C	2.58	2.63	2.68		
			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	2.55		2.70		
		MAX63T	$T_A = +25^{\circ}C$	3.03	3.08	3.14		
			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	3.00		3.15		
		MAX63S	T _A = +25°C	2.88	2.93	2.98		
			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	2.85		3.00		
		MAYCO	T _A = +25°C	2.58	2.63	2.68		
		MAX63R	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	2.55		2.70		
VacQ Threshold (Nata 2)	$\lambda = 0$	MAX63Z	$T_A = +25^{\circ}C$	2.28	2.32	2.35		
V _{CC} 2 Threshold (Note 3)	V _{TH} 2	WIAX03Z	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	2.25		2.38		
		MAXCO	T _A = +25°C	2.16	2.19	2.22		
		MAX63Y	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	2.13		2.25		
			T _A = +25°C	1.64	1.67	1.70		
		MAX63W	$T_{A} = -40^{\circ}C \text{ to } +85^{\circ}C$	1.62		1.71		
		MAXCO	T _A = +25°C	1.55	1.58	1.61		
		MAX63V	T _A = -40°C to +85°C	1.53		1.62		

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC}1 = V_{CC}2 = +1.2V \text{ to } +5.5V, T_A = -40^{\circ}C \text{ to } +85^{\circ}C, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL			MIN	ТҮР	MAX	UNITS	
Reset Threshold Tempco	∆V _{TH} /°C				20		ppm/°C	
Reset Threshold Hysteresis				V	′тн/500		V	
V _{CC} to Reset Delay		100mV overdrive			20		μs	
Reset Timeout Period	t _{RP}	$V_{CC1} > V_{TH1}$ (MAX), V_{CC}	C2 > VTH2 (MAX)	100	180	280	ms	
		V _{CC} 1 or V _{CC} 2 \ge 2.7V, I	SINK = 1.2mA			0.3		
		V _{CC} 1 or V _{CC} 2 \ge 4.5V, I	SINK = 3.2mA			0.4		
RESET Output Voltage Low	V _{OL}	$V_{CC}1$ or $V_{CC}2 \ge 1V$, I_{SI} $T_A = 0^{\circ}C$ to $+70^{\circ}C$	ΝΚ = 50μΑ,			0.3	V	
		$V_{CC}1 \text{ or } V_{CC}2 \ge 1.2V,$ $T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	I _{SINK} = 50µA;			0.3		
RESET Output Voltage High		$(_W, _V versions only),$ V _{CC1} > V _{TH1(MAX)} , V _{CC2} > V _{TH2(MAX)}	ISOURCE = 350µA	0.8 × V _{CC}				
(MAX6351/MAX6353/MAX6354/ MAX6356/MAX6357/MAX6359/ MAX6360 only)	V _{OH}	$(V_{CC1} > V_{TH1(MAX)}, V_{CC2} > V_{TH2(MAX)}, I_{SOURCE} = 500 \mu A$		$0.8 \times V_{CC}$			V	
		$(_T, _S, _R versions only),$ V _{CC1} > V _{TH1(MAX)} , V _{CC2} > V _{TH2(MAX)}	ISOURCE = 800µA	V _{CC} - 1.5				
WATCHDOG INPUT (MAX6358/N	1AX6359/MA	X6360)						
Watchdog Timesout David		First timeout period after reset		25.6	46.4	72.0	-	
Watchdog Timeout Period	twd	After first WDI falling edge		1.6	2.9	4.5	S	
WDI Pulse Width (Note 4)	twdi	$V_{IL} = 0.4V, V_{IH} = 0.8 x$	Vcc	50			ns	
	VIL				(0.3 x V _{CC}	V	
WDI Input Voltage (Note 5)	VIH	$V_{CC}1 = 5V$		$0.7 \times V_{CC}$			v	
WDI Input Current (Note 6)		$V_{WDI} = V_{CC}$			120	160		
WDI Input Current (Note 8)		V _{WDI} = 0		-20	-15		μΑ	
MANUAL RESET INPUT								
	VIL	MAX63L_, MAX63	$_M_, V_{CC}1 > V_{TH}1_{(MAX)}$			0.8		
MR Input Voltage	VIH	MAX63L_, MAX63	$M_, V_{CC}1 > V_{TH}1(MAX)$	2.3			V	
	VIL	MAX63Y, V _{CC} 1 > V	VTH ¹ (MAX)			0.3 x V _{CC}	v	
	VIH	MAX63Y, V _{CC} 1 > V	VTH1(MAX)	$0.7 \times V_{CC}$	c			

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC}1 = V_{CC}2 = +1.2V \text{ to } +5.5V, T_A = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}\text{C}.)$ (Note 1)

PARAMETER	SYMBOL	COND	ITIONS	MIN	ТҮР	МАХ	UNITS
MR Pullup Resistance				32	63.5	100	kΩ
MR Minimum Pulse Width	t _{RP}			1			μs
MR Glitch Rejection					100		ns
MR to Reset Delay	t _{MD}				0.1		μs
ADJUSTABLE RESET COMPARA	TOR INPUT	Г (MAX6355/MAX6356/I	MAX6357)				
RSTIN Input Threshold	Veeru	$V_{CC1} > V_{TH1(MAX)},$ $V_{CC2} > V_{TH2(MAX)}$	$T_A = +25^{\circ}C$	1.20	1.22	1.24	v
	V _{RSTIN}		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	1.19		1.25	v
RSTIN Input Current	IRSTIN	$0 < V_{RSTIN} < V_{CC}1 - 0.3V$		-25		25	nA
RSTIN Hysteresis					2.5		mV

Note 1: Overtemperature limits are guaranteed by design and not production tested.

Note 2: The reset output is guaranteed to be in the correct state if either V_{CC}1 or V_{CC}2 is within its specified region of operation.

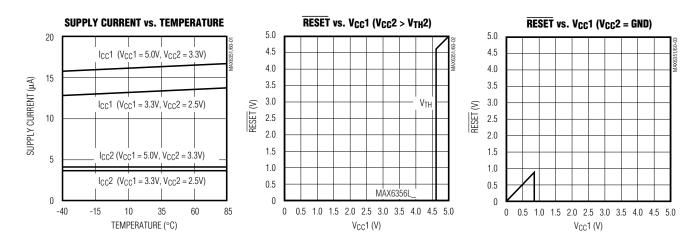
Note 3: The reset output(s) is asserted if either V_{CC}1 or V_{CC}2 drops below its associated trip point.

Note 4: Guaranteed by design. Not production tested.

Note 5: WDI is internally serviced within the watchdog timeout period if WDI is left unconnected.

Note 6: The WDI input current is specified as the average input current when the WDI input is driven high or low.

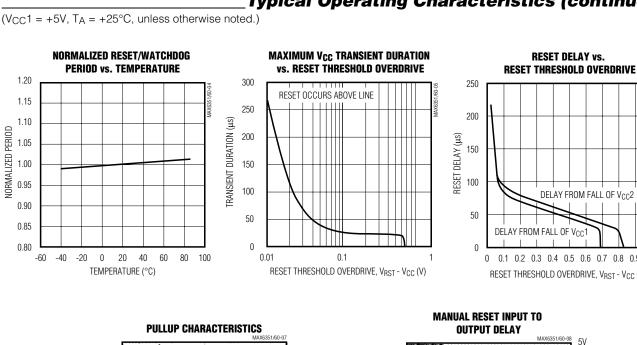
$(V \cap 1 =$	+5V	TΔ =	+25°C.	unless	otherwise	noted.)
(,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,	· A	0 0,		01110111100	



Typical Operating Characteristics

///XI//

Typical Operating Characteristics (continued)



5V

0

5V

0

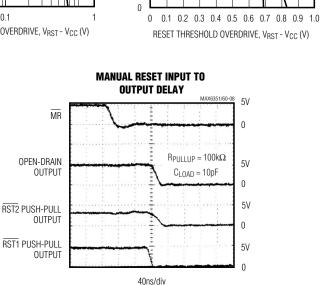
3V

Λ

 $R_{PULLUP} = 100k\Omega$

 $C_{LOAD} = 10 pF$

400ns/div



RST1 PUSH-PULL

OUTPUT

OPEN-DRAIN OUTPUT

RST2 PUSH-PULL

OUTPUT

MAX6351-MAX6360

		PIN						
FUNCTION	NAME	MAX6358 MAX6359 MAX6360	MAX6355 MAX6356 MAX6357	MAX6352 MAX6353 MAX6354	MAX6351			
Active-Low, CMOS Reset Output Referenced to V _{CC} 1	RST1	_	_	_	1			
Active-Low Reset Output. Open drain for the MAX6352 MAX6355/MAX6358, push-pull with respect to V _{CC} 1 for the MAX6353/MAX6356/MAX6359, and push-pull with respect to V _{CC} 2 for the MAX6354/MAX6357/MAX6360.	RST	1	1	1	_			
Ground	GND	2	2	2	2			
$\begin{array}{c} \mbox{Manual-Reset Input. Pull low to force a reset. \overline{RST}, \\ \hline RST1, and \overline{RST2} remain active as long as \overline{MR} is low \\ \mbox{and for the timeout period after } \overline{MR} \mbox{goes high.} \\ \hline Leave unconnected or connect to V_{CC}1 \mbox{ if unused.} \\ \hline V_{\overline{MR}} \mbox{must not exceed } V_{CC}1. \end{array}$	MR	3	3	3	3			
Supply Input. Powers the device when it is above V_{CC} 1 and monitors its own voltage.	V _{CC} 2	4	4	4	4			
Active-Low, CMOS Reset Output Referenced to V _{CC} 2	RST2				5			
Undervoltage Reset Comparator Input. Asserts reset when the monitored voltage falls below 1.22V. Set the reset threshold with an external resistive divider. Connect to V _{CC} 1 if unused. V _{RSTIN} must no exceed V _{CC} 1.	RSTIN	_	5	_	_			
Watchdog Input. If WDI remains either high or low longer than the timeout period, then reset is trig- gered. The timer clears when reset is asserted or whenever WDI sees a rising or falling edge. Leave floating to disable it if unused.	WDI	5		_	_			
Supply Input. Powers the device when it is above V _{CC} 2 and monitors its own voltage.	V _{CC} 1	6	6	5	6			

Pin Description

Detailed Description

Supply Voltages

The MAX6351–MAX6360 microprocessor (μ P) supervisory circuits maintain system integrity by alerting the μ P to fault conditions. These ICs monitor multiple-supply systems. The output reset state is guaranteed to remain viable while either V_{CC1} or V_{CC2} is above +1V.

Threshold Levels

All the possible input voltage threshold level combinations are indicated by a two-letter code in the *Voltage Threshold Levels* table.

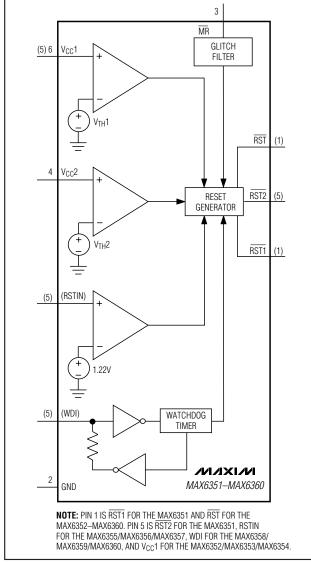


Figure 1. Functional Diagram



Reset Outputs

The MAX6351 provides two active-low, push-pull reset outputs, one corresponding to each of its two monitored voltages. The MAX6353/MAX6356/MAX6359 have an active-low, push-pull reset output that is referenced to V_{CC}1. The MAX6354/MAX6357/MAX6360 have an active-low, push-pull reset output that is referenced to V_{CC}2. The MAX6352/MAX6355/MAX6358 provide an active-low open-drain reset. The reset outputs are maintained as long as at least one of the supplies is above +1V.

Negative-Going Vcc Transients

The MAX6351–MAX6360 are designed to ignore short negative-going V_{CC}1 and V_{CC}2 transients. See the Maximum V_{CC} Transient Duration vs. Reset Threshold Overdrive graph in the *Typical Operating Characteristics*.

Third Input Voltage (MAX6355/MAX6356/MAX6357)

The MAX6355/MAX6356/MAX6357 provide an additional input to monitor a third voltage. The threshold voltage at RSTIN is typically 1.22V. To monitor a voltage higher than 1.22V, connect a resistive divider to the circuit as shown in Figure 2. The threshold at V_{EXT} is:

$$V_{EXT_TH} = 1.22V \left(\frac{R1 + R2}{R2}\right)$$

Note that RSTIN is powered by V_{CC}1, and its voltage must therefore remain lower than or equal to V_{CC}1.

Watchdog Input (MAX6358/MAX6359/MAX6360)

The MAX6358/MAX6359/MAX6360 include a dualmode watchdog timer to monitor μ P activity. The flexible timeout architecture provides a startup mode, allowing complicated systems to complete lengthy boots, and a normal mode, allowing the supervisor to provide quick alerts when processor activity fails.

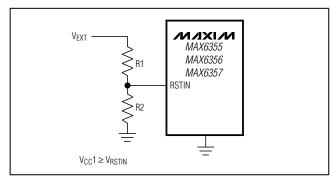


Figure 2. Monitoring a Third Voltage

During the normal operating mode, the supervisor will issue a reset pulse for the reset timeout period (140ms min) if the μ P does not update the WDI with a valid transition (HIGH to LOW or LOW to HIGH) within the standard timeout period (1.6s min).

After each reset event (V_{CC} power-up, manual reset, or watchdog reset), there is an initial watchdog startup timeout period of 25.6s. The startup mode provides an extended period for the system to power up and fully initialize all μ P and system components before assuming responsibility for routine watchdog updates. The normal watchdog timeout period (1.6s min) begins at the conclusion of the startup timeout period or after the first transition on WDI before the conclusion of the startup period (Figure 3).

Applications Information

Ensuring a Valid RESET Output Down to VCC = 0

In some systems, it is necessary to ensure a valid reset even if V_{CC} falls to 0. In these applications, use the circuit shown in Figure 4. Note that this configuration does not work for the open-drain outputs of the MAX6352/MAX6358.

Interfacing to µPs with Bidirectional Reset Pins

Microprocessors with bidirectional reset pins will contend with the push-pull outputs of these devices. To prevent this, connect a 4.7k Ω resistor between RESET and the μP 's reset I/O port, as shown in Figure 5. Buffer RESET as shown in the figure if this reset is used by other components in the system.

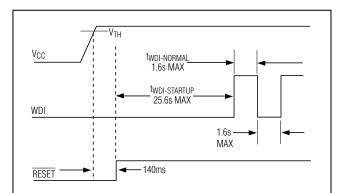


Figure 3. Normal Watchdog Startup Sequence

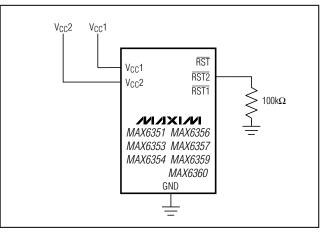


Figure 4. Ensuring a Valid Reset Low to V_{CC} 1 and V_{CC} 2 = 0

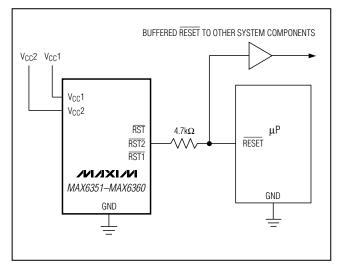
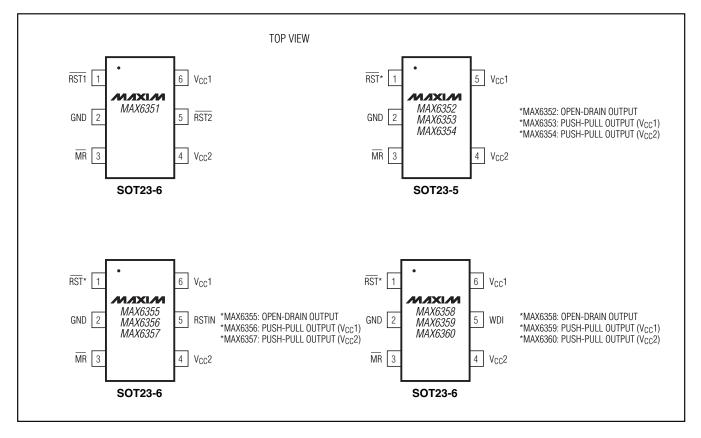


Figure 5. Interfacing to µPs with Bidirectional Reset I/O

____Chip Information

TRANSISTOR COUNT: 855

Pin Configurations

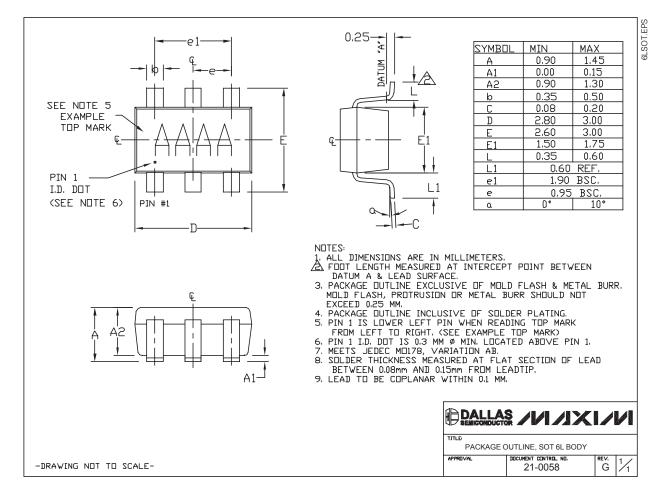


Selector Guide

PART	PIN COUNT	NUMBER OF SUPPLIES MONITORED	RST1	RST2	OPEN-DRAIN RESET	WATCHDOG TIMER	MANUAL RESET
MAX6351	6	2	~	~			~
MAX6352	5	2		_	~		~
MAX6353	5	2	~	_		_	~
MAX6354	5	2		~			~
MAX6355	6	3	—	_	~	—	~
MAX6356	6	3	~		—		~
MAX6357	6	3		~			~
MAX6358	6	2		_	~	~	~
MAX6359	6	2	~	_	_	~	~
MAX6360	6	2		~	—	~	 ✓

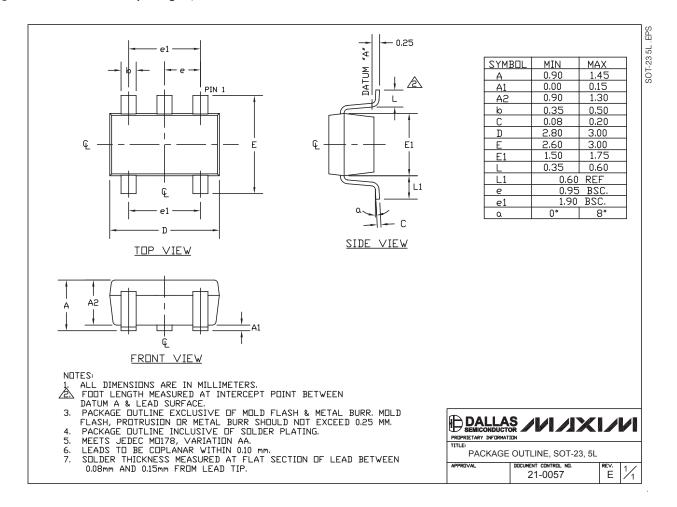
Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



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